

09525105.03400

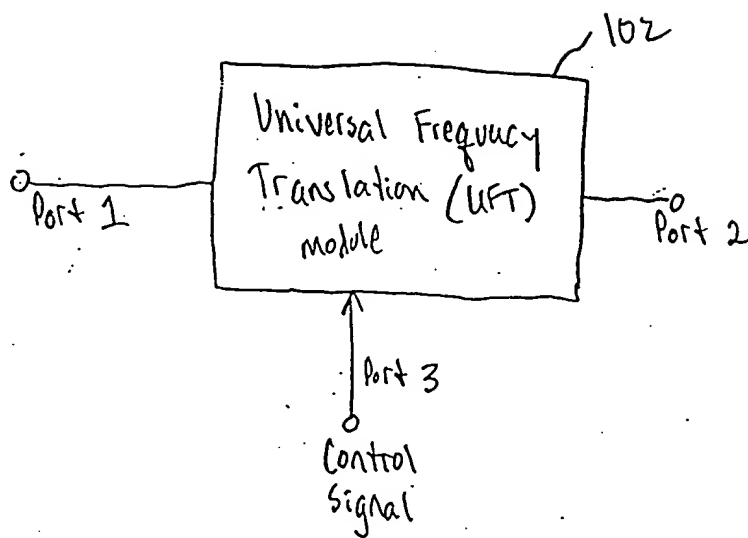


FIG. 1A

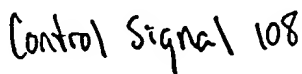


FIG. 1B

0955185-031400

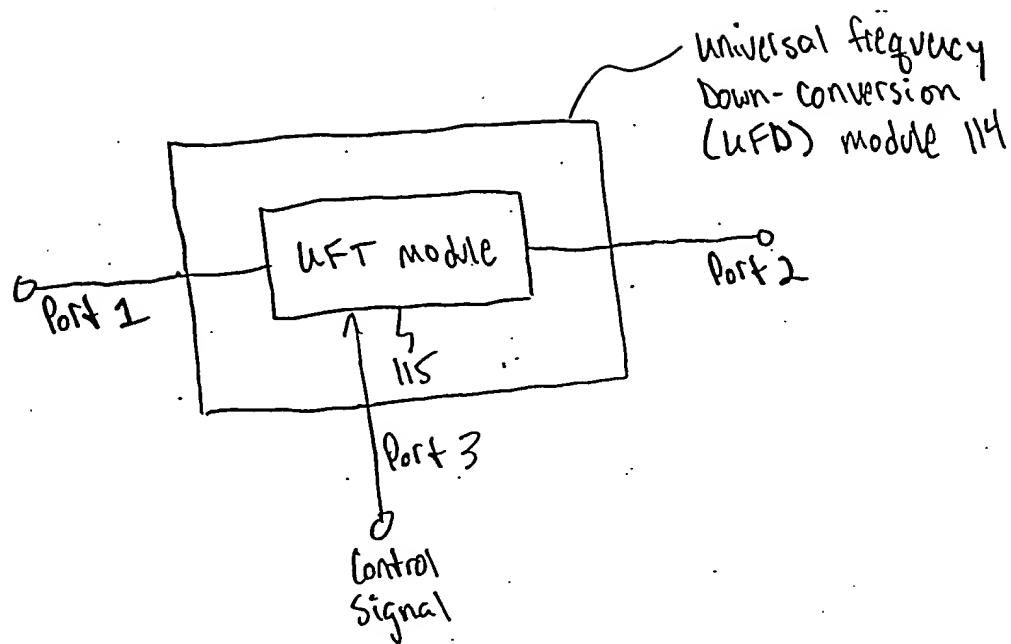


FIG. 1C

09525165 034430

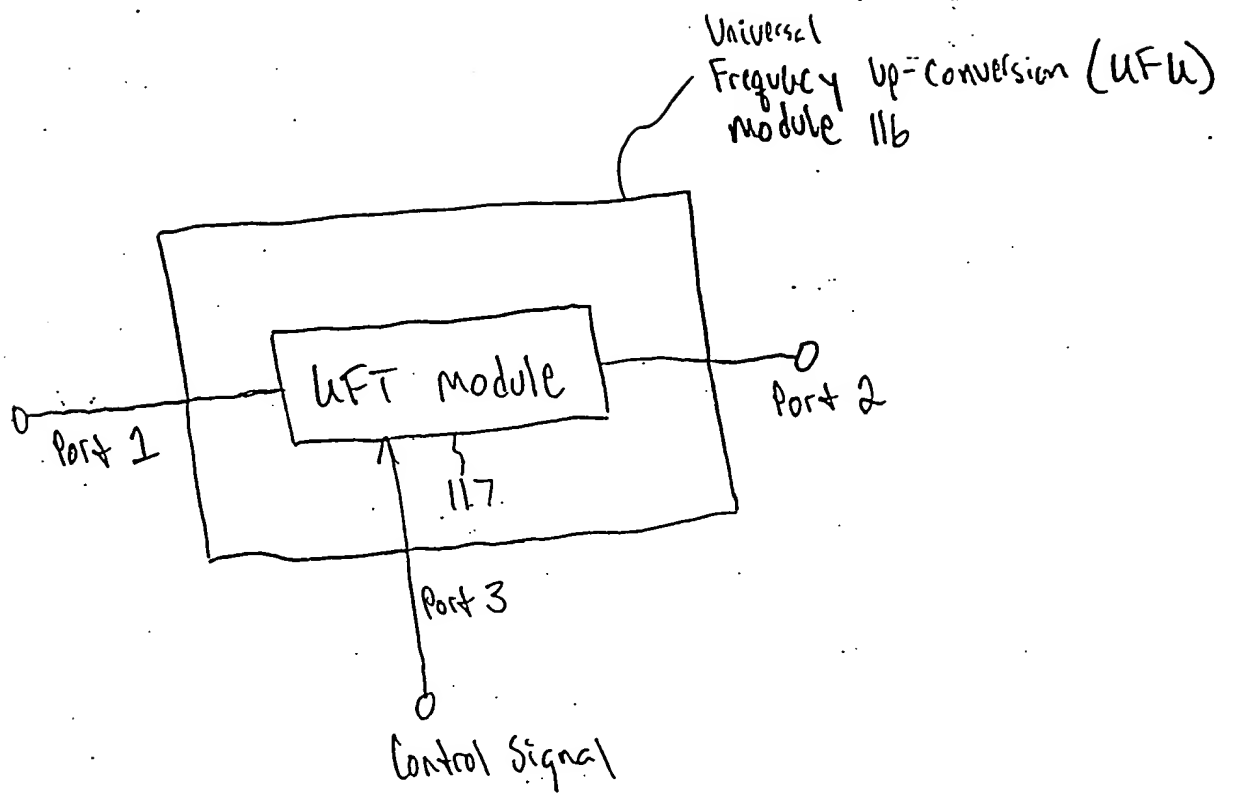


FIG. 1D

09525105 031400

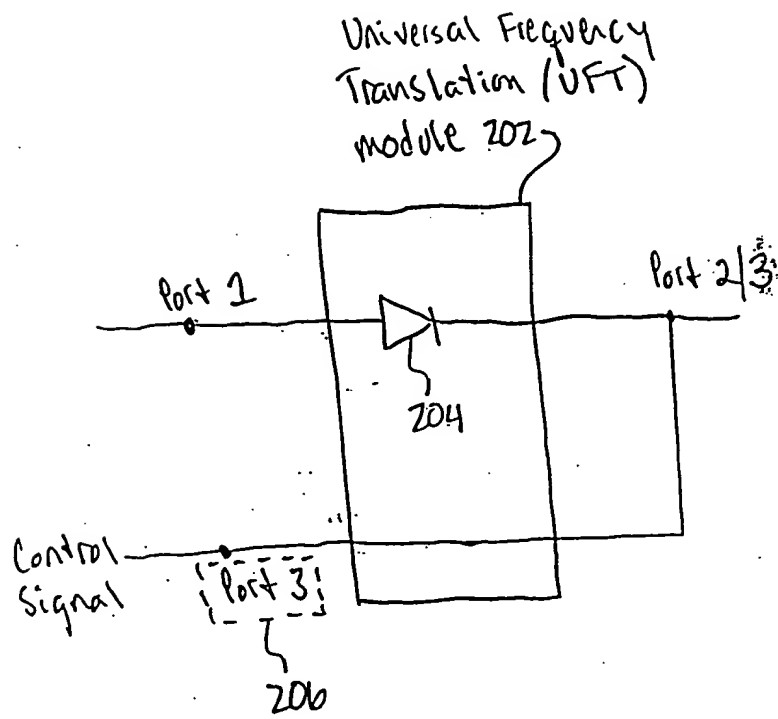


FIG. 2A

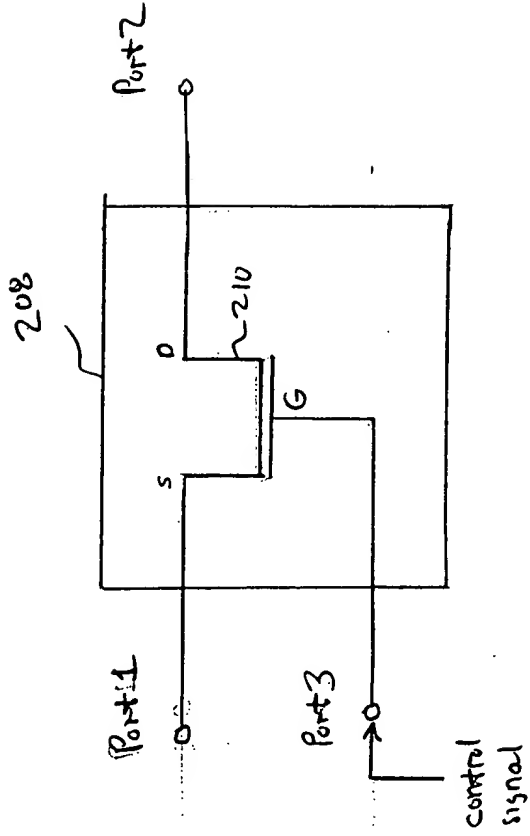


FIG. 2B

Universal Frequency
Up-Conversion (UFU) module 300

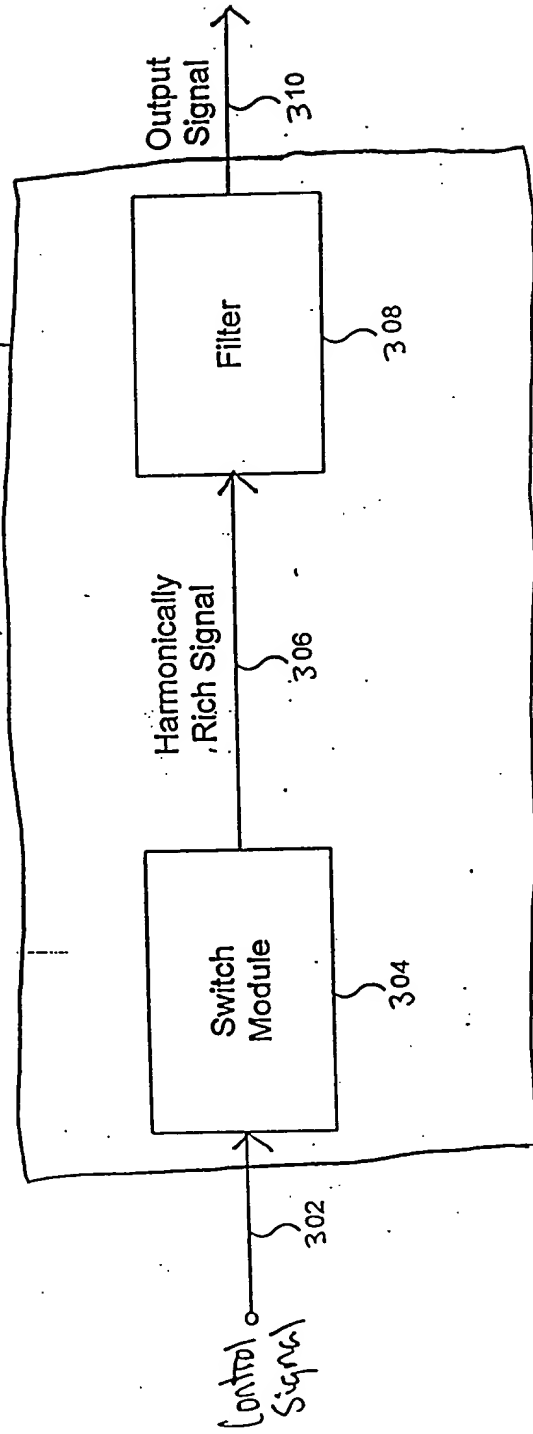


FIG. 3

3

DATE: 03/05/2000

Universal Frequency
Up-conversion (UEU) module 401

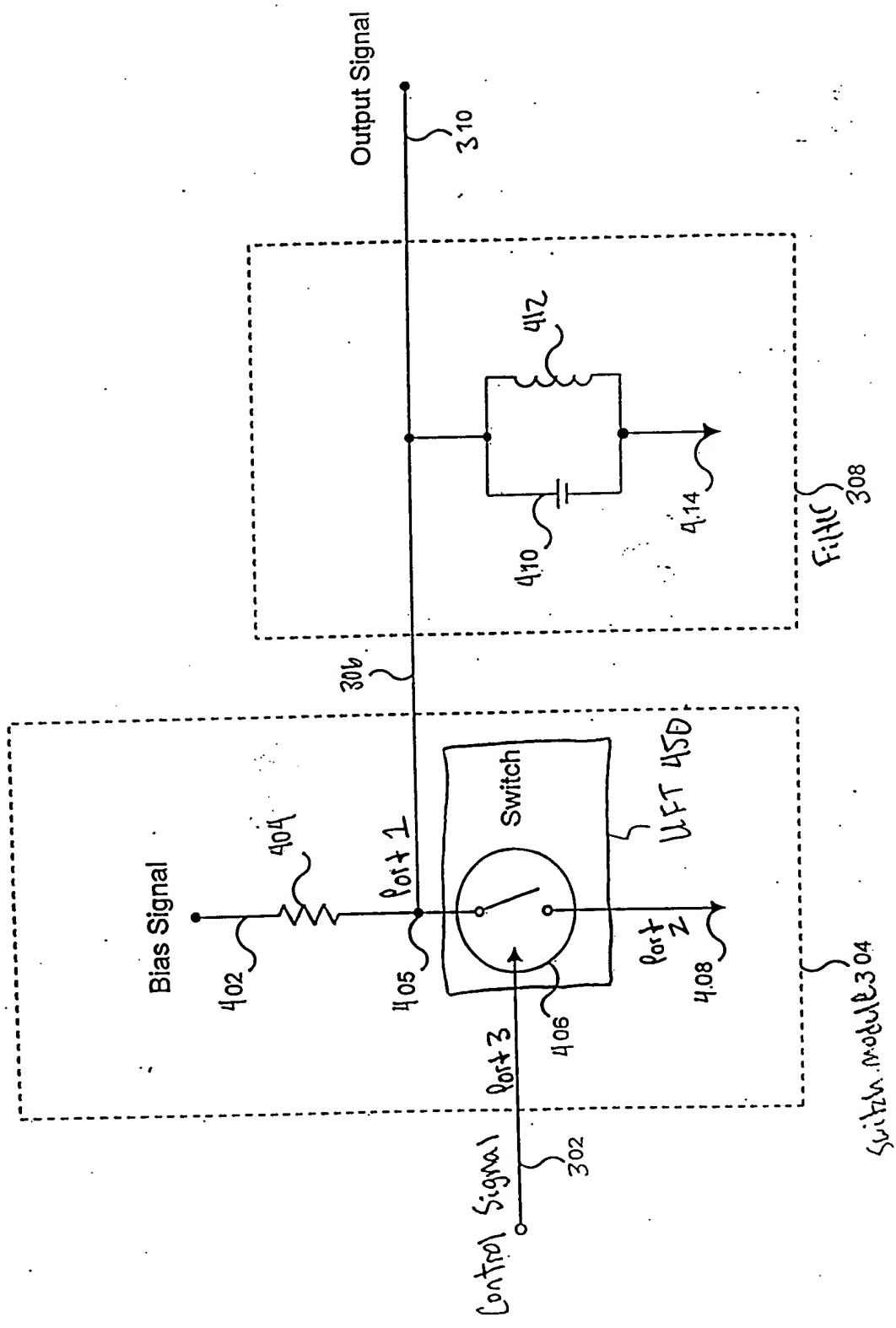


FIG. 4

Universal Frequency
up-conversion
(UFU) module 590

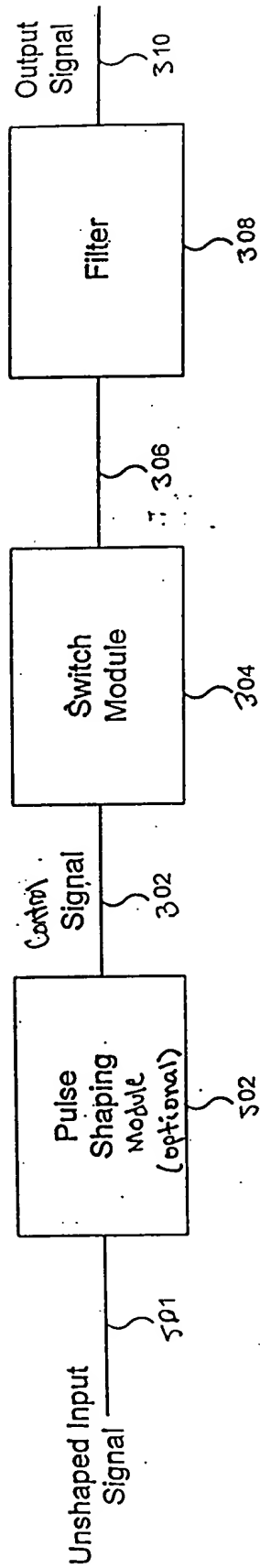


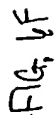
FIG. 5

55



FILED
JUN 19 1964

HARMONICS OF
SIGNAL 610
(SHOWN SEPARATELY)

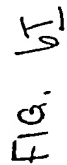


HARMONICS OF SIGNAL W/2 SHOWING SEPARATELY)



FIG. 6 (cont)

917



612C
610C

FIG. 6 (cont)

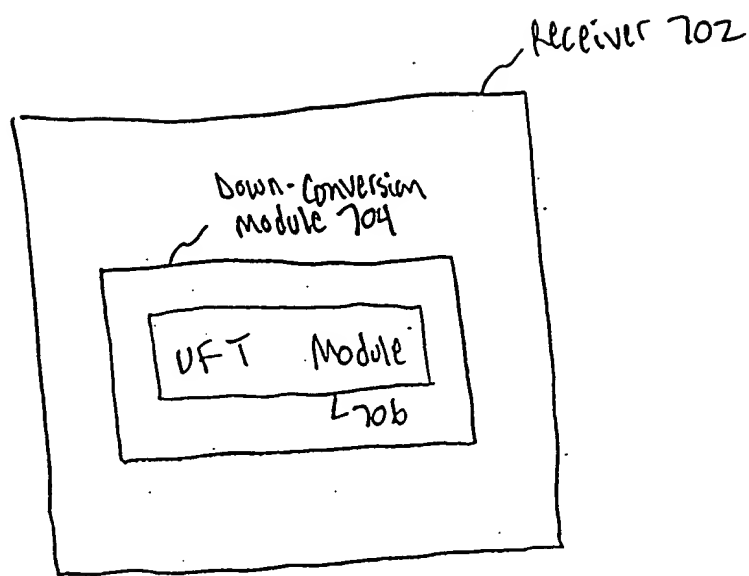


FIG. 7

004700 003400 003400 003400

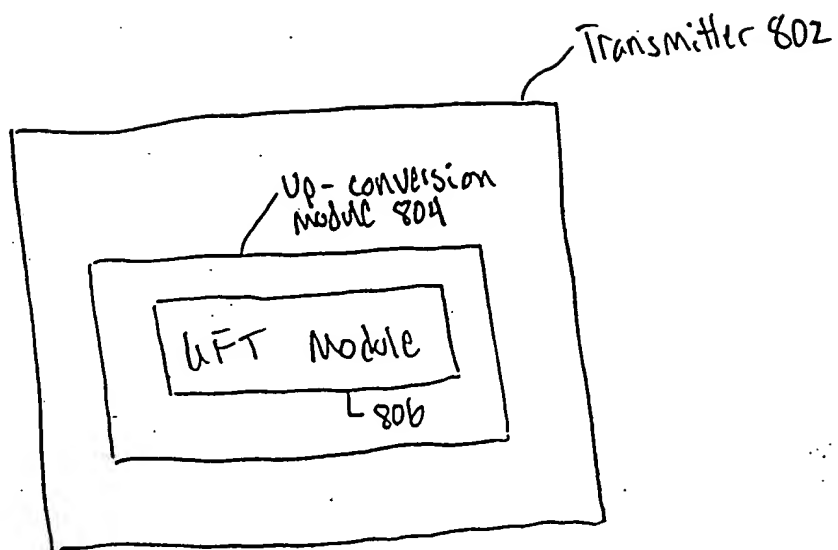


FIG. 8

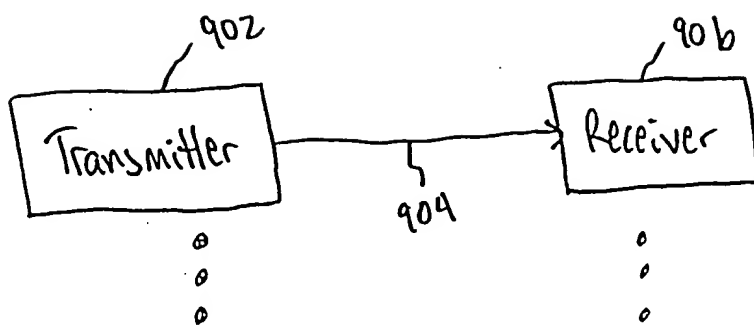


FIG. 9

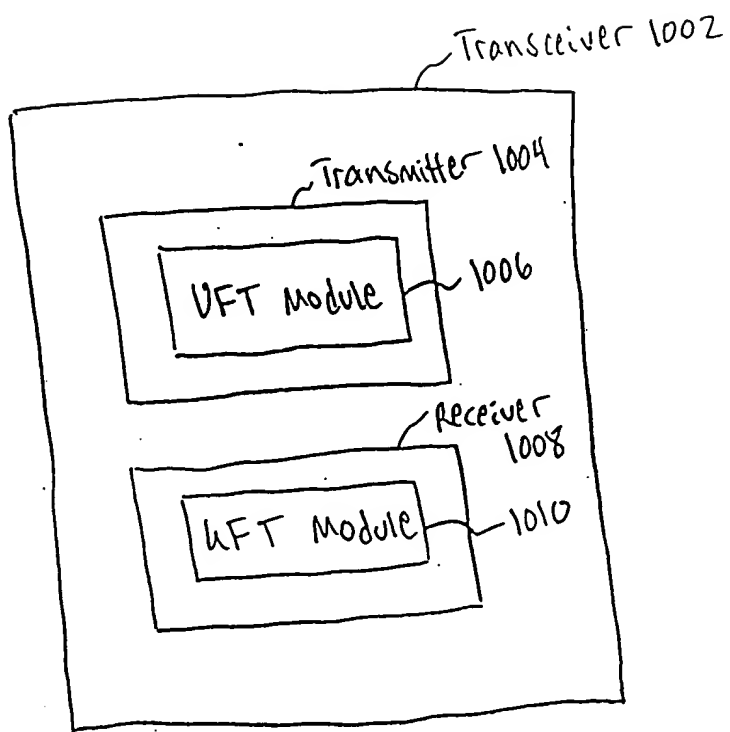


FIG. 10

09525105 034400

09525195.034420

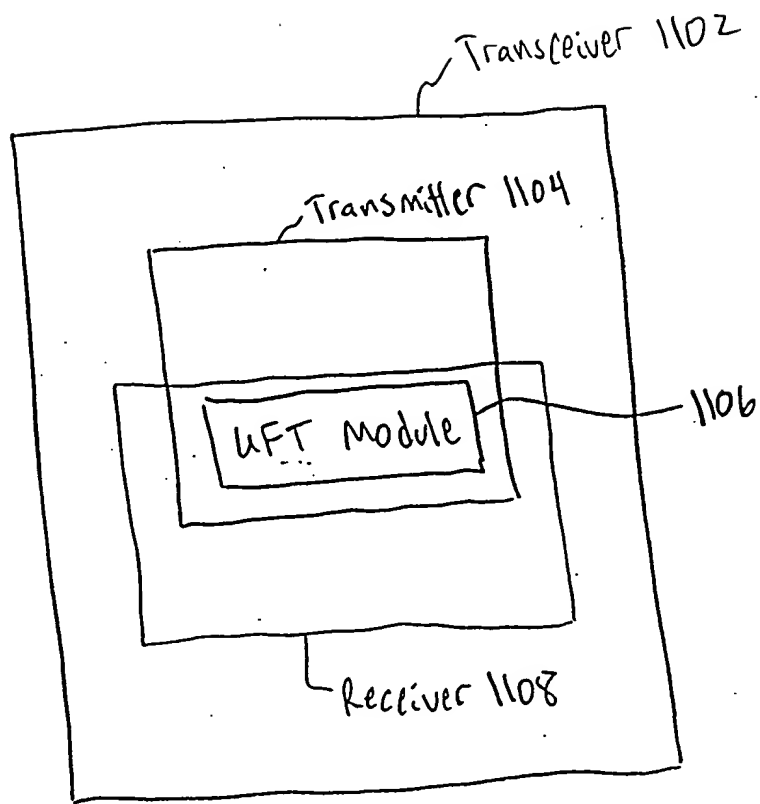
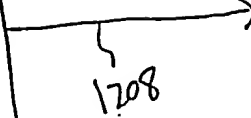
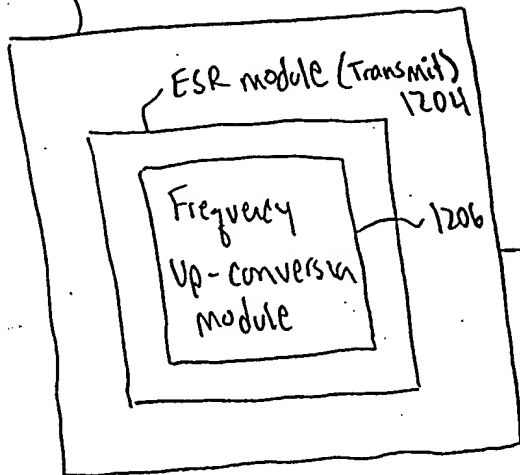


FIG. 11

Transmitter 1202



Receiver 1210

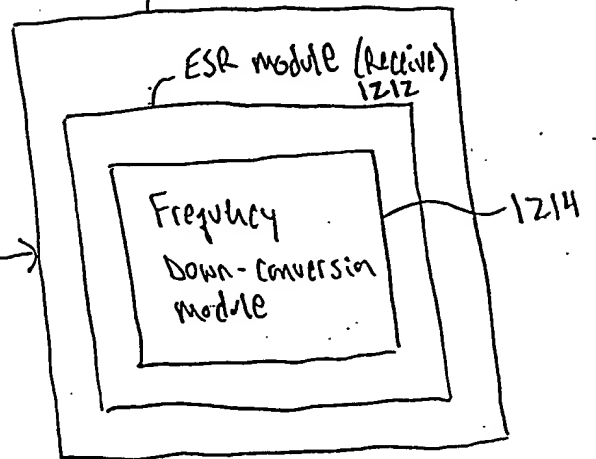


FIG. 12

Unified Down-converting
and Filtering (UDF) module 1302

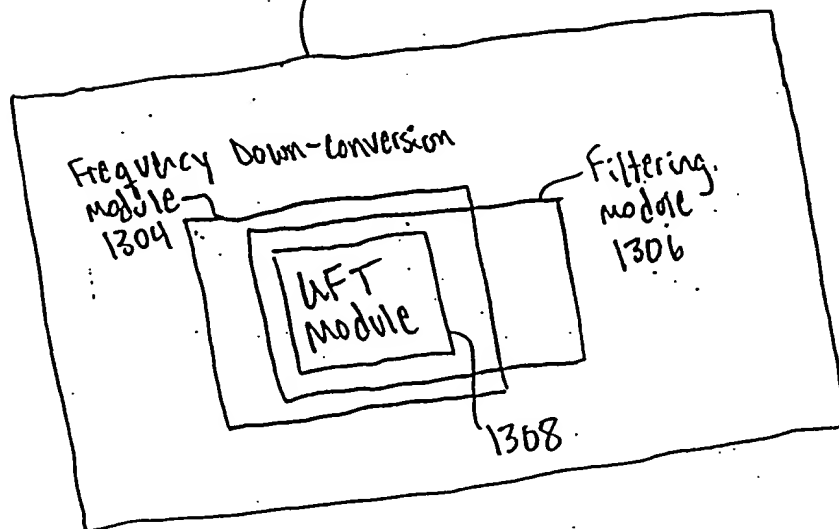


FIG. 13

09525465 034400

Unified Down-Convoluting
and Filtering
(UDF) module

Unified Down-Converting and Filtering (UDF) module

L 1404

FIG. 14

[illegible]



FIG. 15A

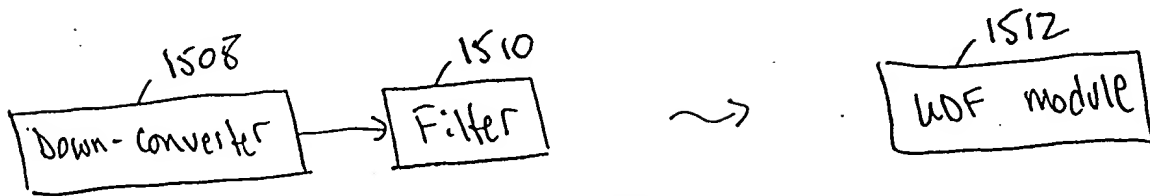


FIG. 15B

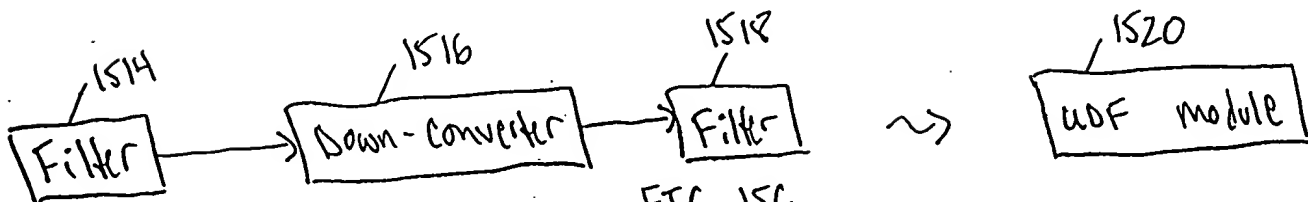


FIG. 15C



FIG. 15D

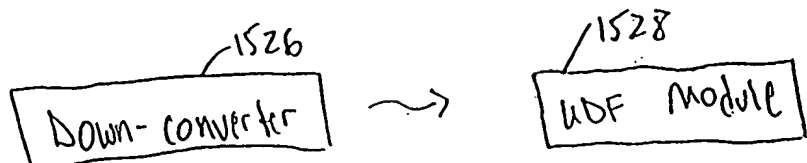


FIG. 15E

00525405-031400

1530
Amplifier



1532
WDF Module

FIG. 15F

00526185 034400

0955485-24400
007423 38752560

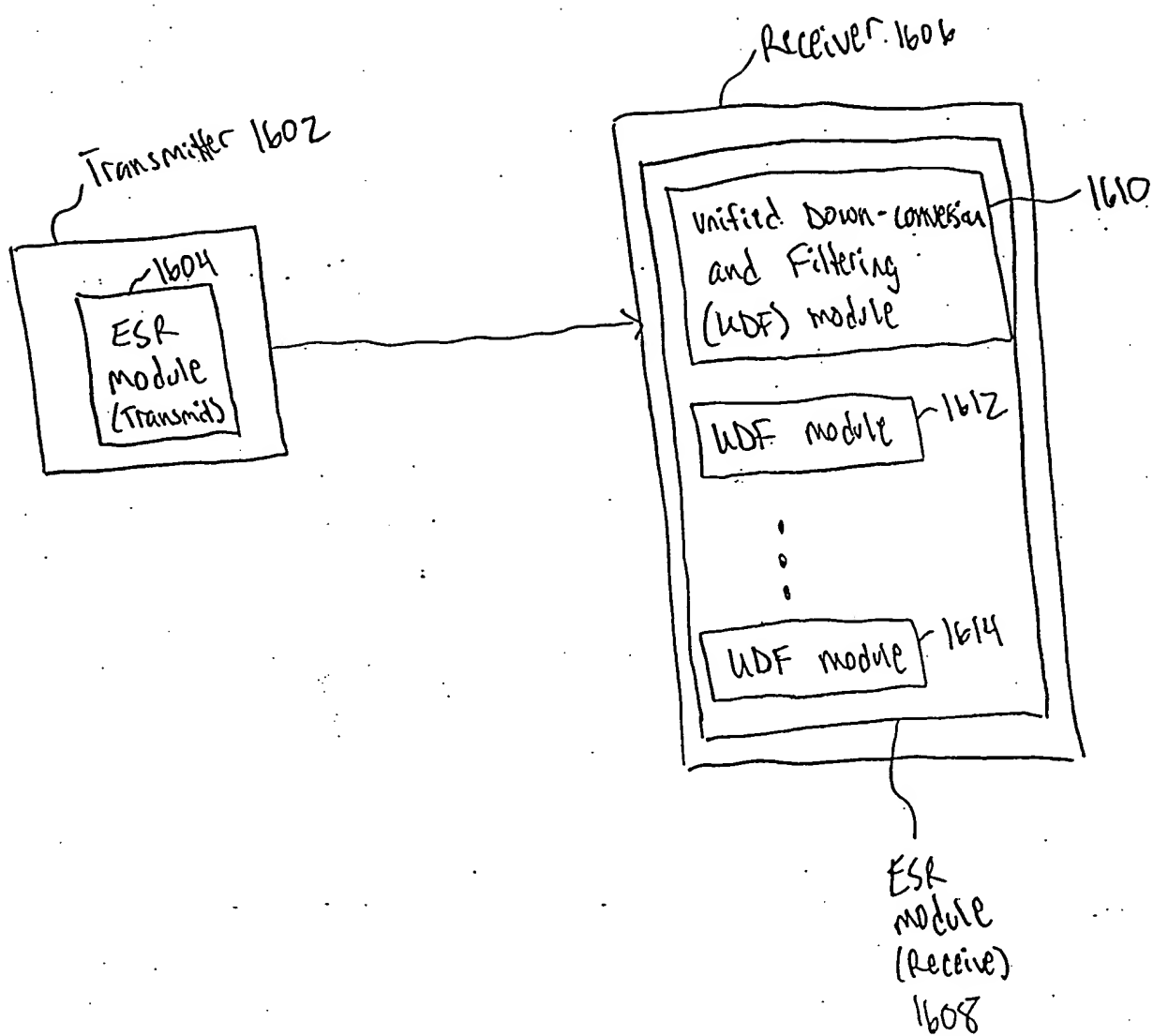


FIG. 16

9809-02 v.2.0

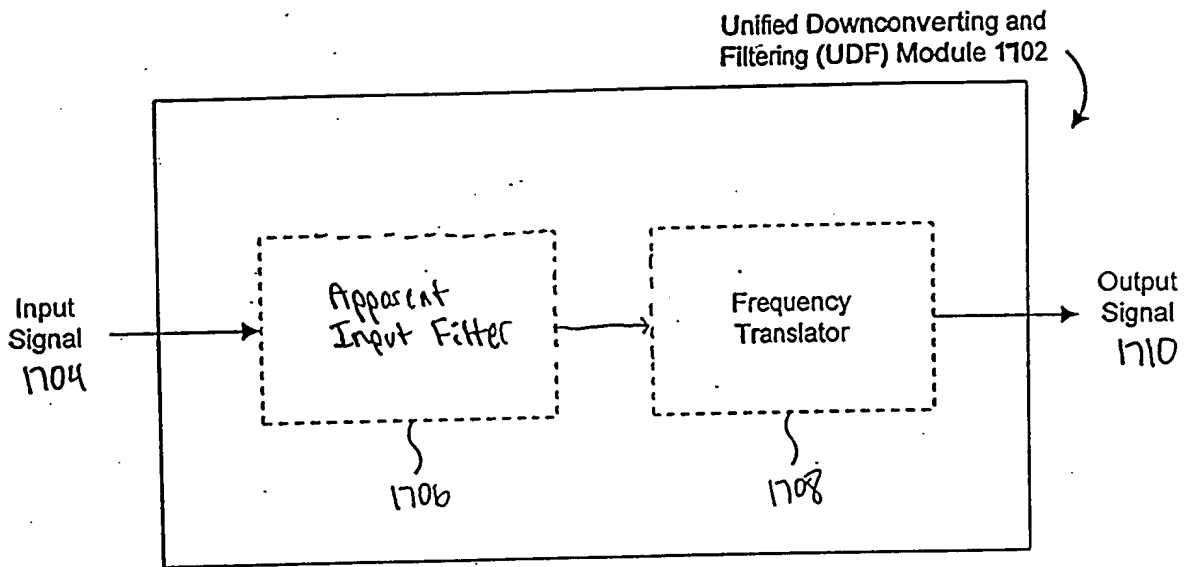


FIG. 11

1802

Time Node	t-1 (rising edge of ϕ_1)	t-1 (rising edge of ϕ_2)	t (rising edge of ϕ_1)	t (rising edge of ϕ_2)	t+1 (rising edge of ϕ_1)
1902	VI_{t-1} 1804	VI_{t-1} 1808	VI_t 1816	VI_t 1826	VI_{t+1} 1838
1904	—	VI_{t-1} 1810	VI_{t-1} 1818	VI_t 1828	VI_t 1840
1906	VO_{t-1} 1806	VO_{t-1} 1812	VO_t 1820	VO_t 1830	VO_{t+1} 1842
1908	—	VO_{t-1} 1814	VO_{t-1} 1822	VO_t 1832	VO_t 1844
1910	— 1807	—	VO_{t-1} 1824	VO_{t-1} 1834	VO_t 1846
1912	—	— 1815	—	VO_{t-1} 1836	VO_{t-1} 1848
1918	—	—	—	—	VI_t 1850 0.1 * VO_t 0.8 * VO_{t-1}

FIG. 18

USE MODULE 1922
(band pass)

NOTED: 53152560

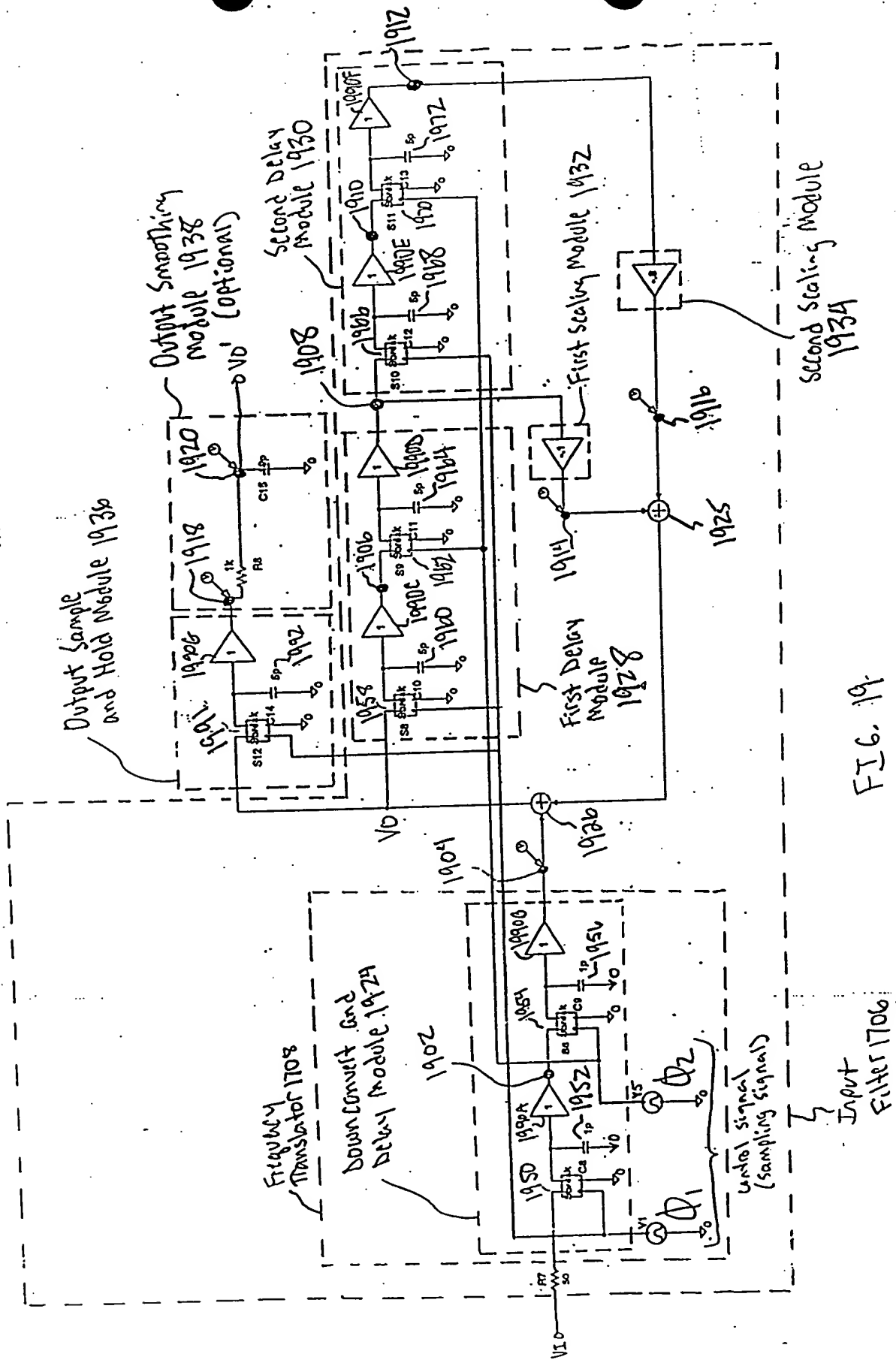


FIG. 19

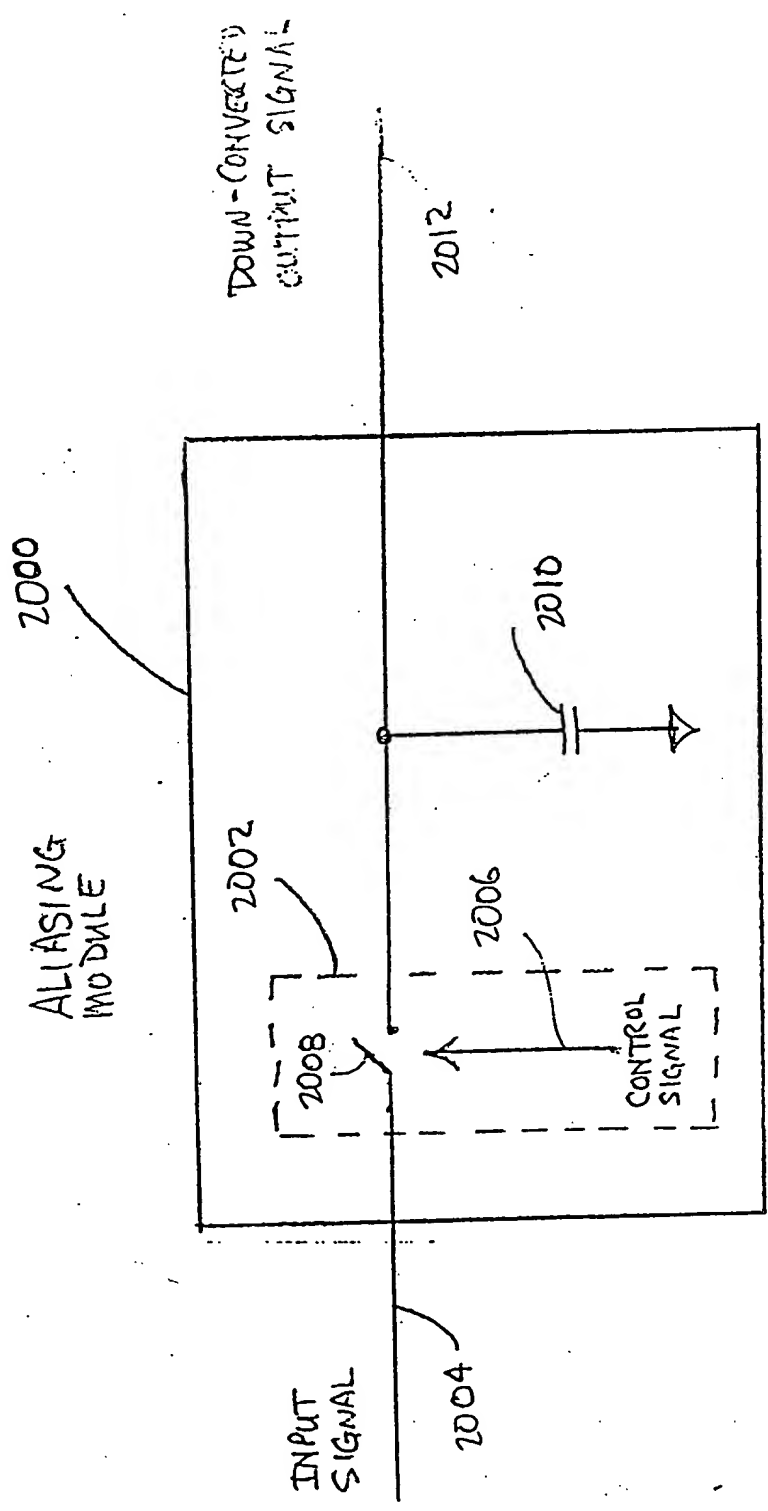


FIG. 20A



OFFICE OF THE ENGINEER

ALIASING MODULE

2000

DOWN-CONVERTED
OUTPUT SIGNAL

2012

2010

INPUT
SIGNAL

2004

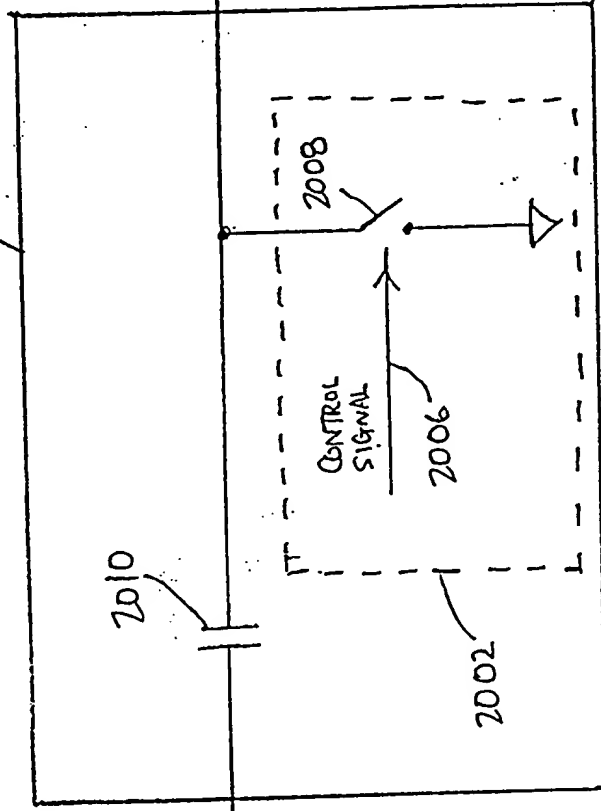
CONTROL
SIGNAL

2006

2008

2002

FIG. 20A-1



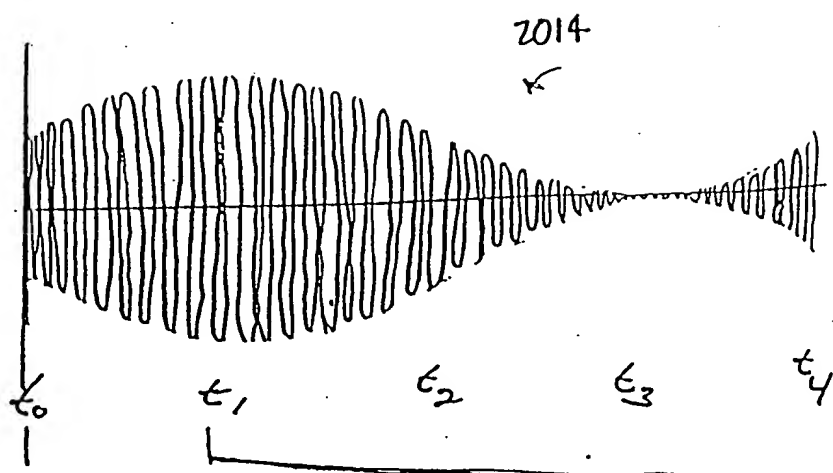


FIG. 20B

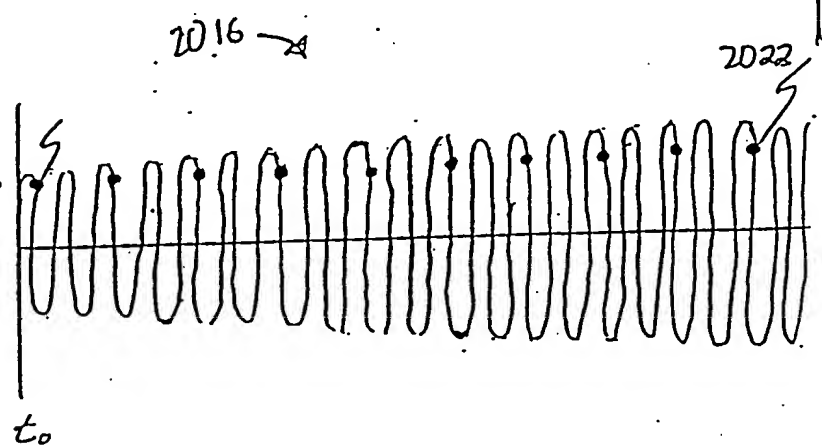


FIG. 20C

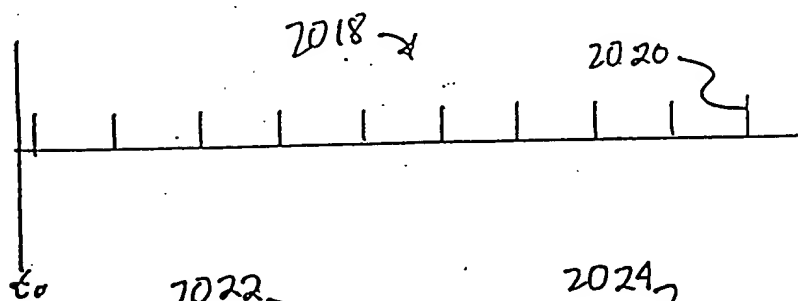


FIG. 20D

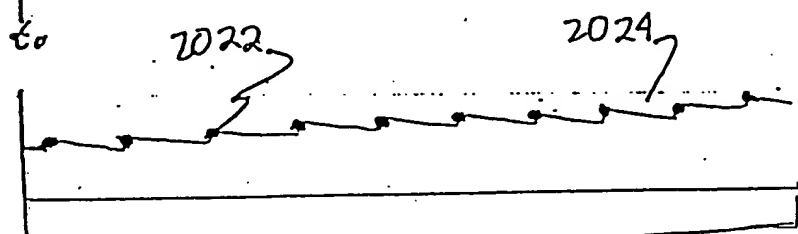


FIG. 20E

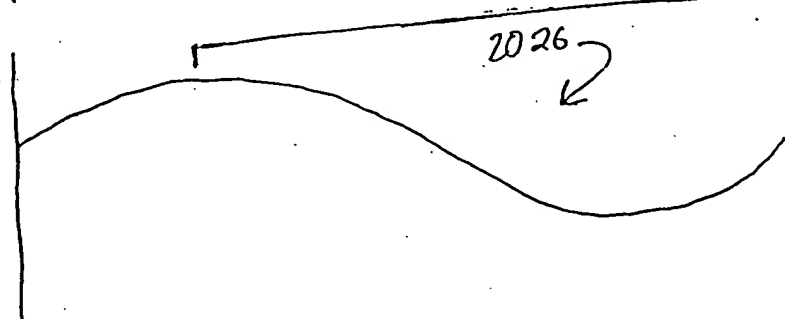


FIG. 20F

0052485-04100

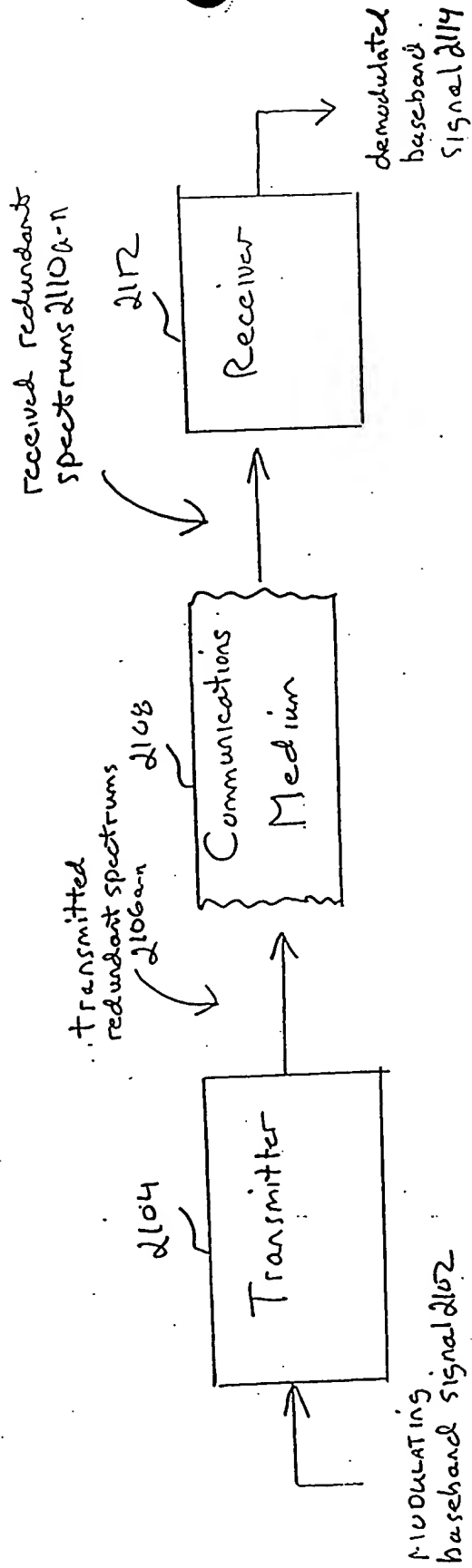
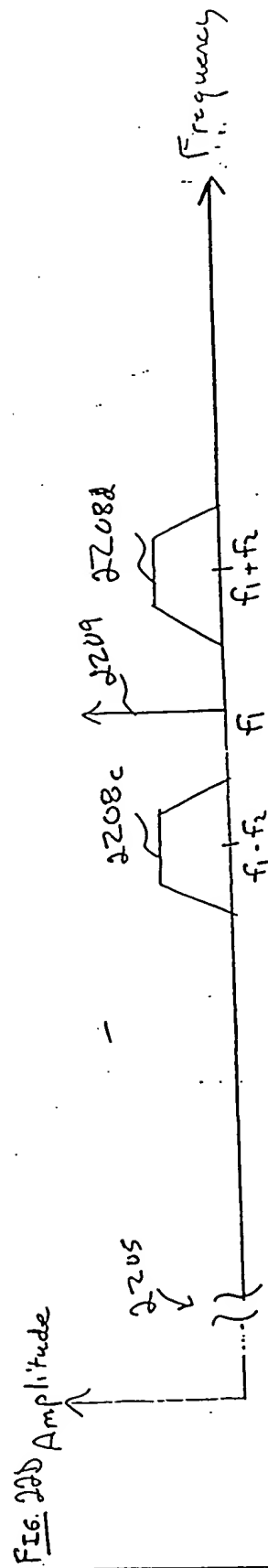
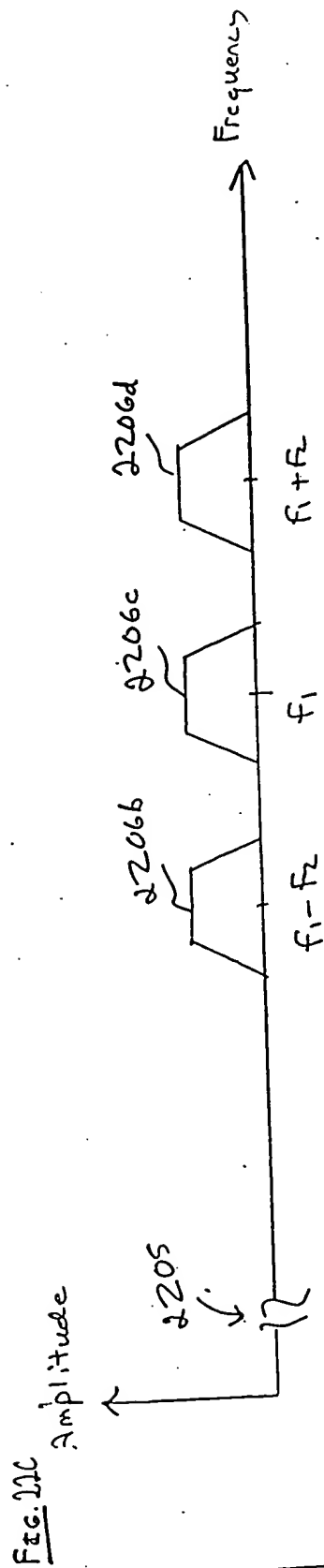
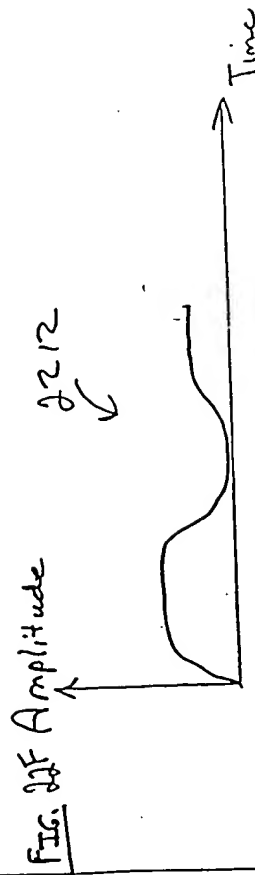
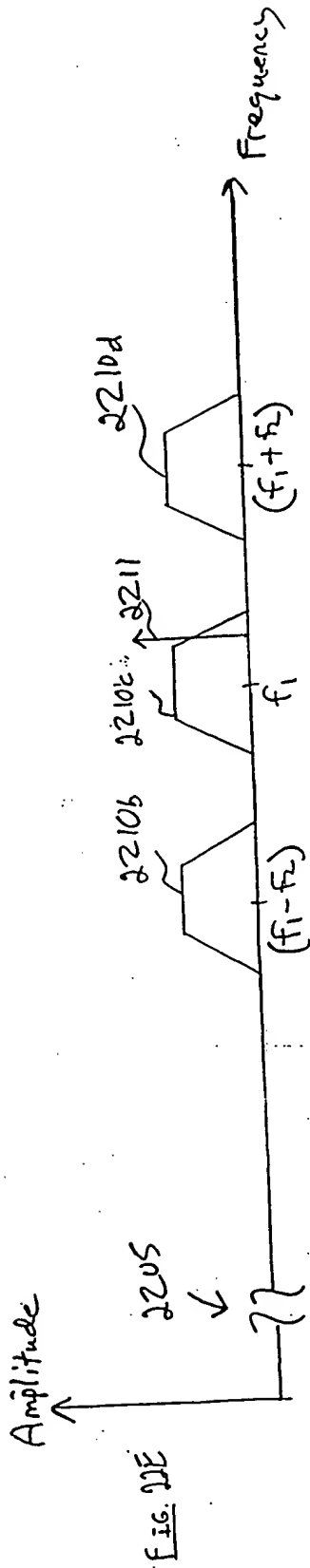


FIG. 21





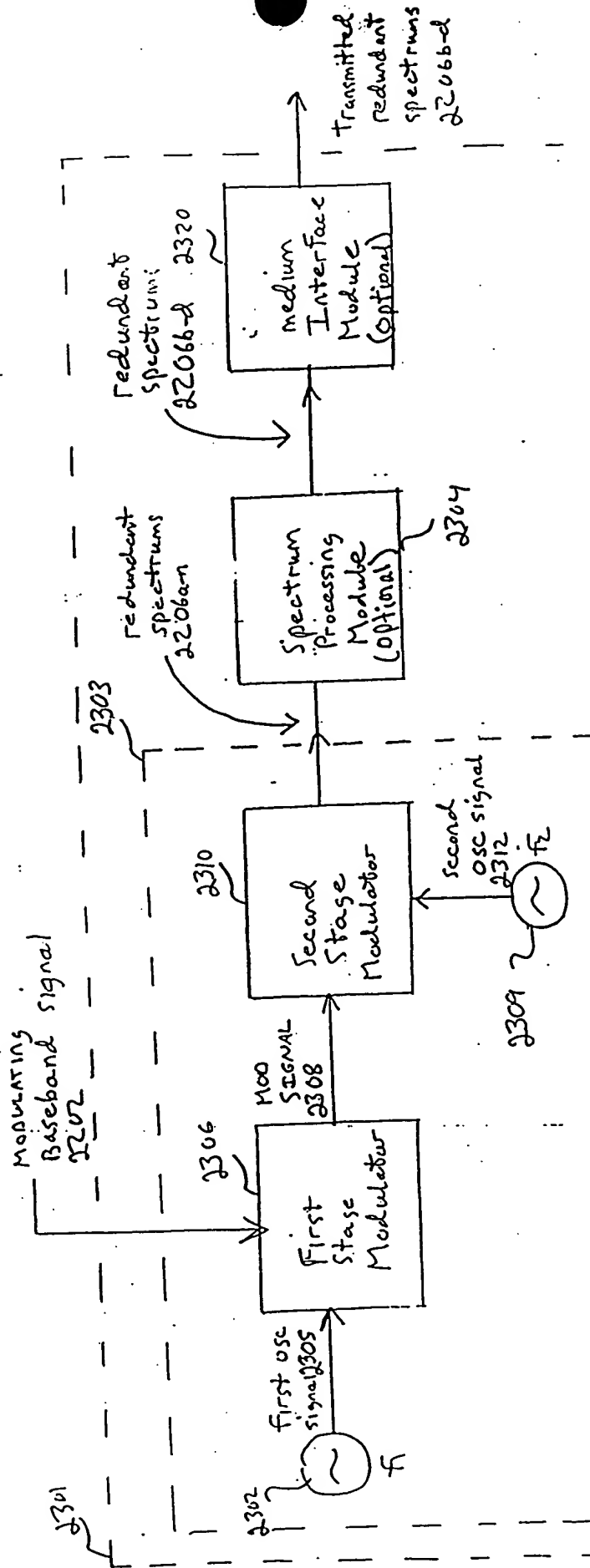
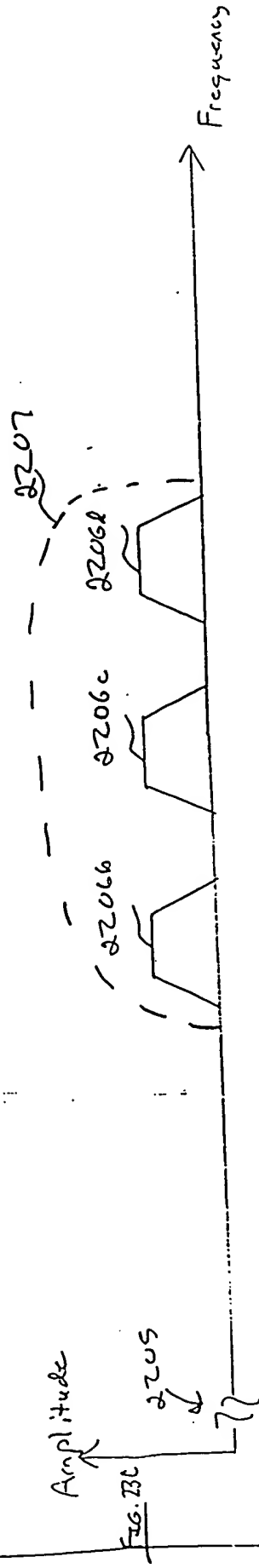
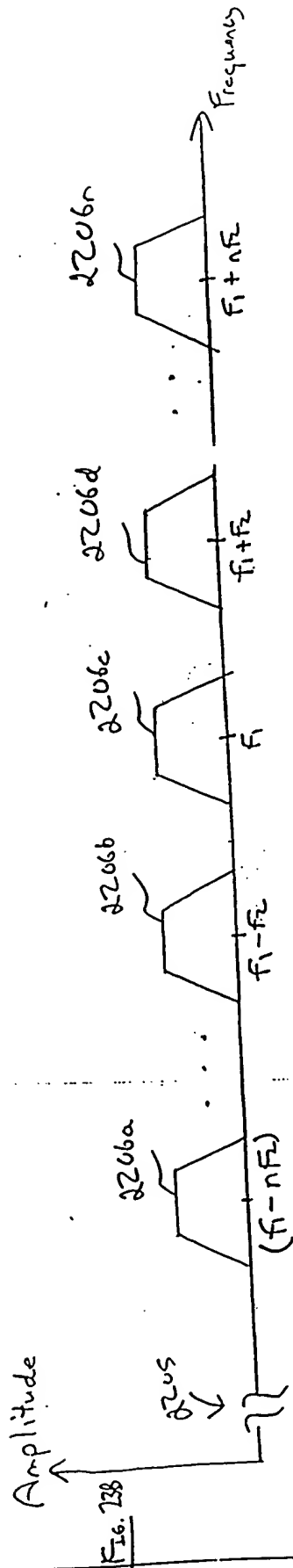


FIG. 23A



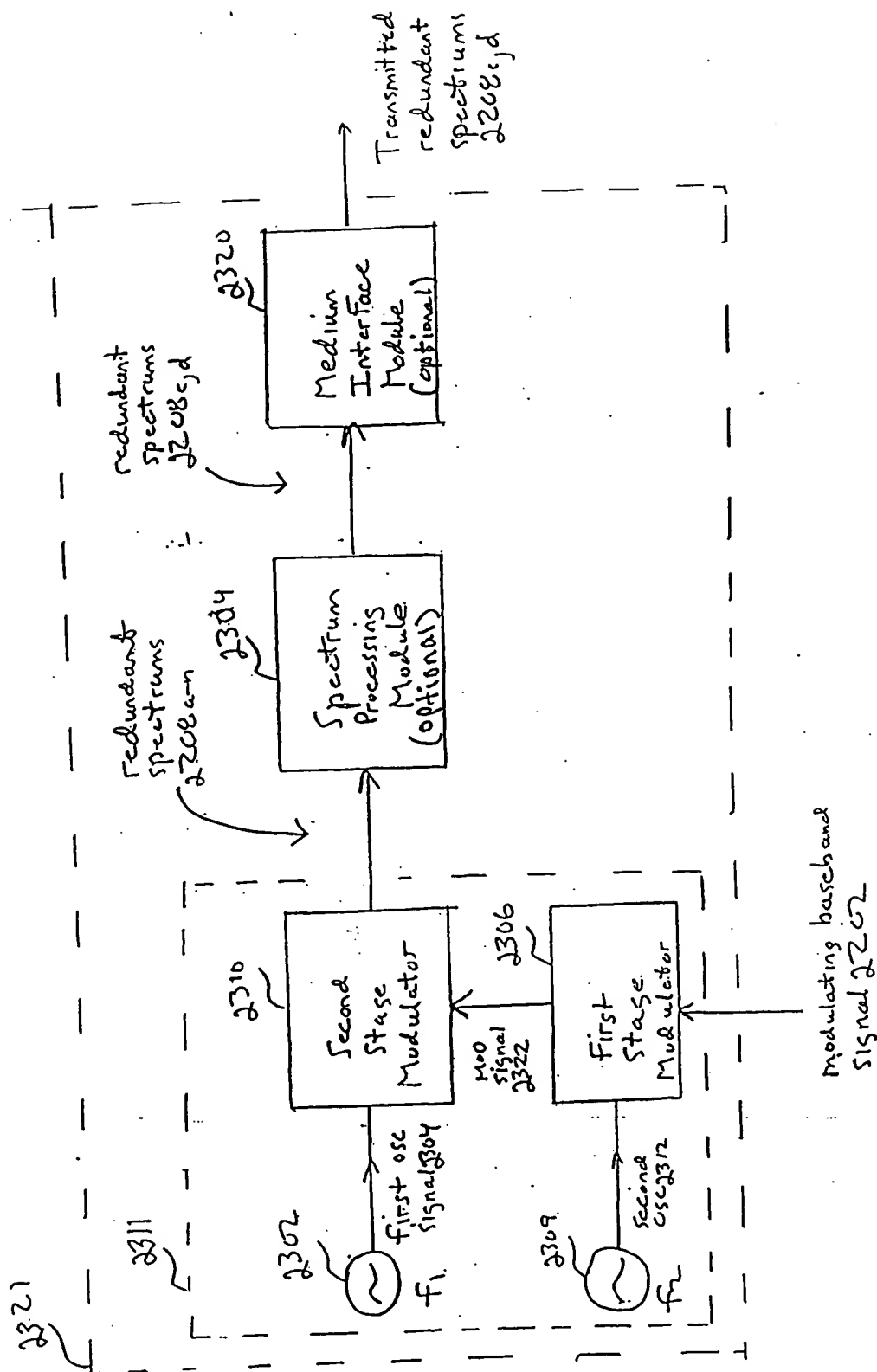


FIG. 23D

FTC: 23E

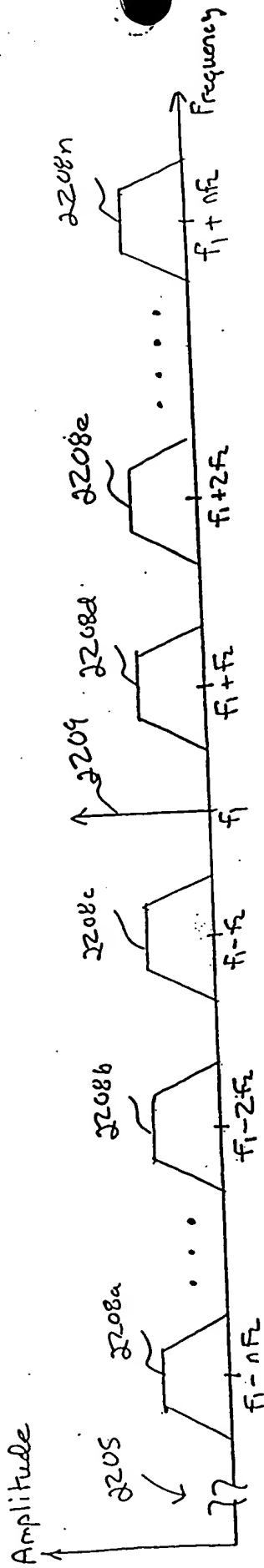
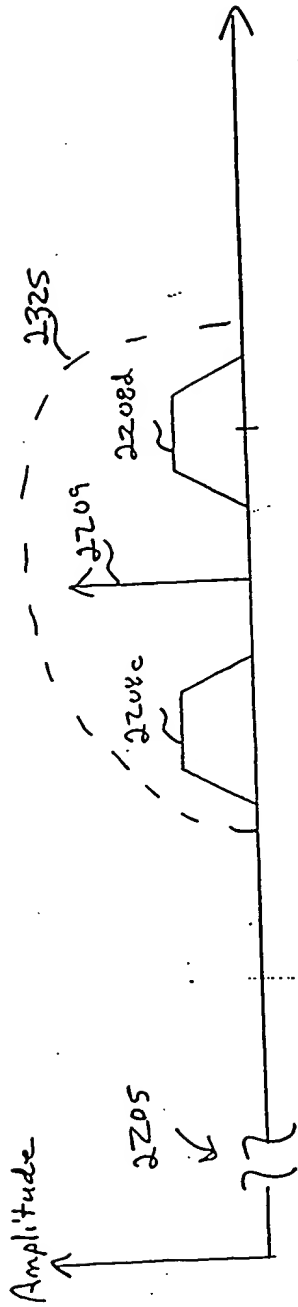
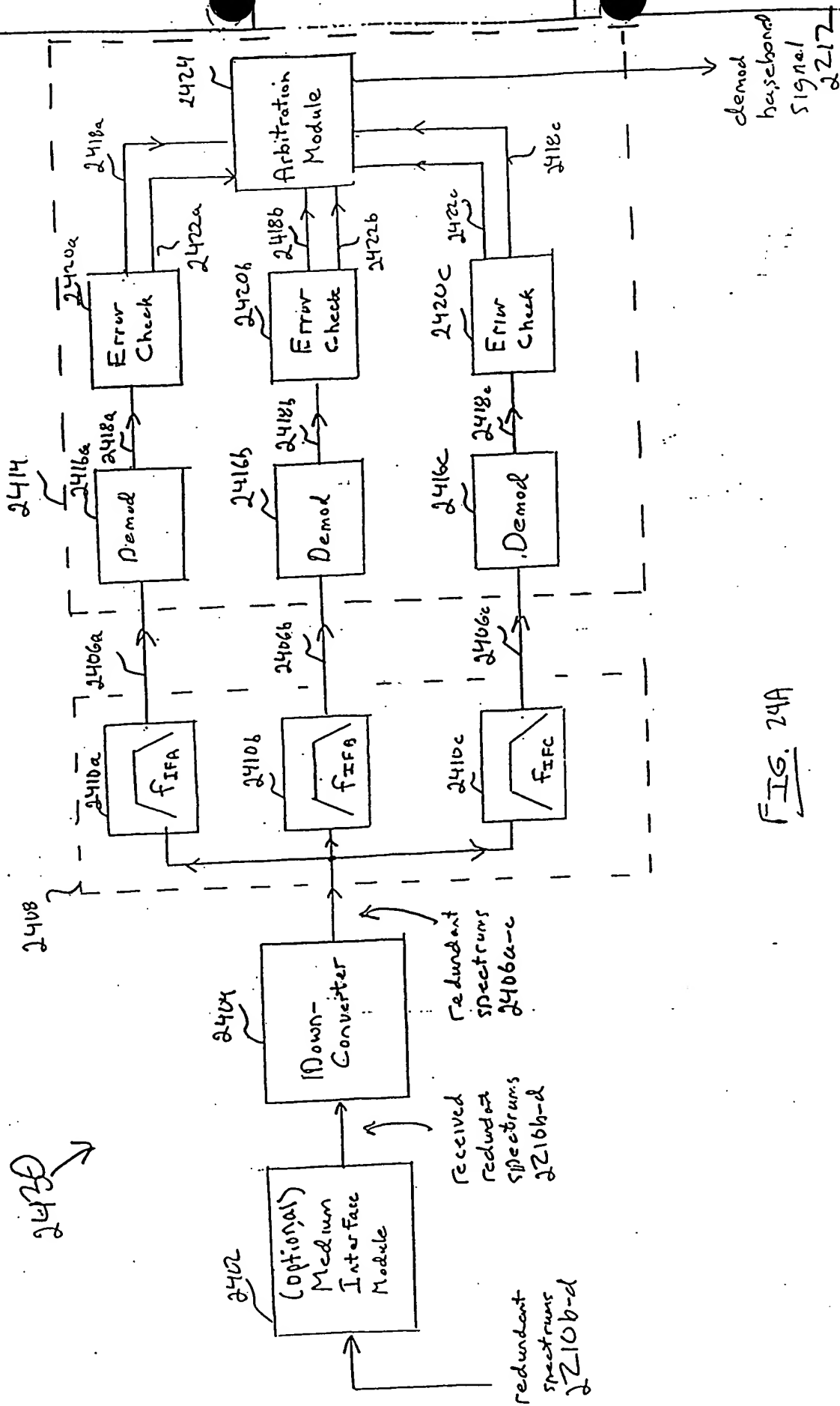
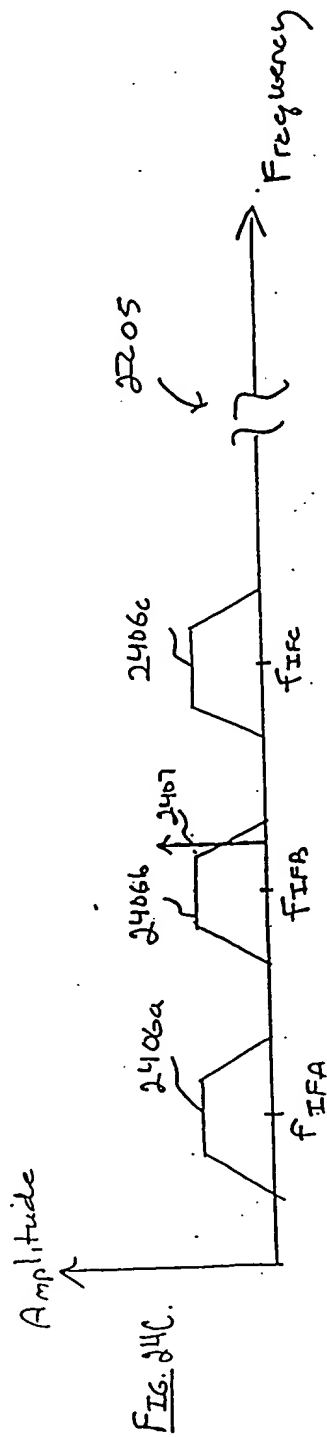
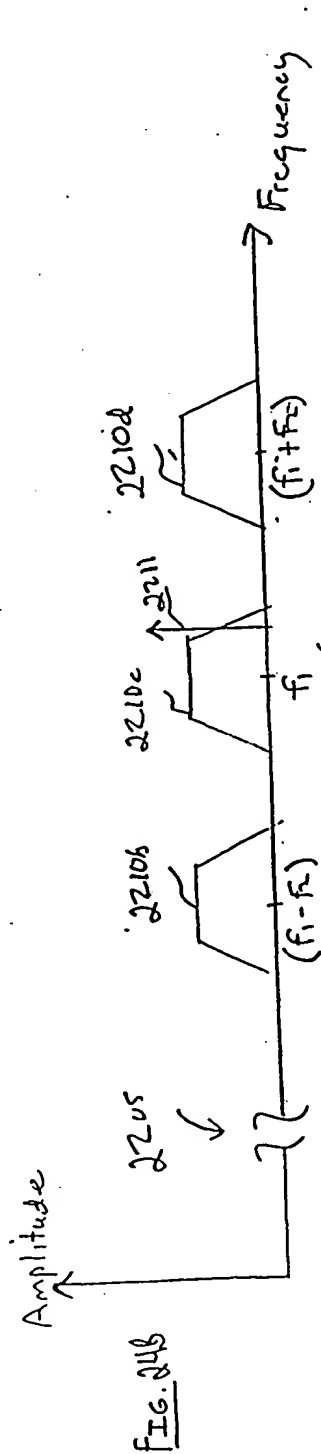


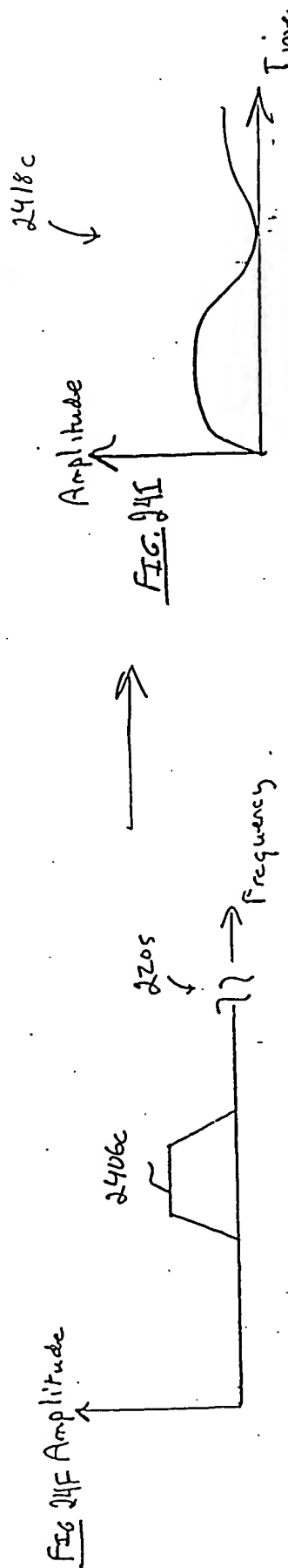
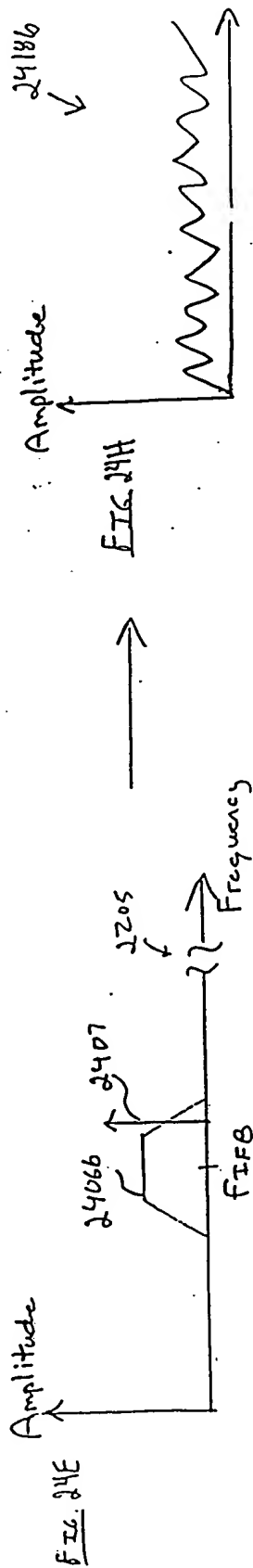
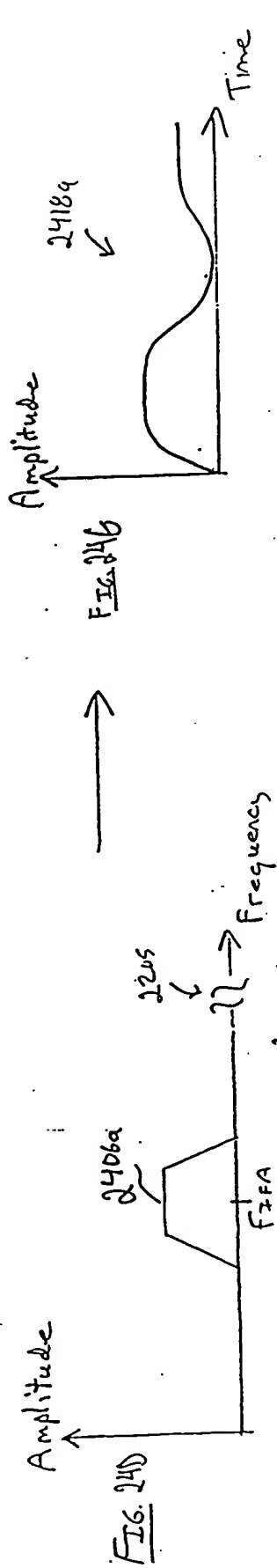
Fig. 23F



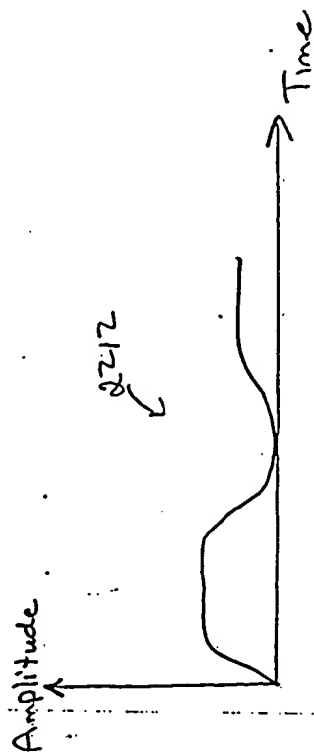


F.I.G. 24A





National Brand

[illegible]

ITC: 245

1. The first part of the paper is devoted to a review of the literature on the effects of the 1997-1998 Asian financial crisis on the economies of the Asian countries. The second part of the paper is devoted to a review of the literature on the effects of the 1997-1998 Asian financial crisis on the economies of the Asian countries. The third part of the paper is devoted to a review of the literature on the effects of the 1997-1998 Asian financial crisis on the economies of the Asian countries. The fourth part of the paper is devoted to a review of the literature on the effects of the 1997-1998 Asian financial crisis on the economies of the Asian countries. The fifth part of the paper is devoted to a review of the literature on the effects of the 1997-1998 Asian financial crisis on the economies of the Asian countries. The sixth part of the paper is devoted to a review of the literature on the effects of the 1997-1998 Asian financial crisis on the economies of the Asian countries. The seventh part of the paper is devoted to a review of the literature on the effects of the 1997-1998 Asian financial crisis on the economies of the Asian countries. The eighth part of the paper is devoted to a review of the literature on the effects of the 1997-1998 Asian financial crisis on the economies of the Asian countries. The ninth part of the paper is devoted to a review of the literature on the effects of the 1997-1998 Asian financial crisis on the economies of the Asian countries. The tenth part of the paper is devoted to a review of the literature on the effects of the 1997-1998 Asian financial crisis on the economies of the Asian countries.

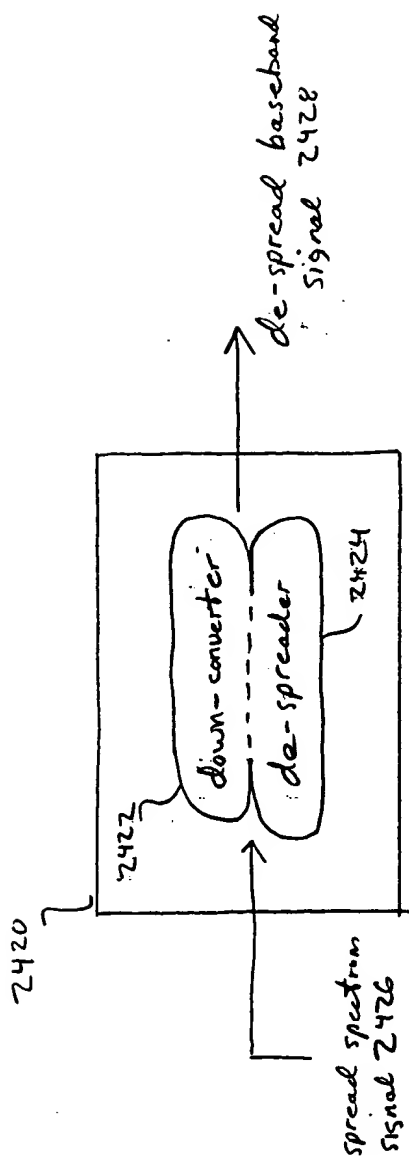


Fig. 24k

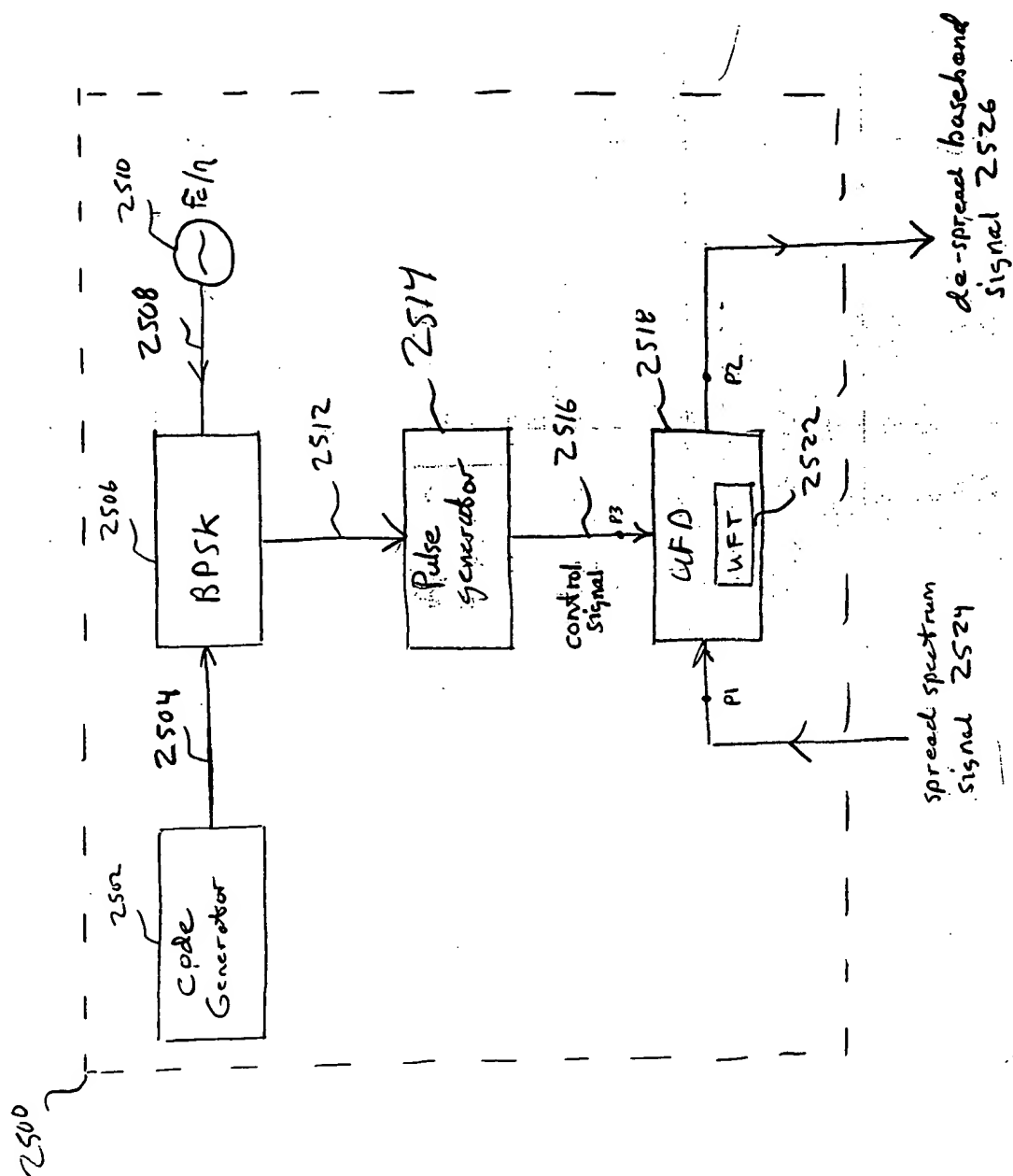
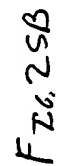


FIG. 25A



2540

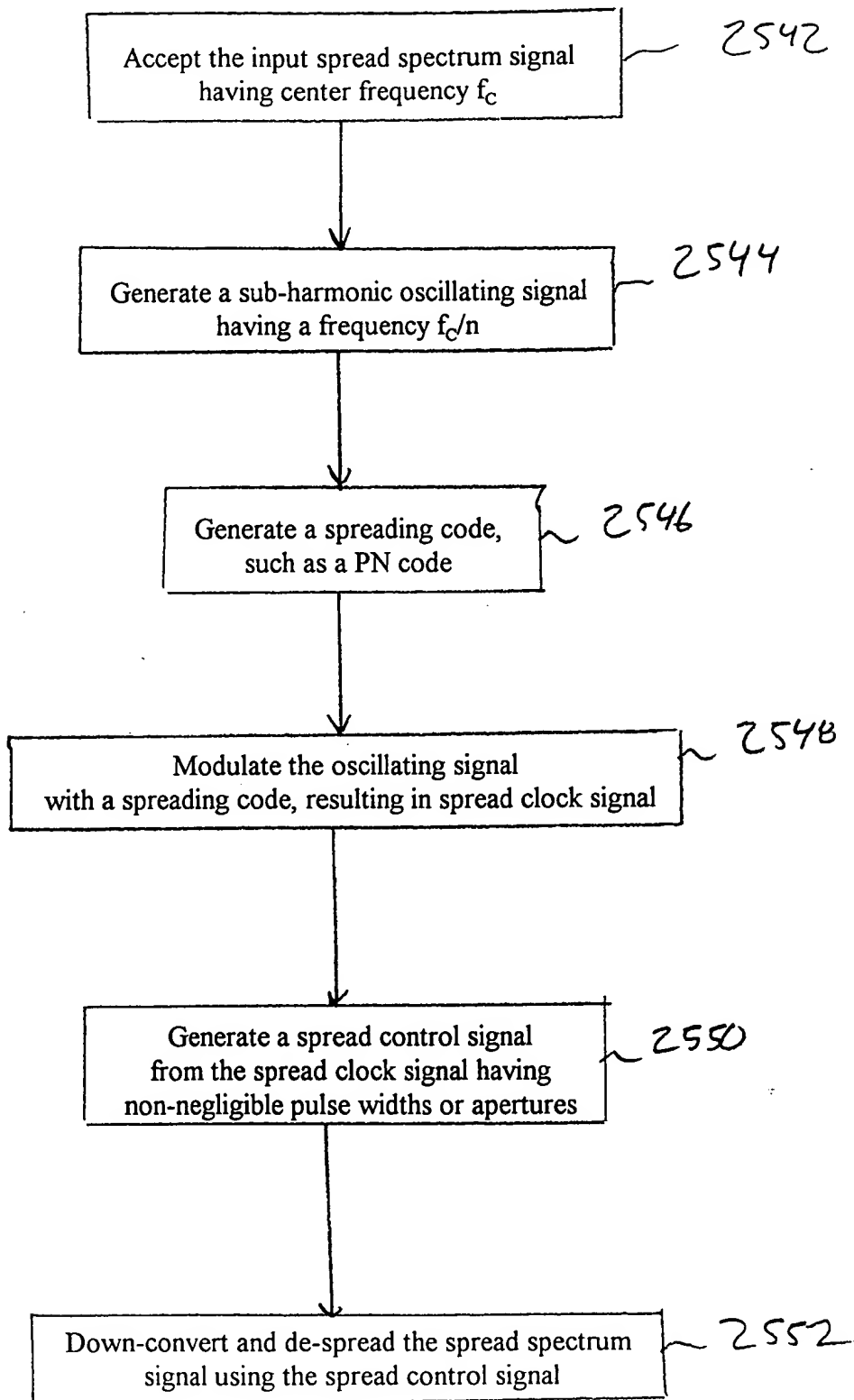


FIG. 25C

Under-sample the spread spectrum signal
according to the spread control signal

2554

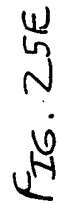


Store the undersamples using a storage element

2556

2552

FIG. 250





F16.2SF

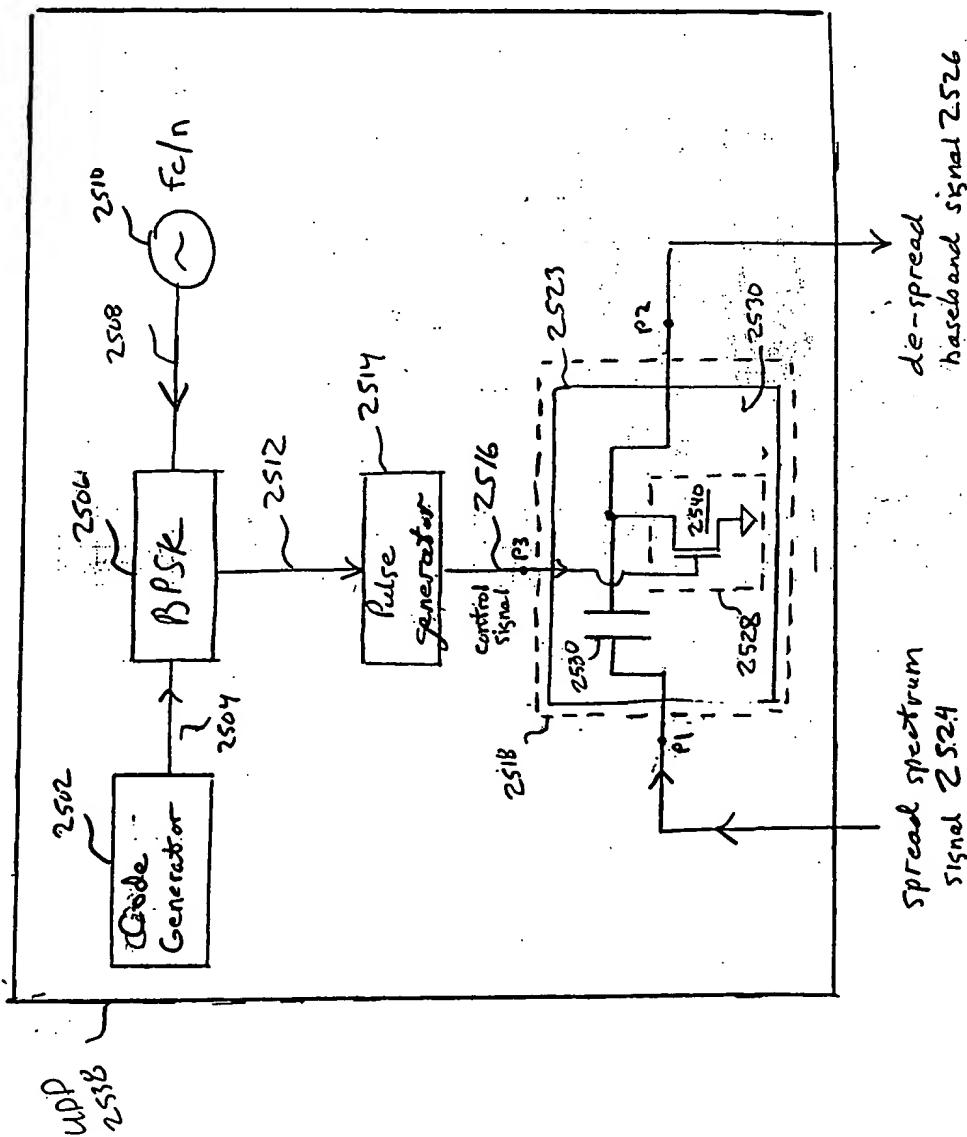


FIG. 25G.

```
graph TD; 2562[Perform matched filter/correlating operation on an approximate half-cycle of the input spread spectrum signal] --> 2564[Accumulate the result of the matched filter correlation]; 2564 --> 2566[Repeat steps 2562 and 2564 over over half cycles of the spread spectrum signal according to the spread control signal];
```

2552

2562

2564

2566

FIG. 25H

004400 03400 03400 03400

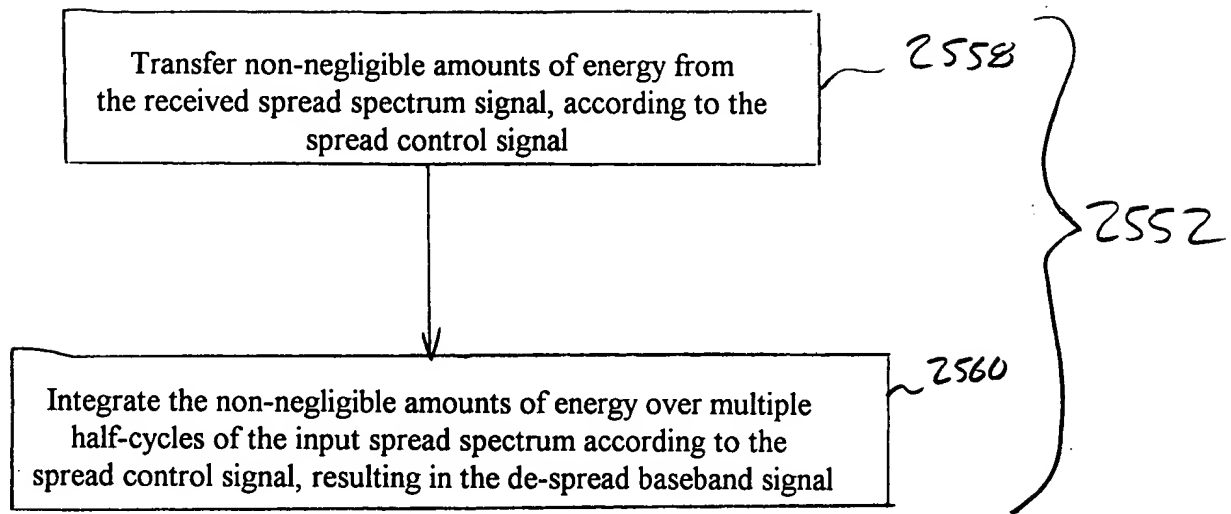


FIG. 25I

09526405 034400

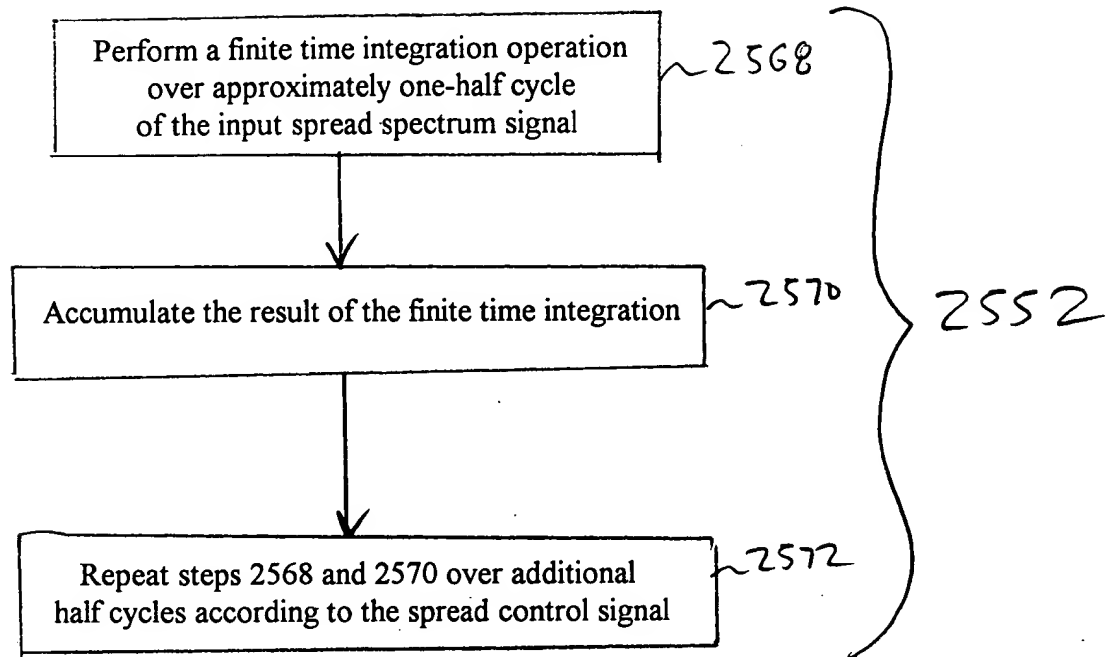
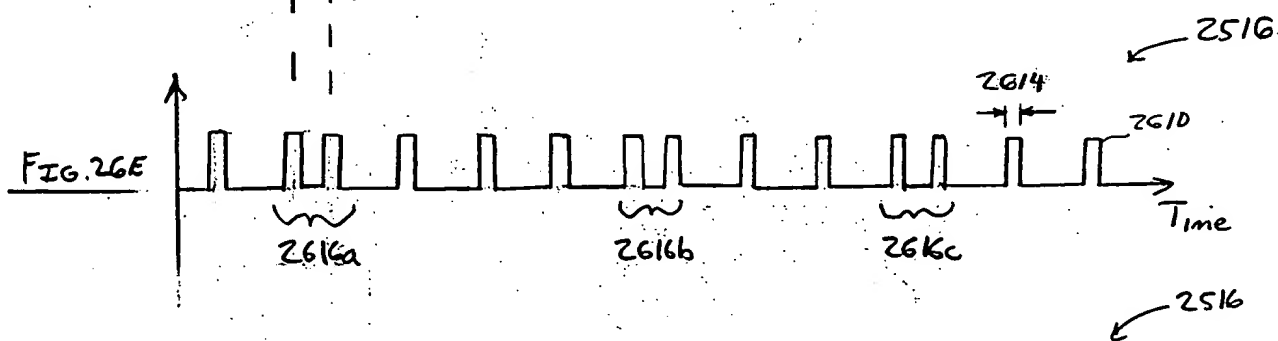
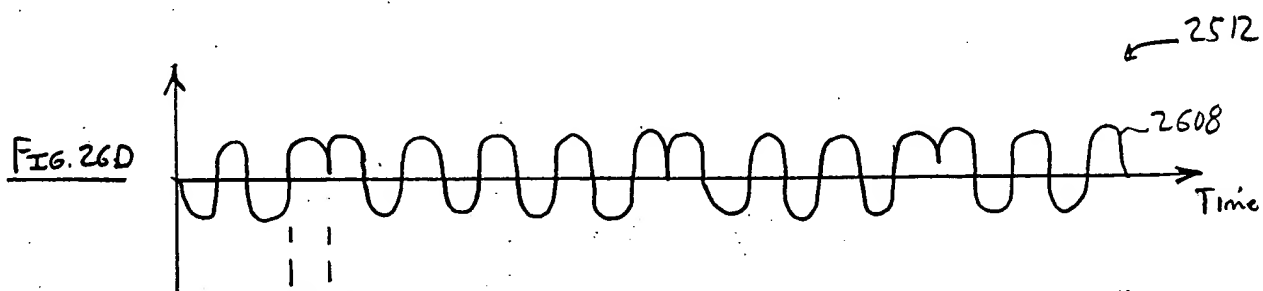
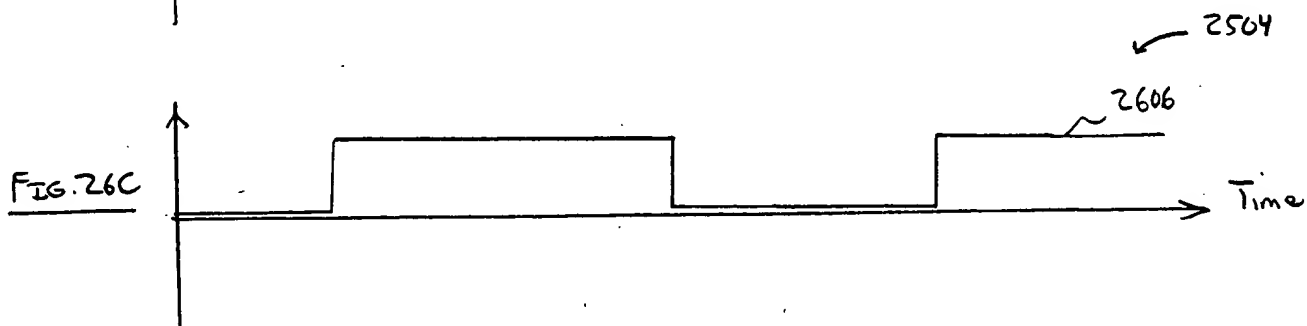
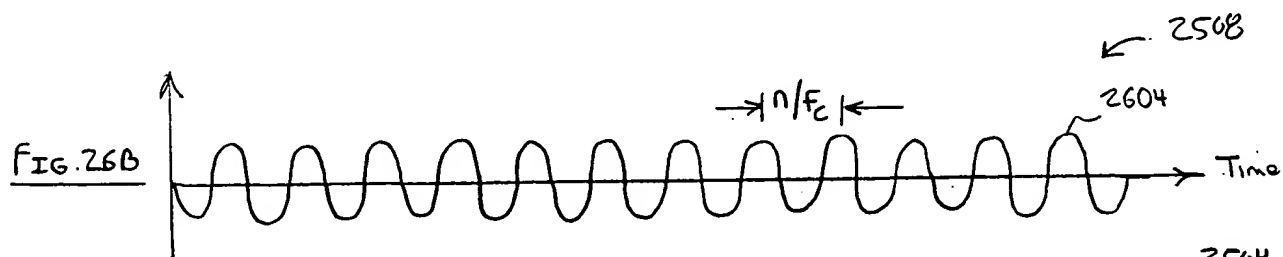


FIG. 25J



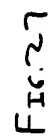


FIG. 28C

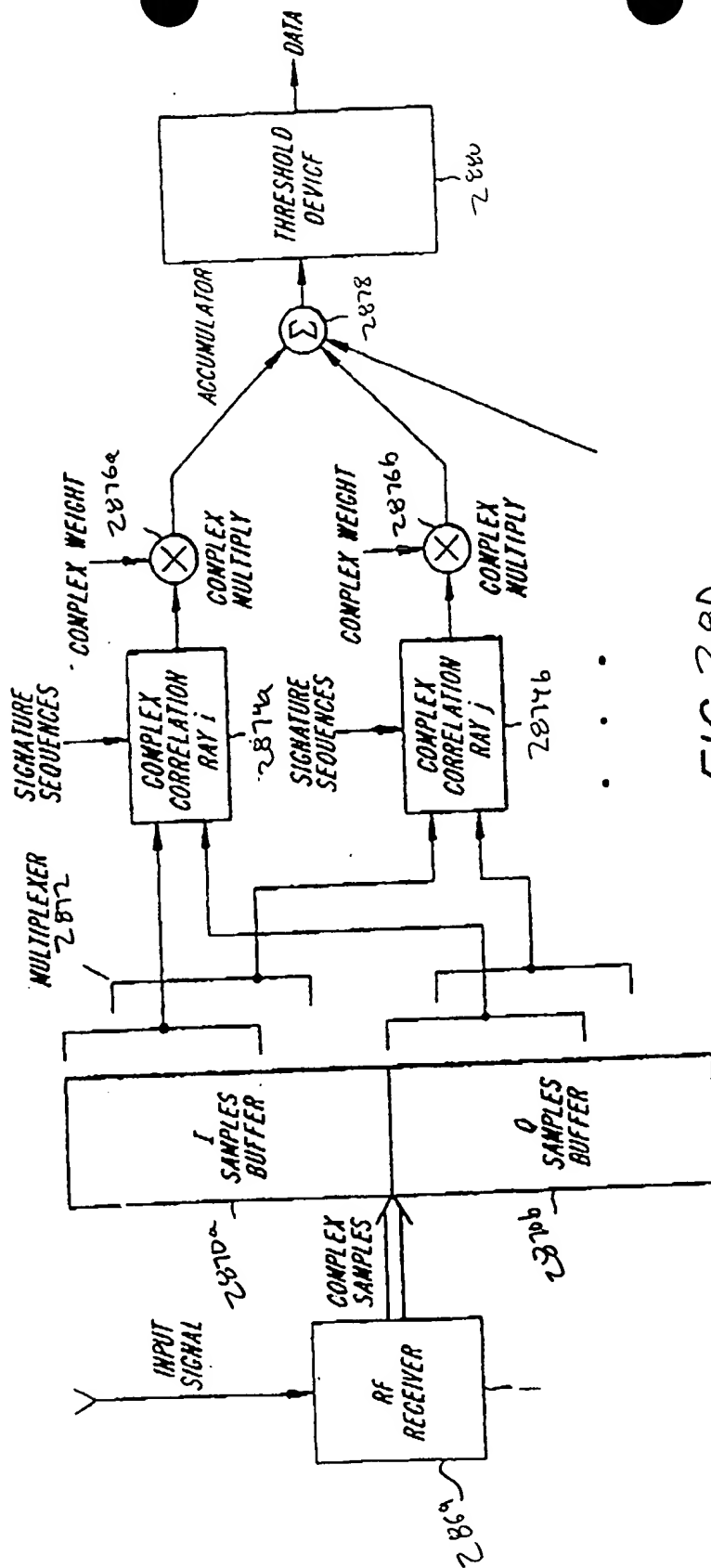


FIG. 28D

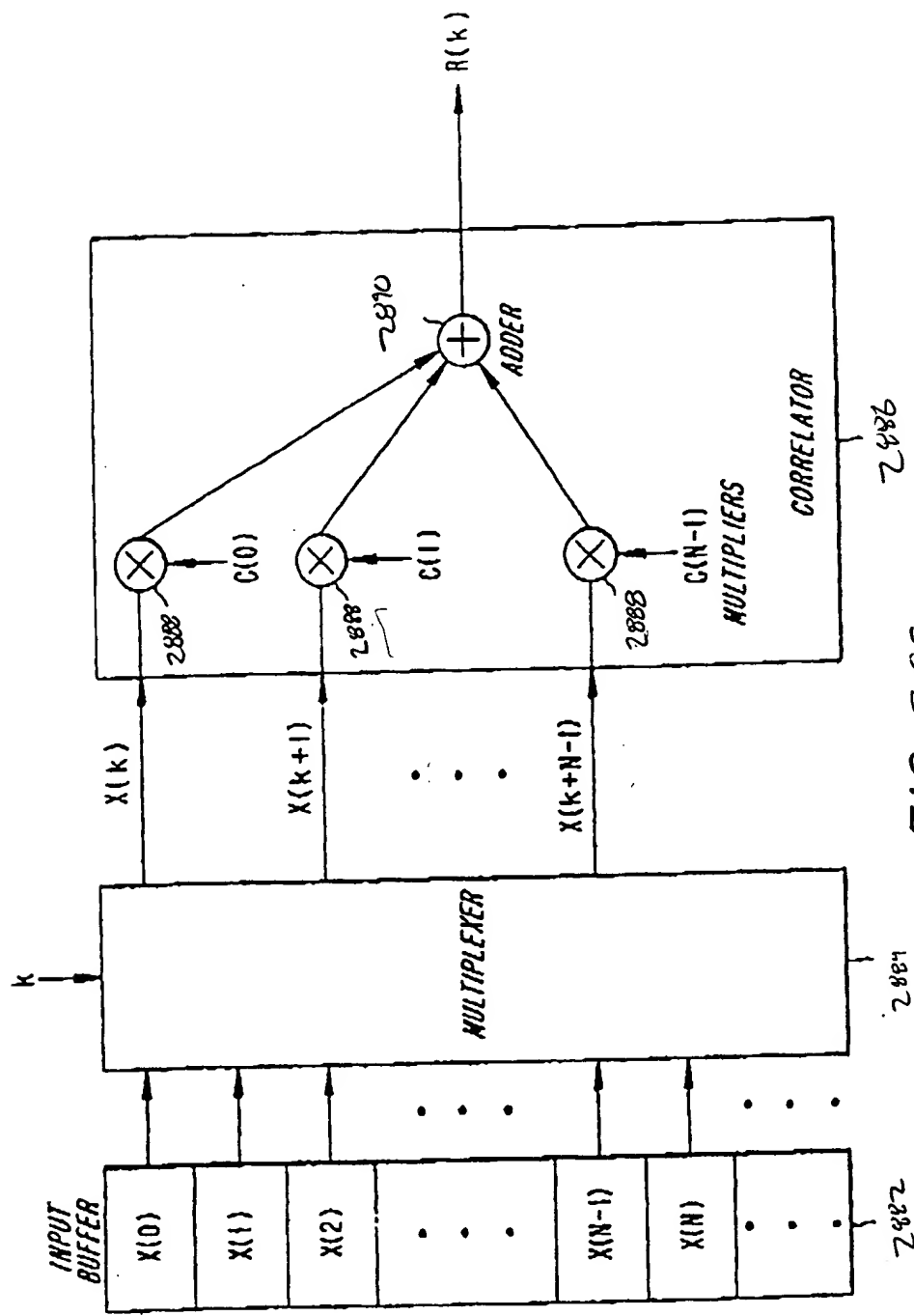


FIG. 28E

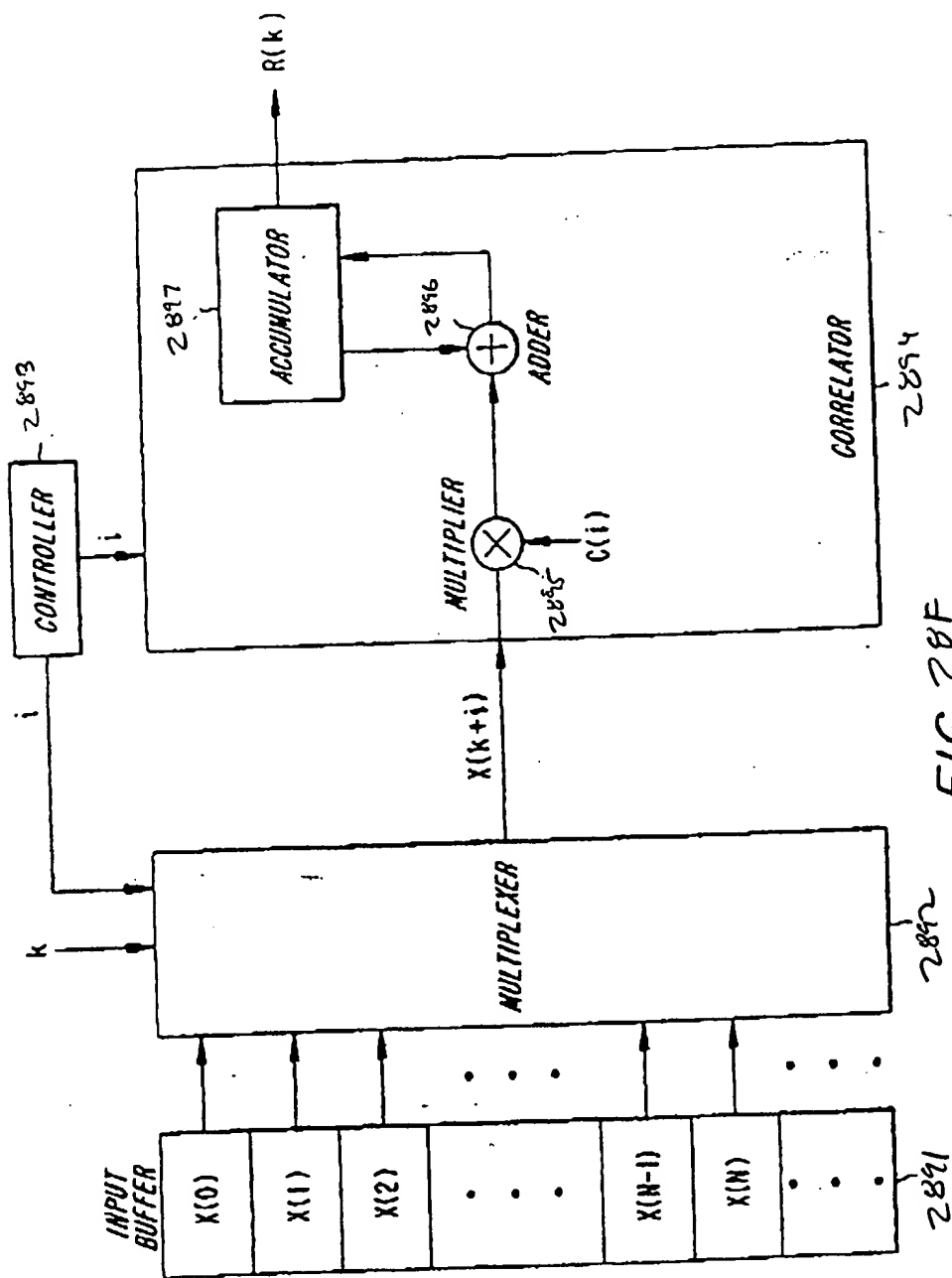


FIG. 28F

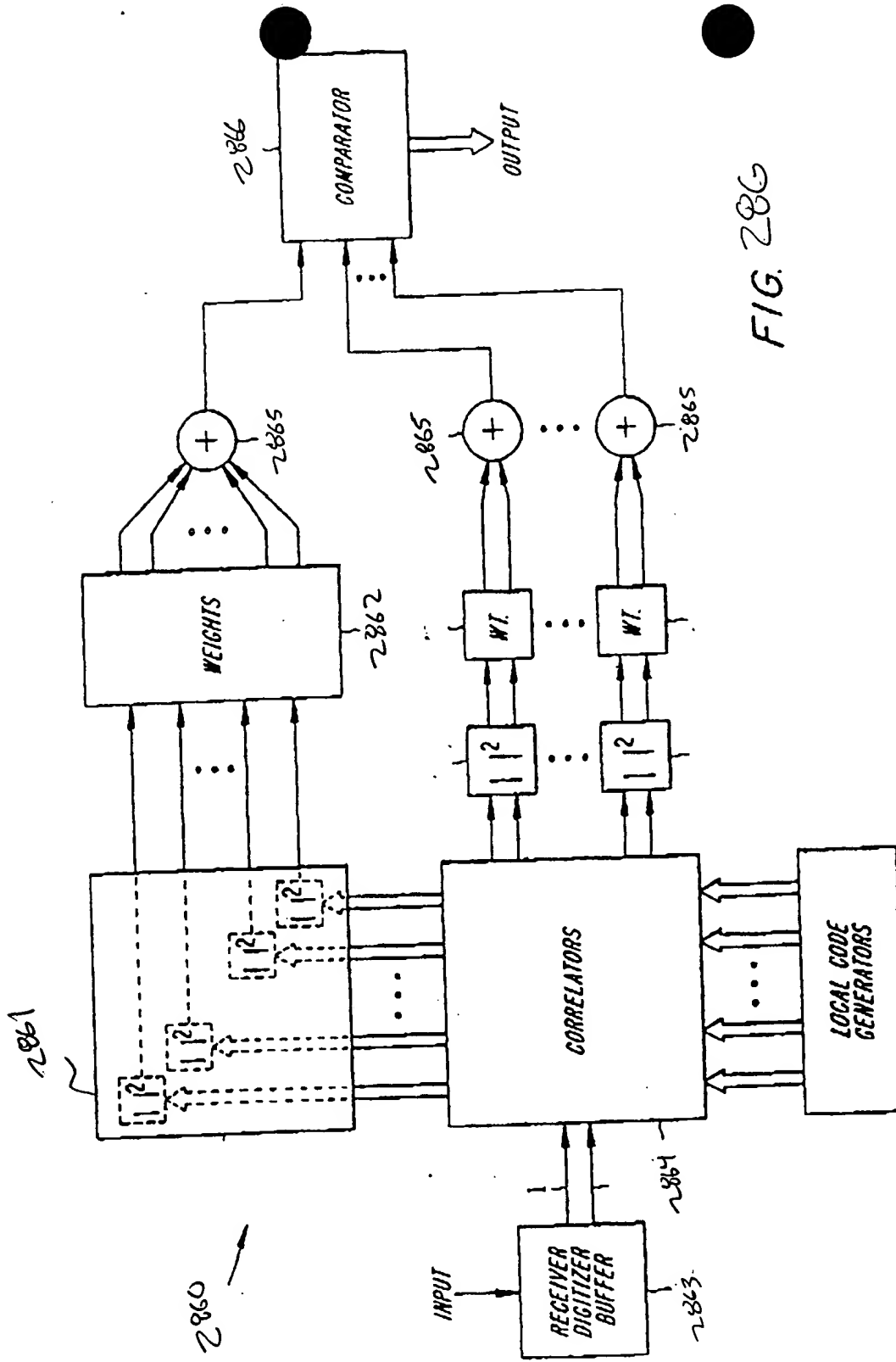


FIG. 286

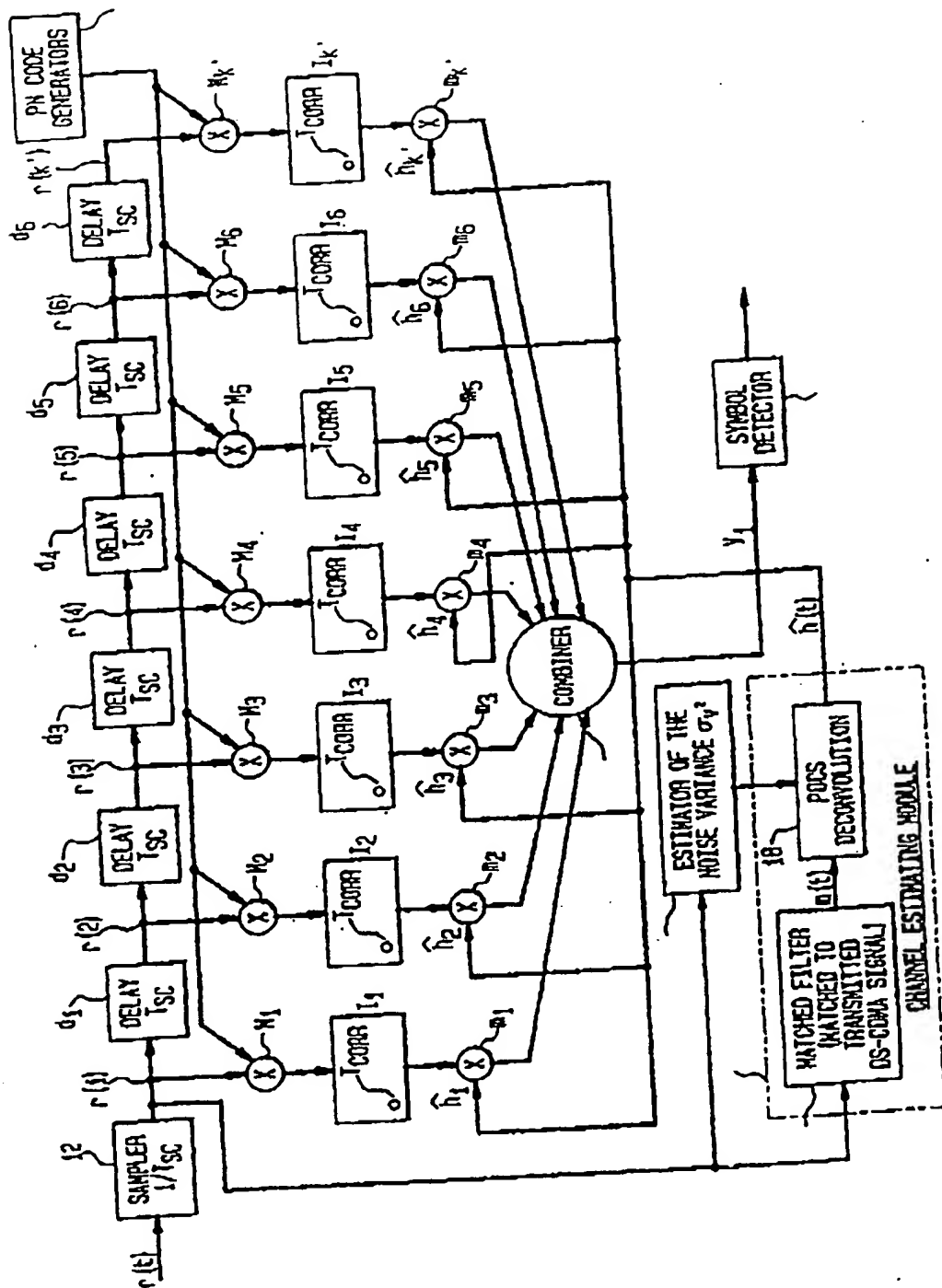
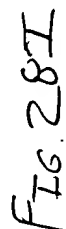


FIG. 2.8H



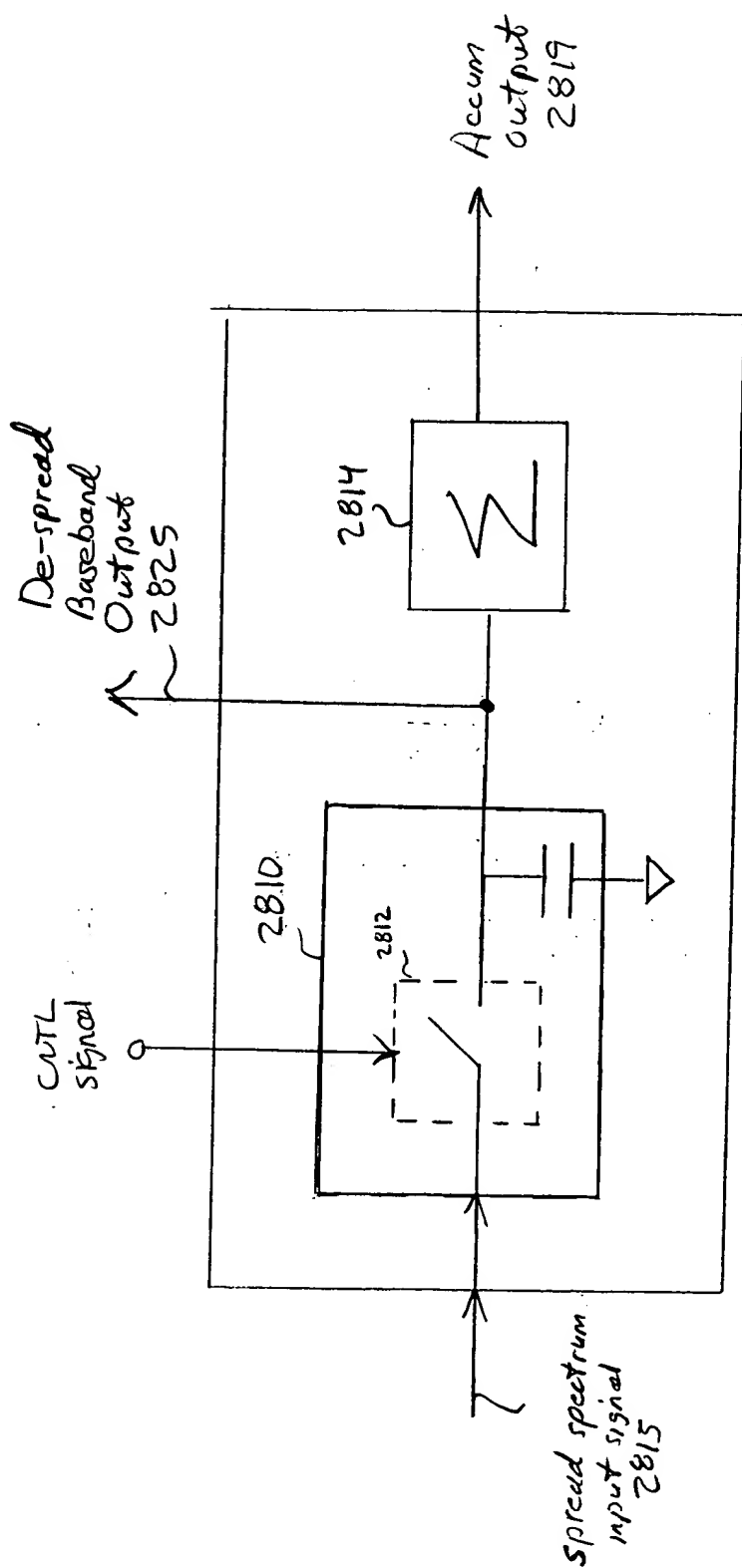
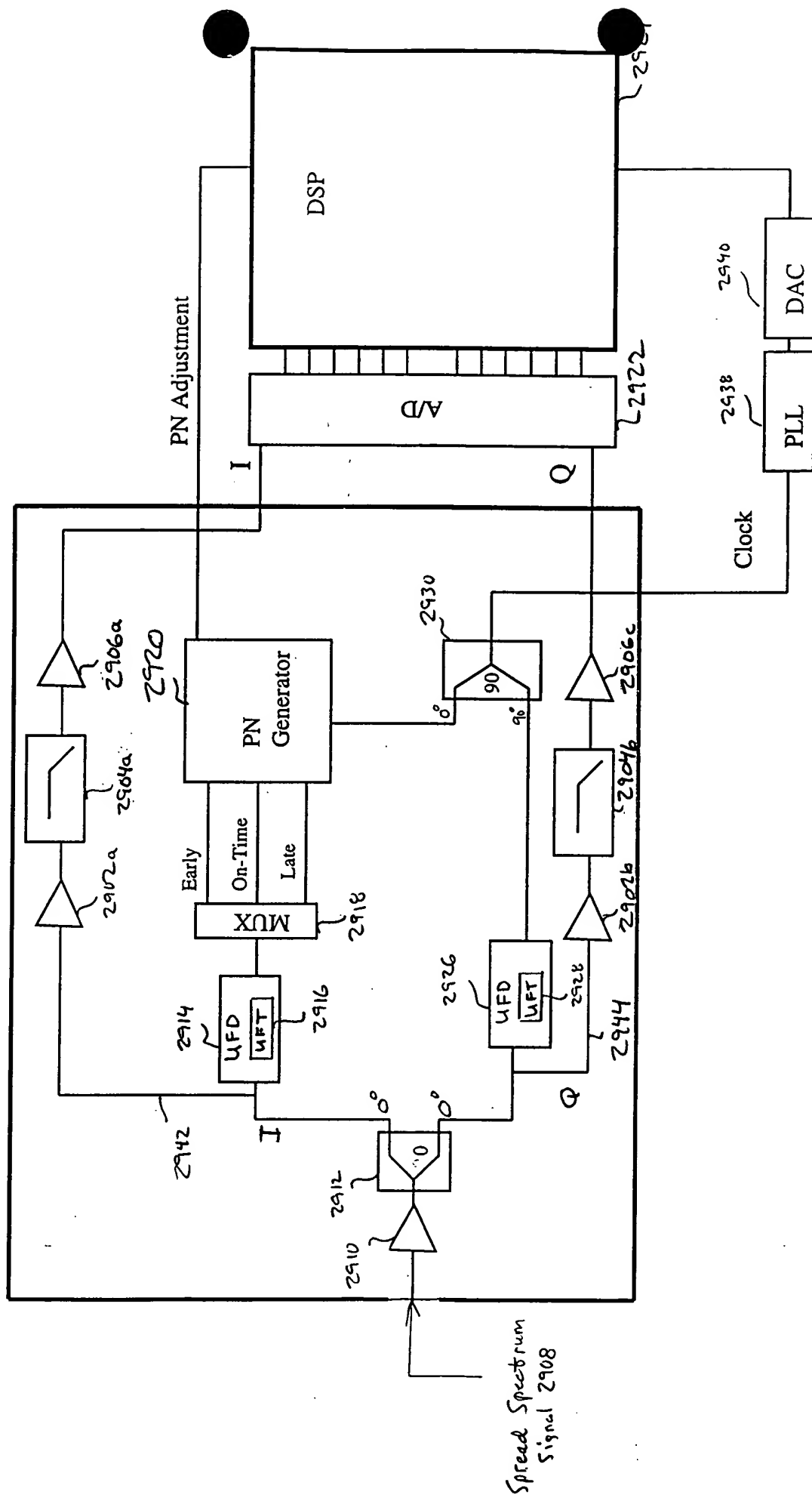


FIG. 28J

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 91. *... ..*
 92. *... ..*
 93. *... ..*
 94. *... ..*
 95. *... ..*
 96. *... ..*
 97. *... ..*
 98. *... ..*
 99. *... ..*
 100. *... ..*



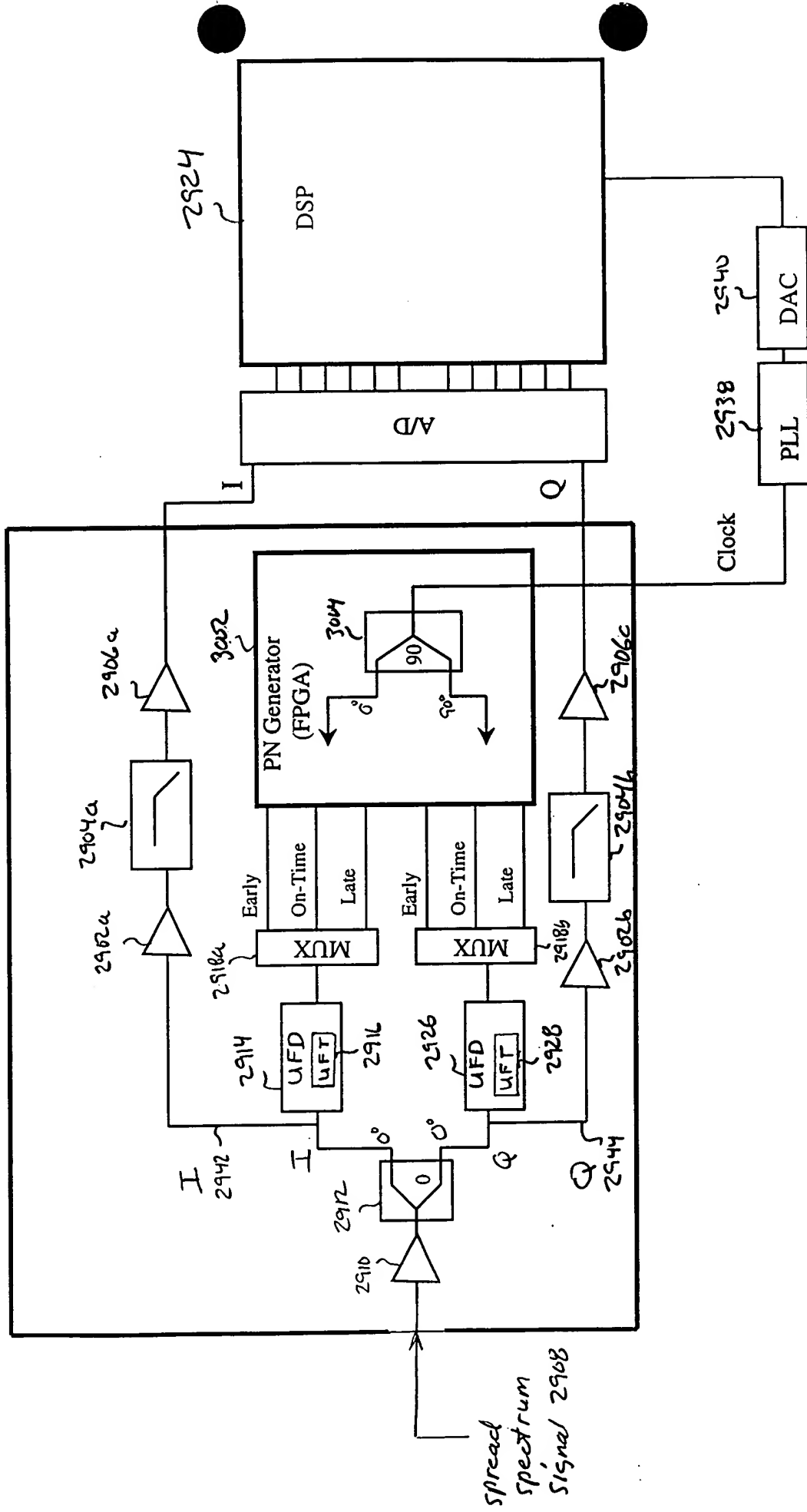


FIG. 30

uF4
3101

3102

Code
Generator

3103

BPSK

3104

Pulse
Shaper

3105

Control
Signal
3109

3106

f_0

Port 3

Port 1

Port 2

3112

Bias Signal

3116

uF4
3114

3117

3120

3122

3118

3110

spread spectrum
signal 3126

Baseband
Signal 3124

FIG. 31A

3117 →

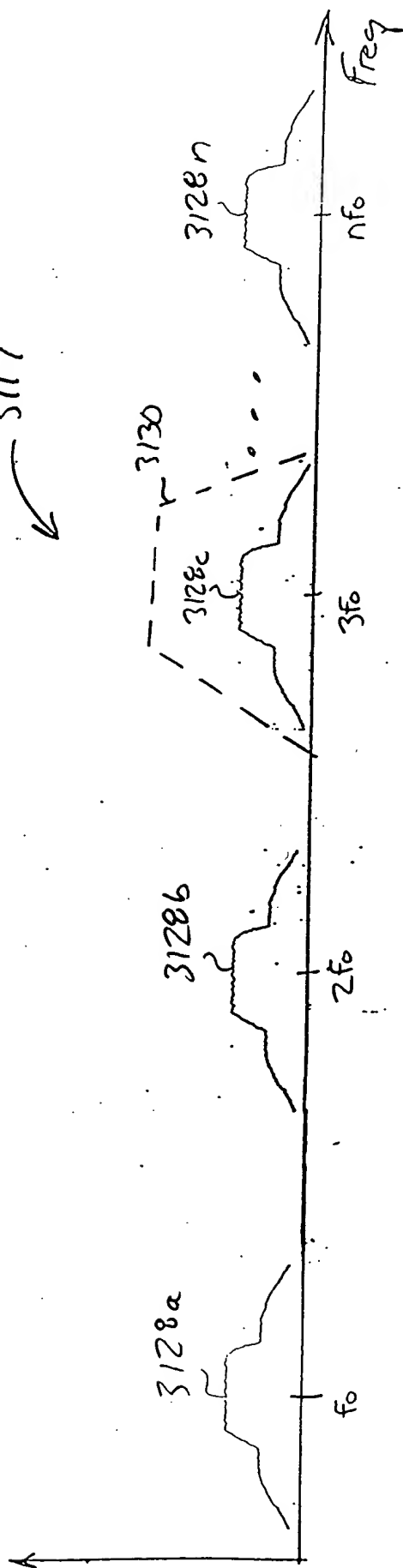


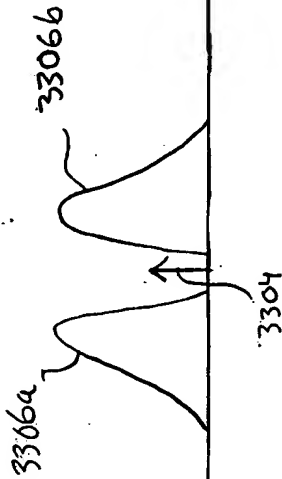
FIG. 3/B



COPIES OF 500

Amplitude

3302



Frequency

FIG. 33A

Amplitude

3308



Frequency

FIG. 33B

42-381 50 SHEETS FILED 5 SQUARE
42-382 100 SHEETS FILED 5 SQUARE
42-383 200 SHEETS FILED 5 SQUARE
42-384 300 SHEETS FILED 5 SQUARE
42-385 400 SHEETS FILED 5 SQUARE
42-386 500 SHEETS FILED 5 SQUARE
42-387 600 SHEETS FILED 5 SQUARE
42-388 700 SHEETS FILED 5 SQUARE
42-389 800 SHEETS FILED 5 SQUARE
42-390 900 SHEETS FILED 5 SQUARE
42-391 1000 SHEETS FILED 5 SQUARE
42-392 1100 SHEETS FILED 5 SQUARE
42-393 1200 SHEETS FILED 5 SQUARE
42-394 1300 SHEETS FILED 5 SQUARE
42-395 1400 SHEETS FILED 5 SQUARE
42-396 1500 SHEETS FILED 5 SQUARE
42-397 1600 SHEETS FILED 5 SQUARE
42-398 1700 SHEETS FILED 5 SQUARE
42-399 1800 SHEETS FILED 5 SQUARE
42-400 1900 SHEETS FILED 5 SQUARE
42-401 2000 SHEETS FILED 5 SQUARE

3402

CNTL signal 3423

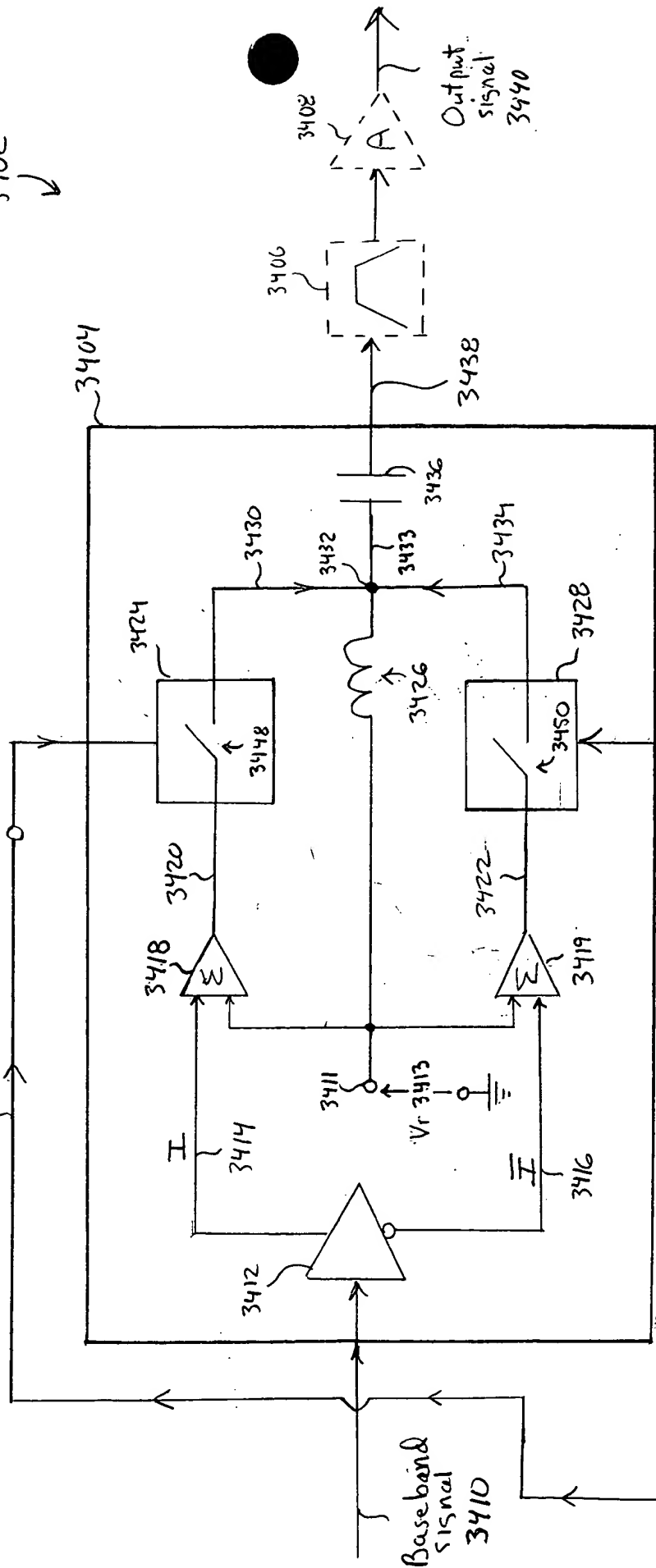
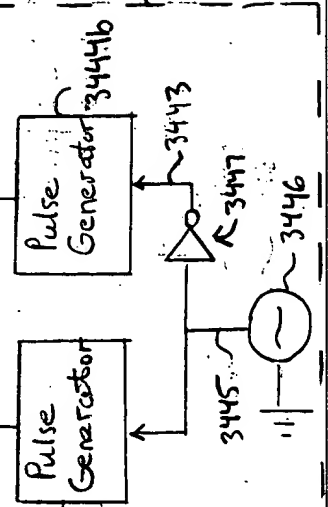


FIG. 34A

CNTL SWGL 3427

3442



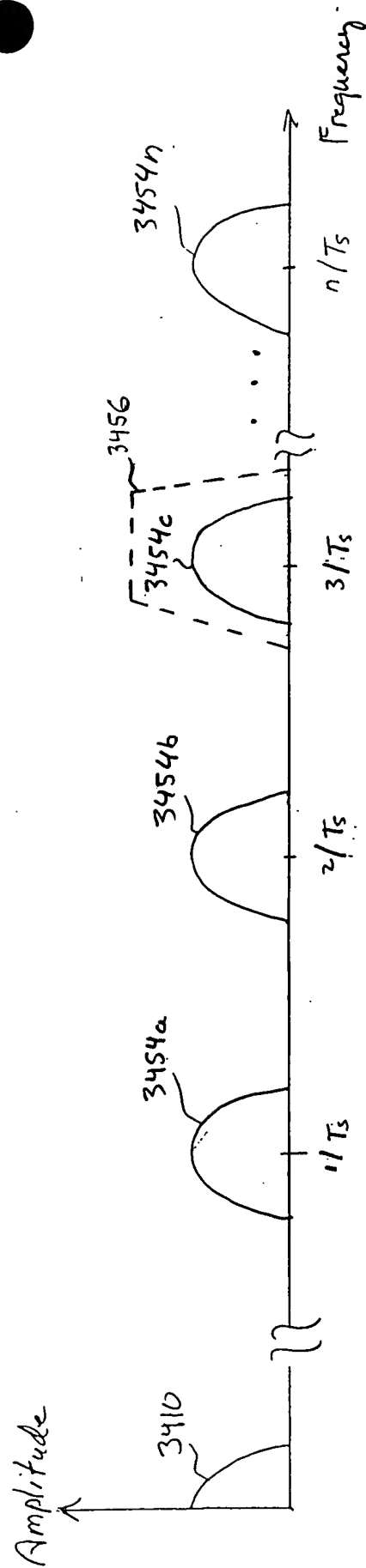


FIG. 34C

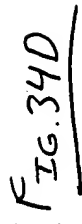


FIG. 35A

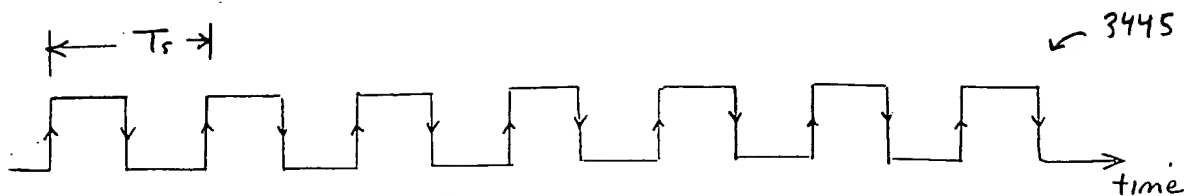


FIG. 35B

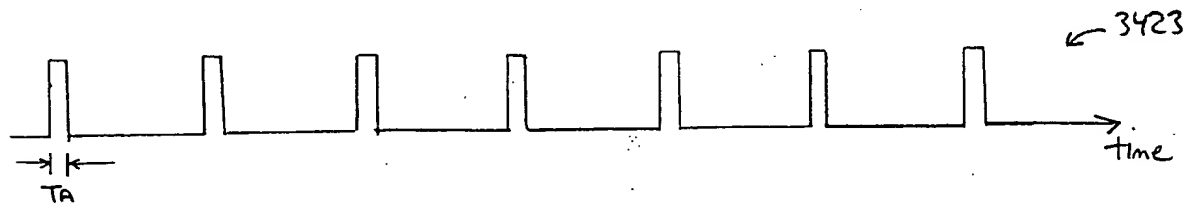


FIG. 35C

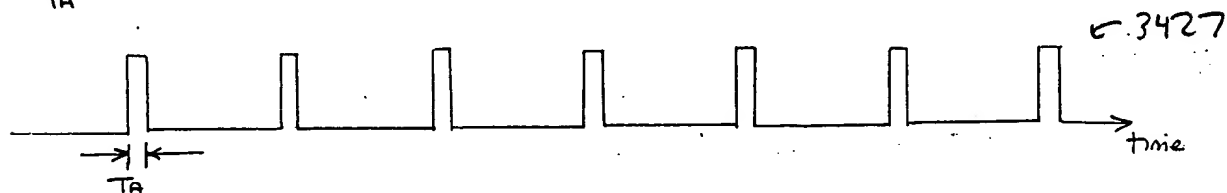


FIG. 35D

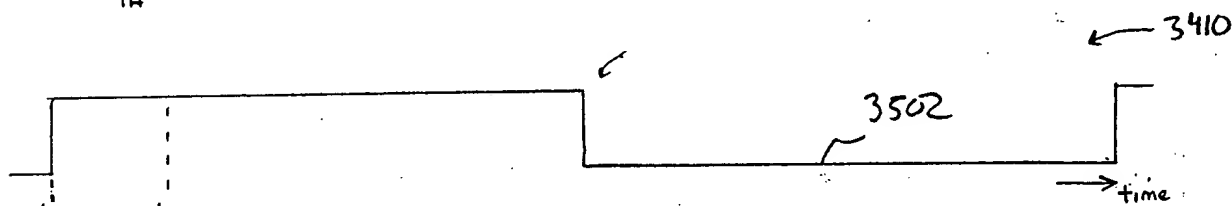


FIG. 35E

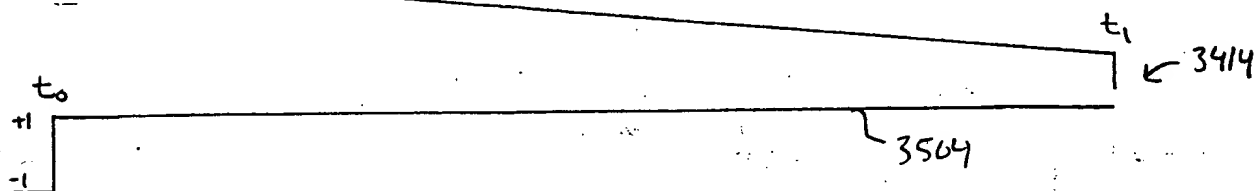


FIG. 35F

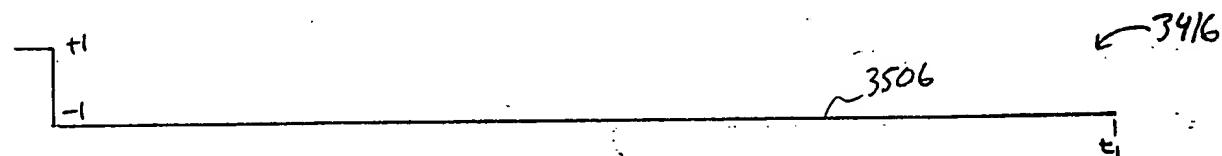


FIG. 35G

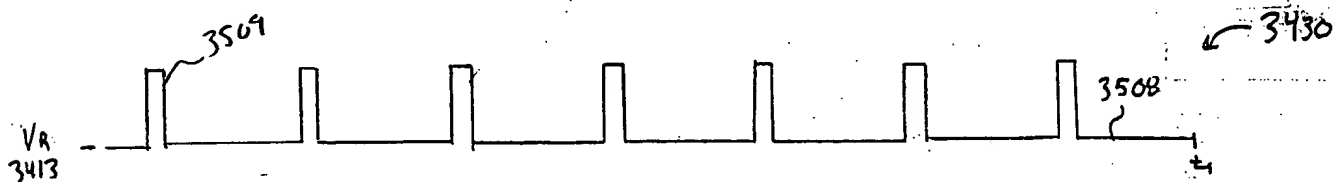


FIG. 35H

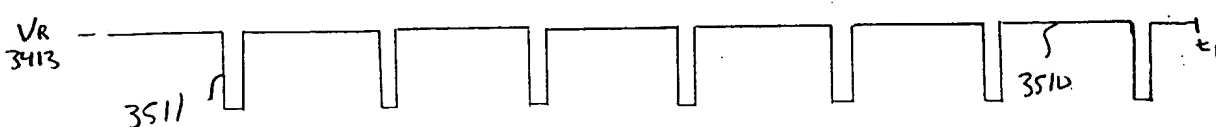
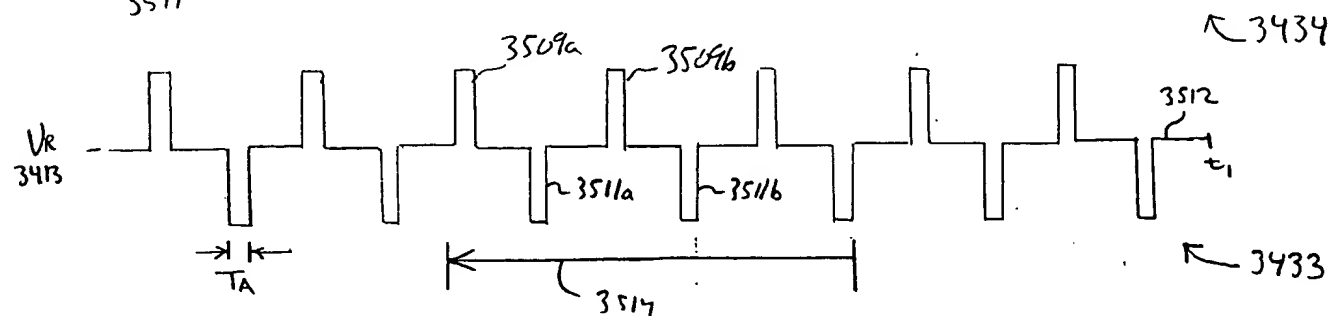


FIG. 35I



00440 50652560

Aperture = 500ps

Fundamental Clock = 200Mhz (5th Subharmonic)

Square Wave Frequency = 200Mhz

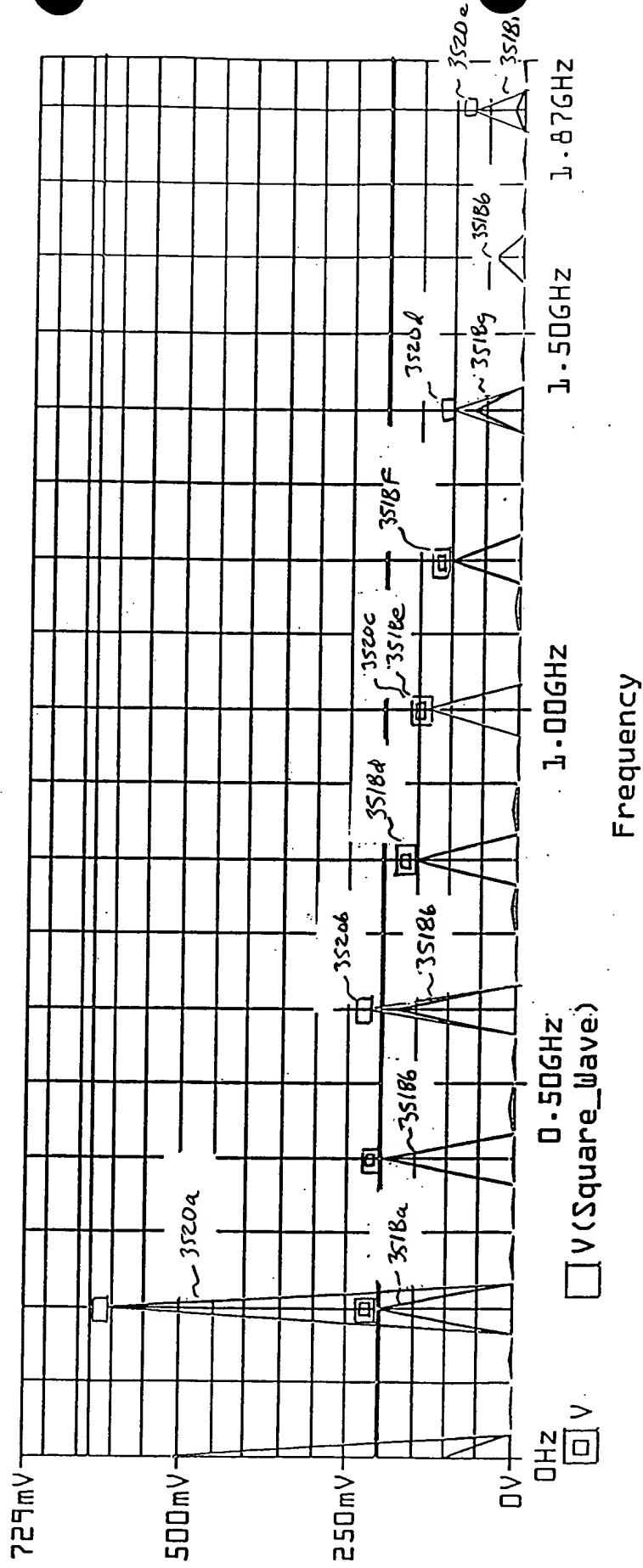


FIG. 35J

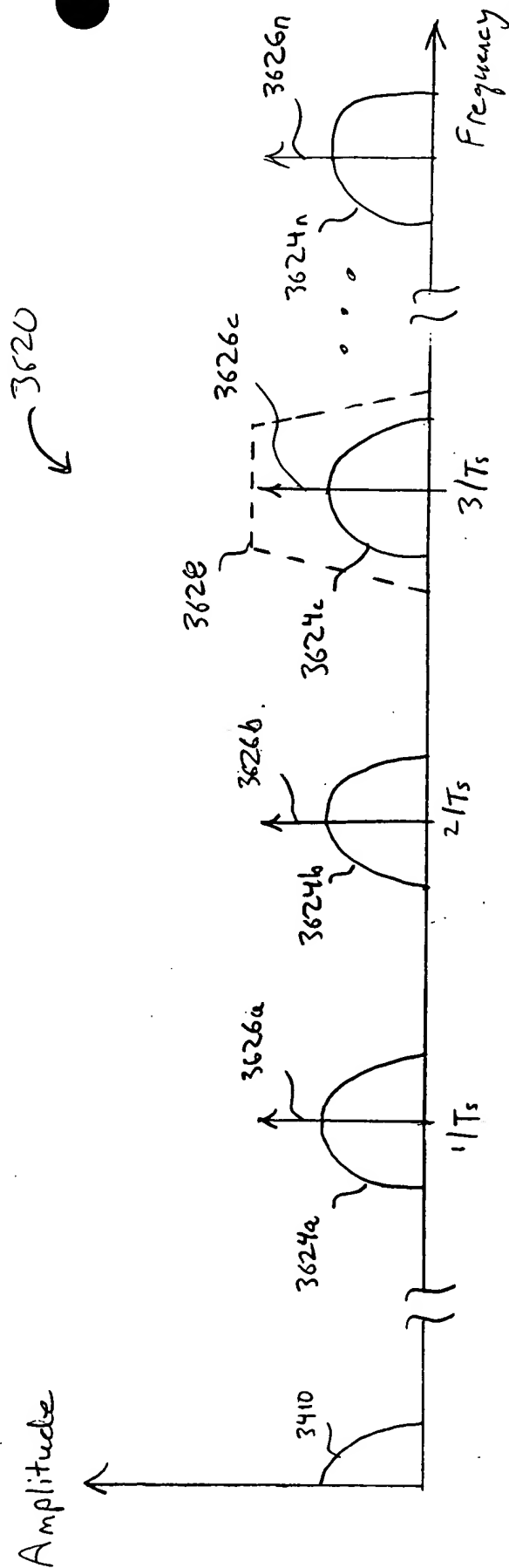
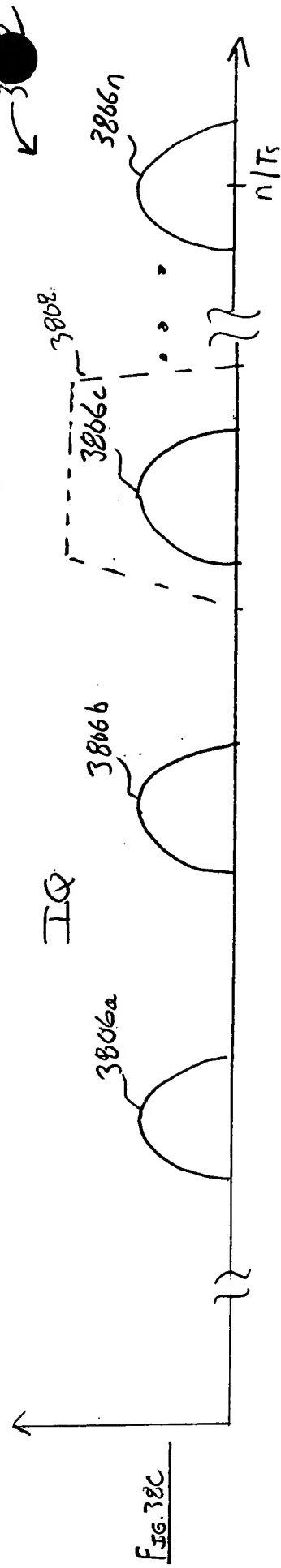
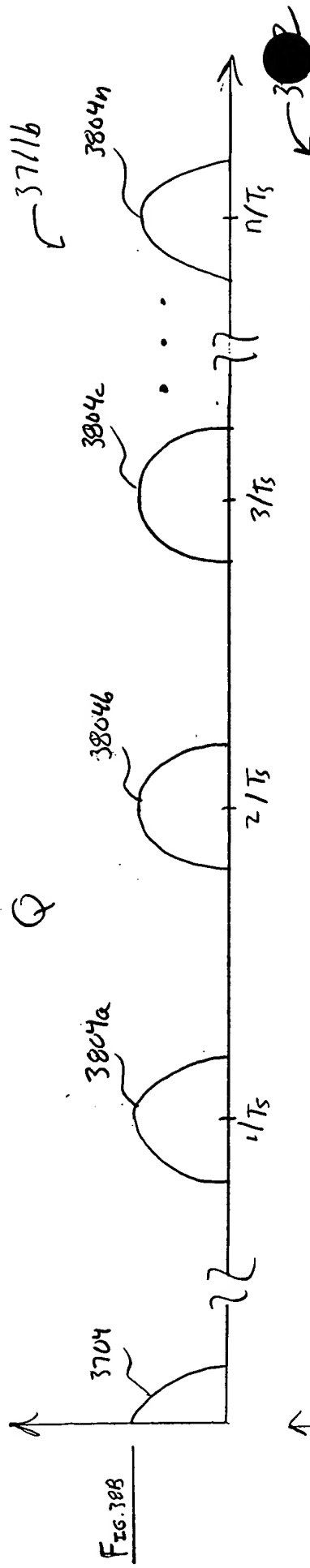
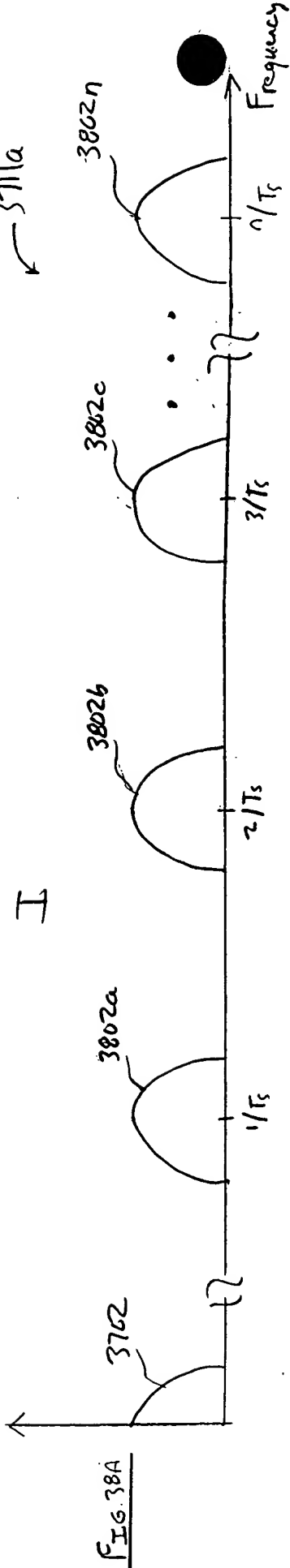


FIG. 36B



3908

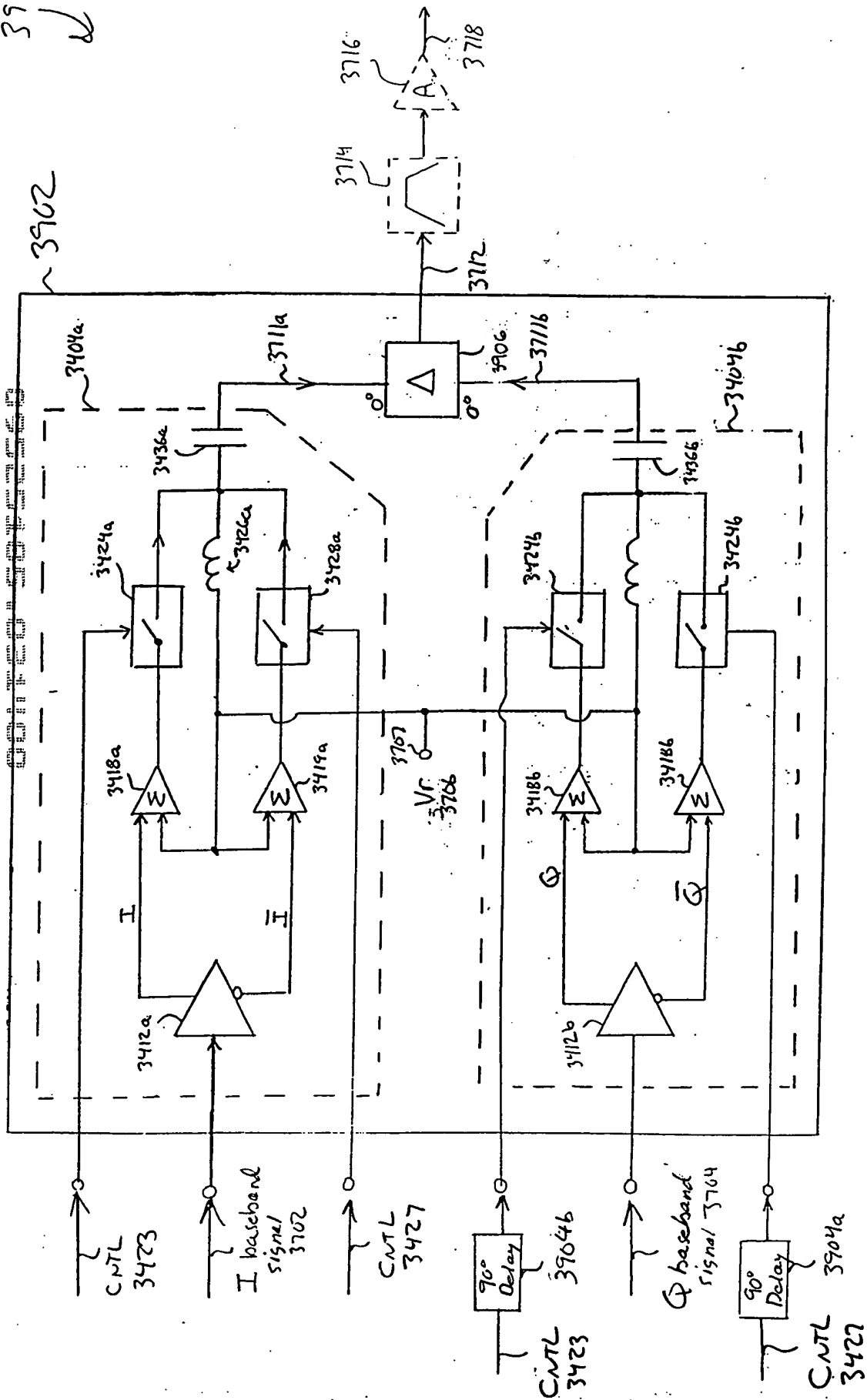


FIG. 39A

3918

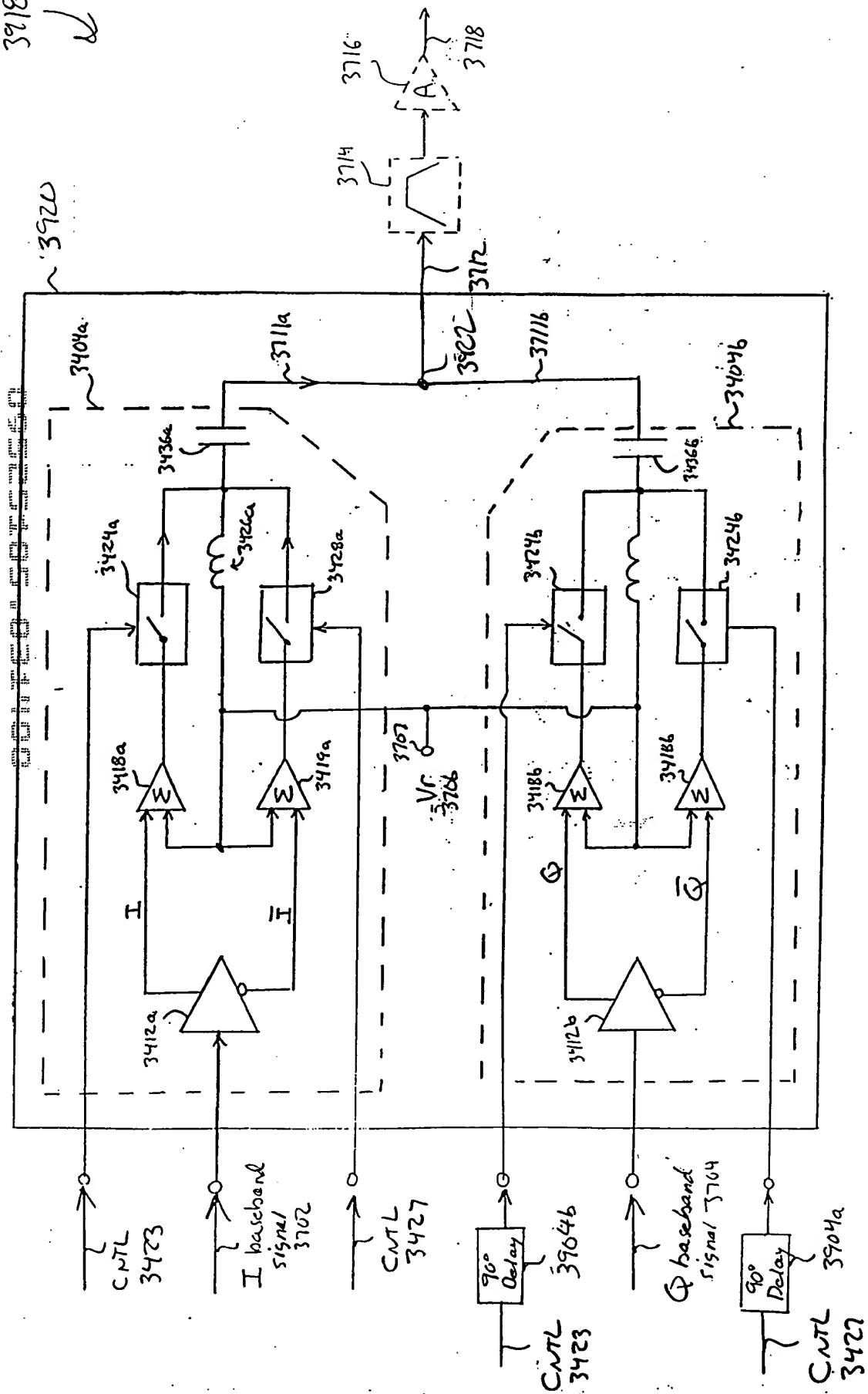
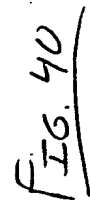
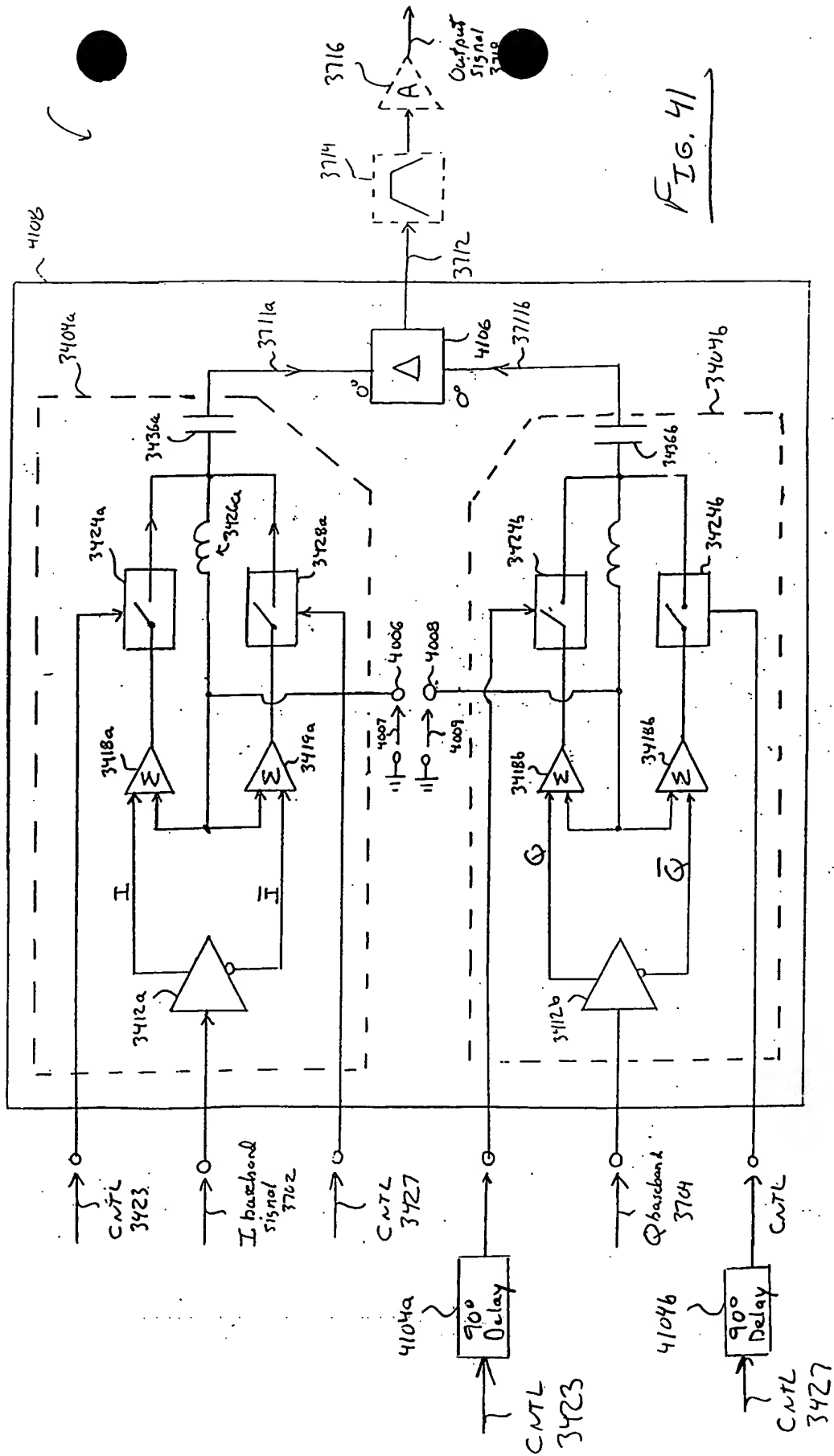


FIG. 39B



0044760 58792560

4102



DATE: 03/05/00

3720

AM
Shaped
BPSK
etc.

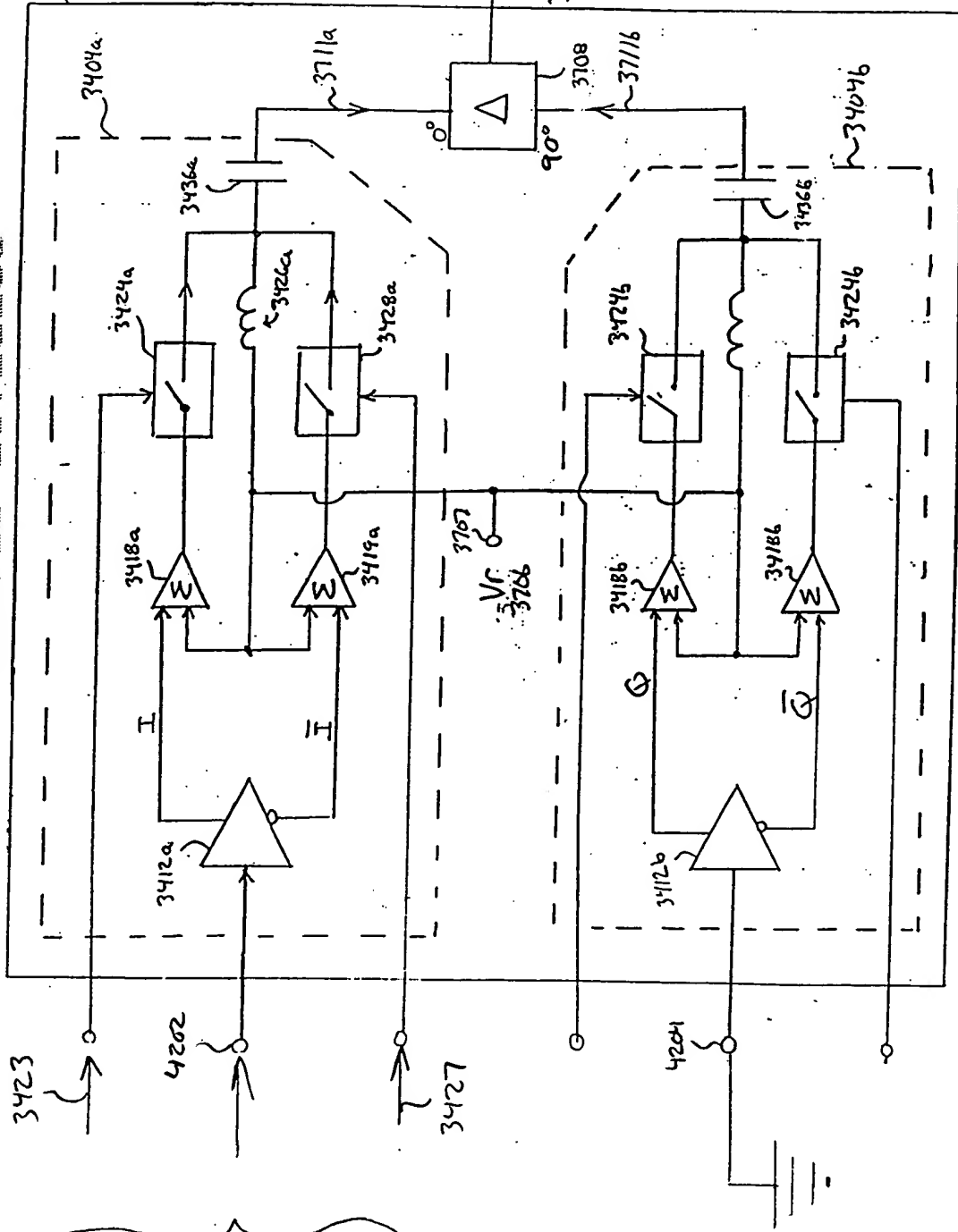


FIG. 42A

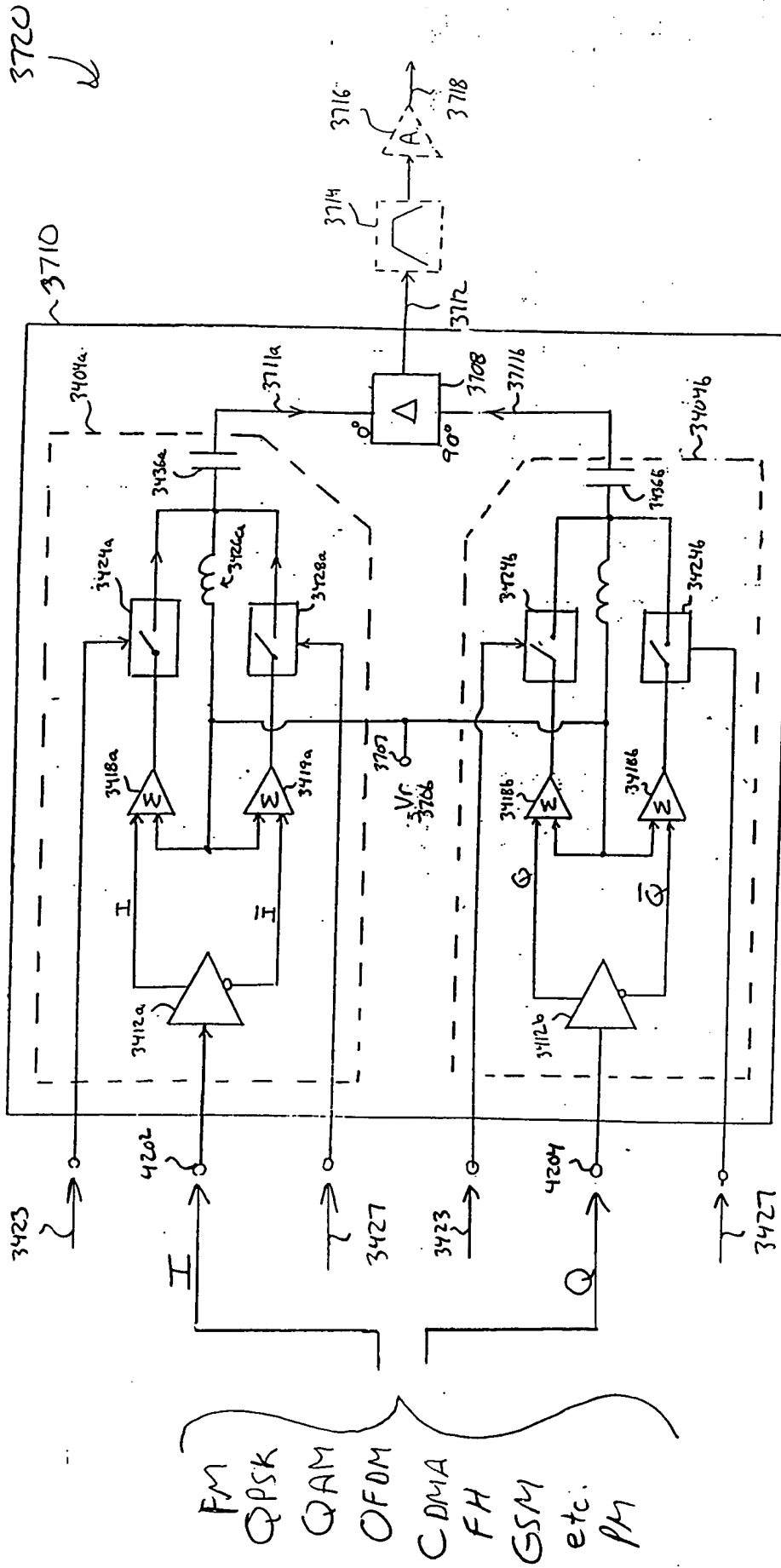


FIG. 42B

FM
QPSK
QAM
OFDM
CDMA
FH
GSM
etc.
PM

4400.

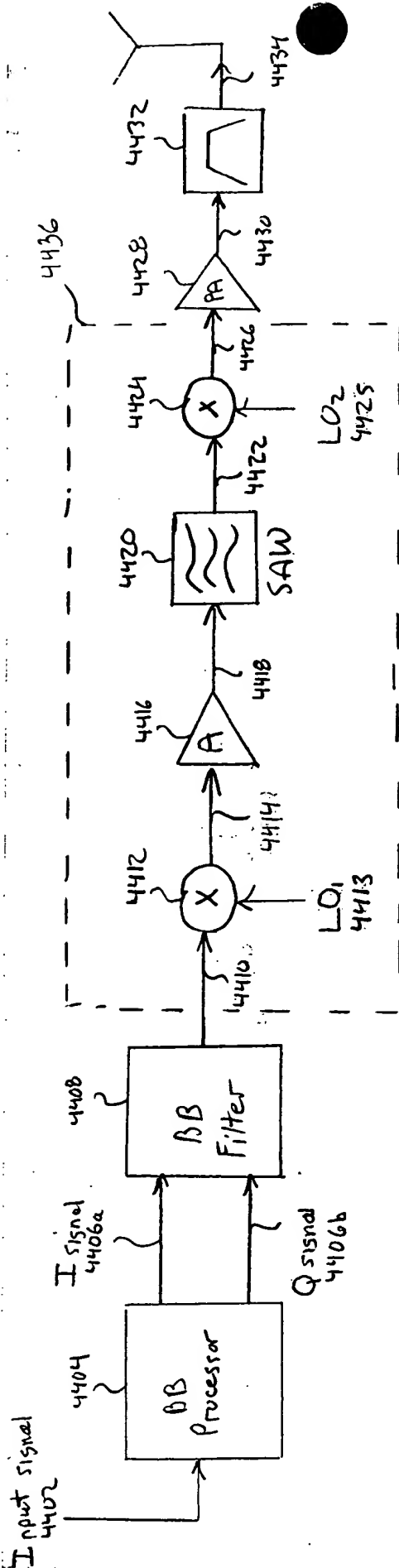


FIG. 44: Conventional Transmitter

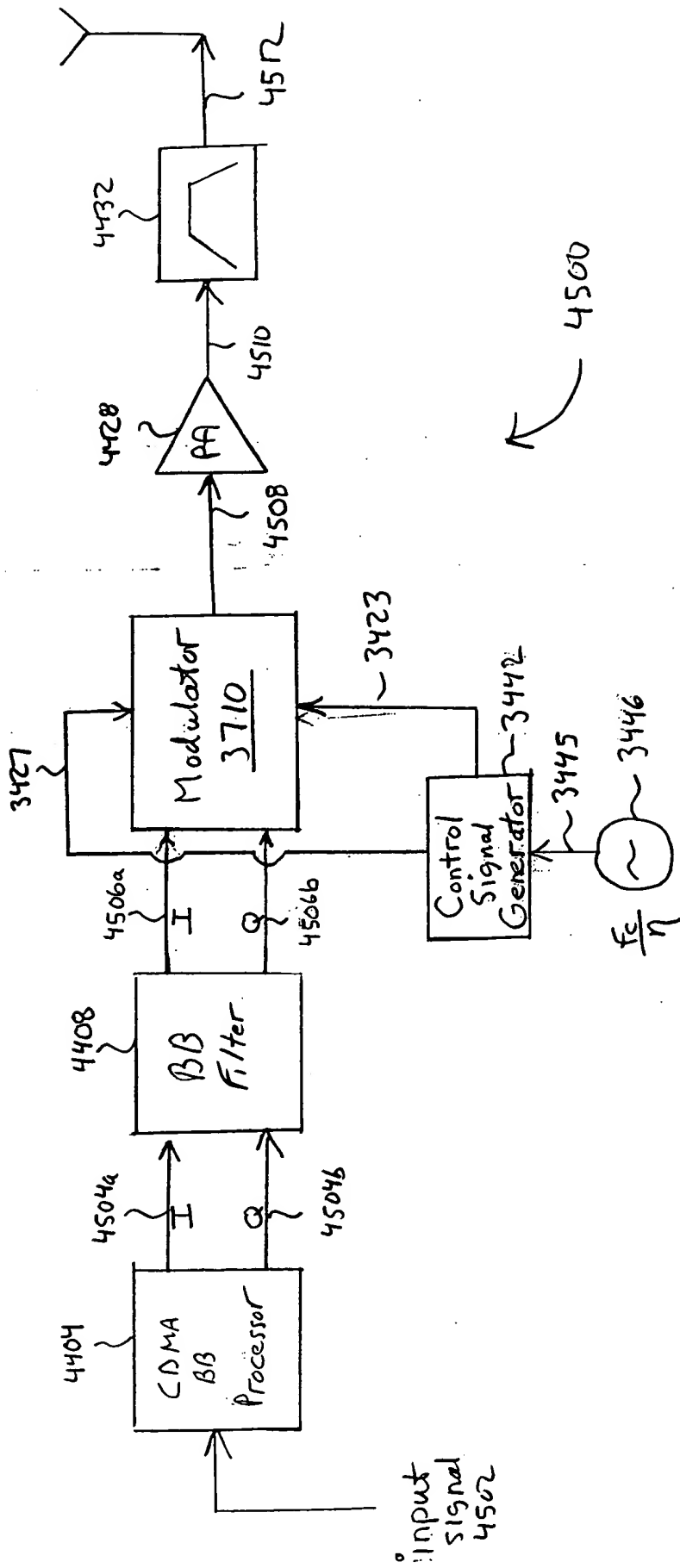
[illegible]

Fig. 45A: CDMA Transmitter

0909400 094400

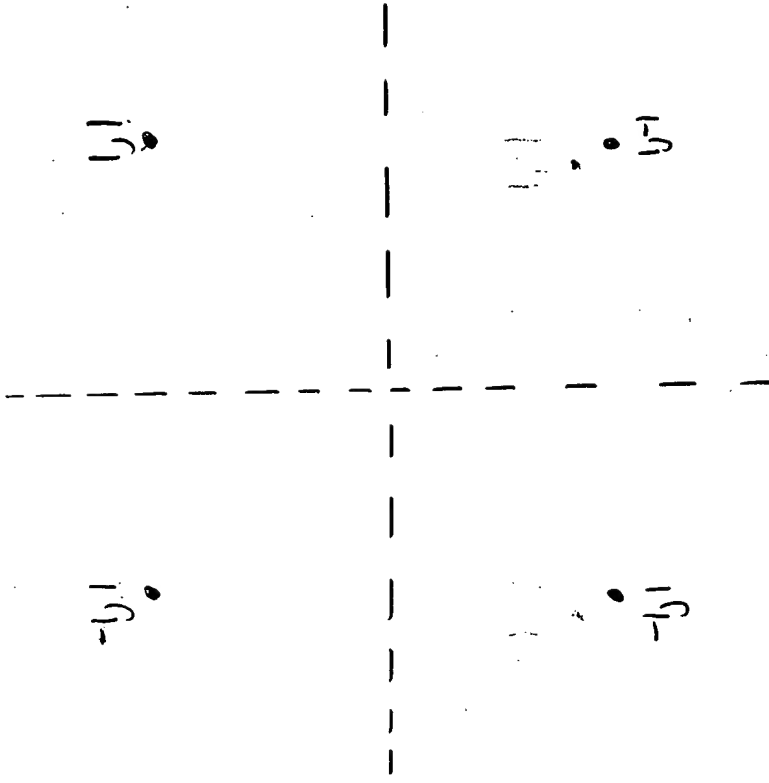


Fig. 450

42-381 50 SHEETS EYE-GLASS SQUARE
42-382 100 SHEETS EYE-GLASS SQUARE
42-383 200 SHEETS EYE-GLASS SQUARE
42-384 100 RECTO ED WHITE 5 SQUARE
42-385 200 RECTO ED WHITE 5 SQUARE
Made in U. S. A.



4508

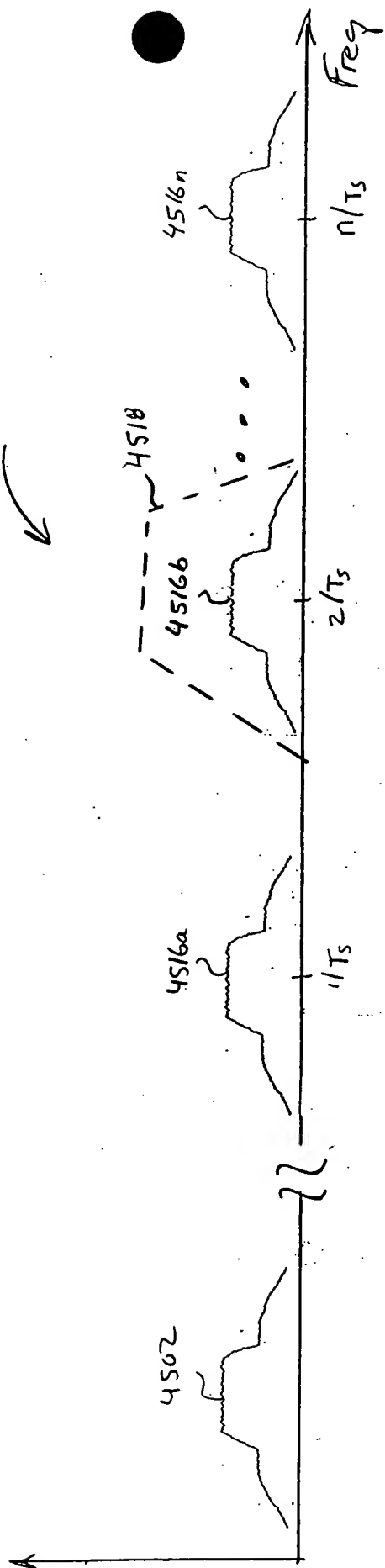


FIG. 45E

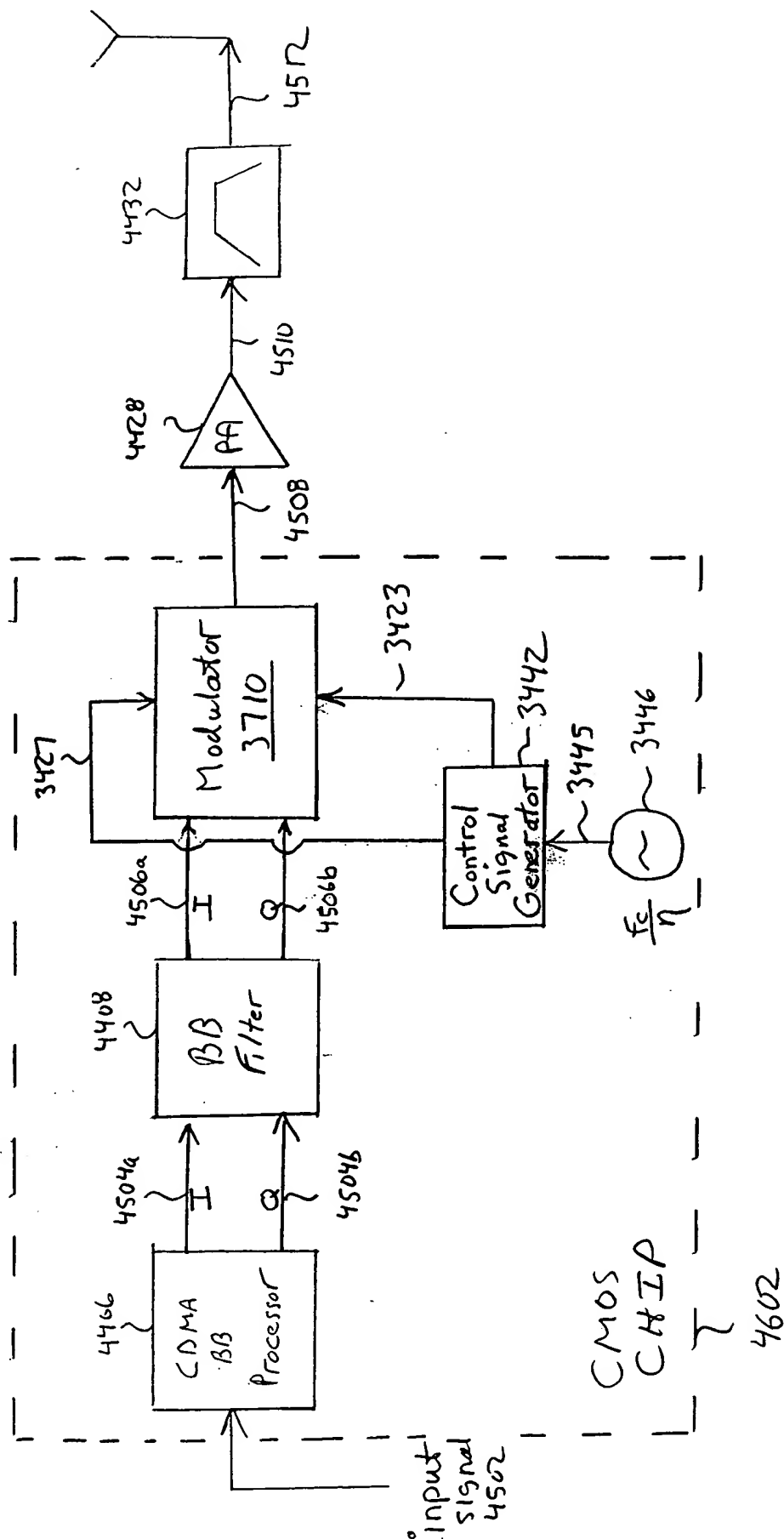
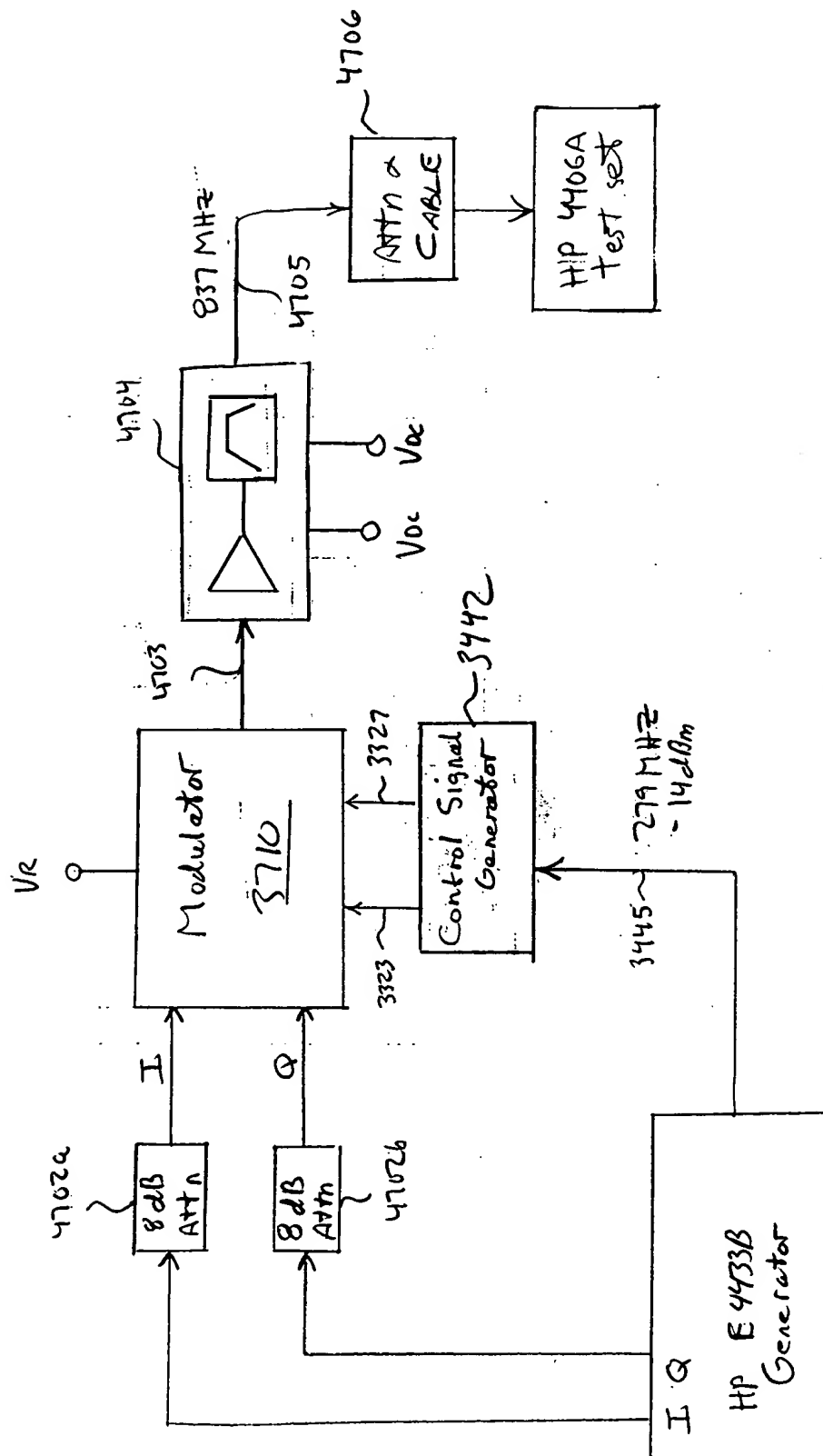


FIG. 46: CDMA CMOS CHIP



FI 6.47

09523435 034400

Base Station

4802

RHO	0.9970
EVM	5.51%
PHASE ERROR	1.80°
MAGNITUDE ERROR	4.53%
CARRIER INSERTION	-37.91 dB
PA POWER OUT	28.06 dBm

FIG. 48

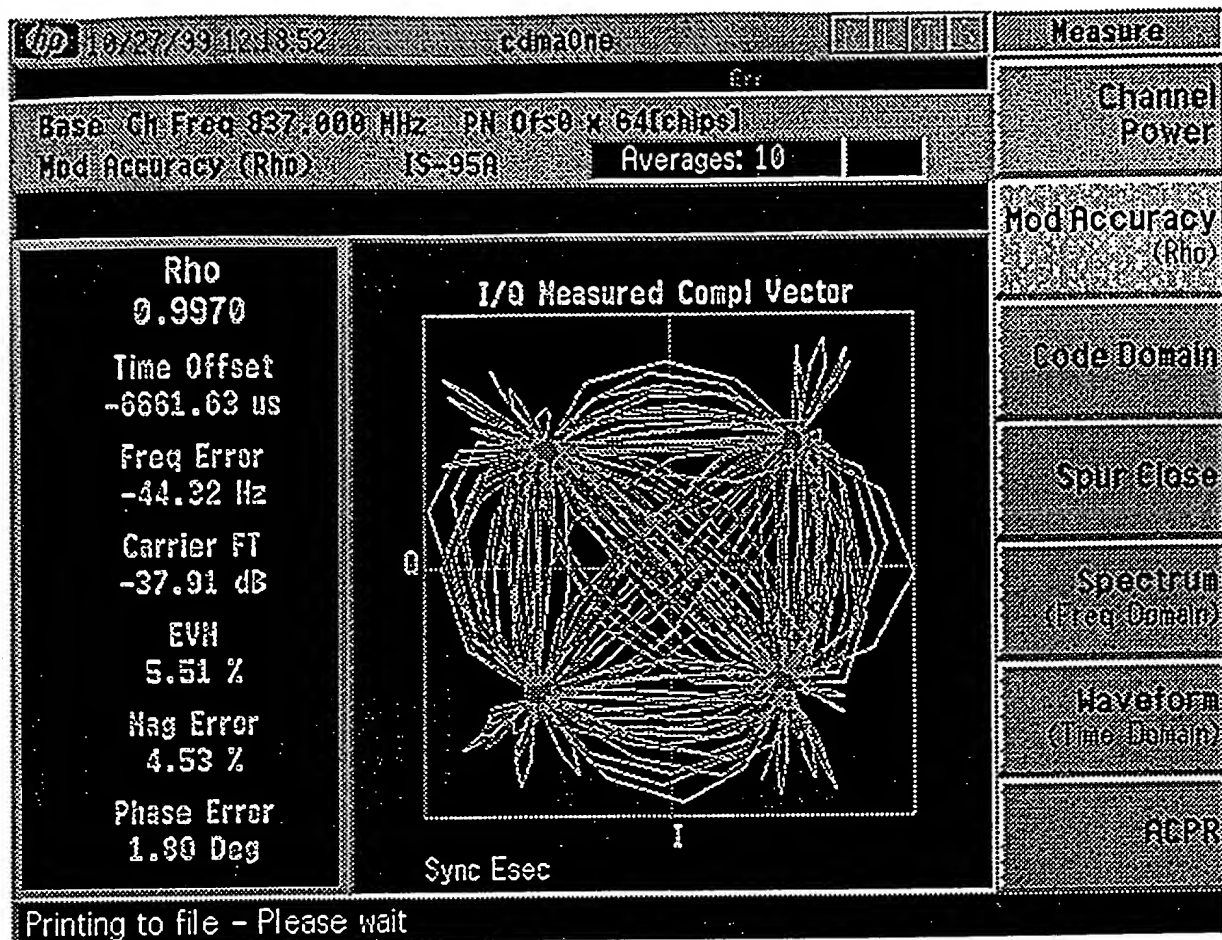
FREQUENCY (MHz) (Mobile Station)

	<i>LOW</i>	<i>MIDDLE</i>	<i>HIGH</i>
RHO	0.9892	0.9969	0.9892
EVM	10.39%	5.54%	10.39%
PHASE ERROR	4.47°	2.24°	4.08°
MAGNITUDE ERROR	6.84%	4.21%	8.27%
CARRIER INSERTION	-40.15 dB	-44.58 dB	-35.27 dB
PA POWER OUT	27.36 dBm	28.11 dBm	27.55 dBm

4902

FIG. 49

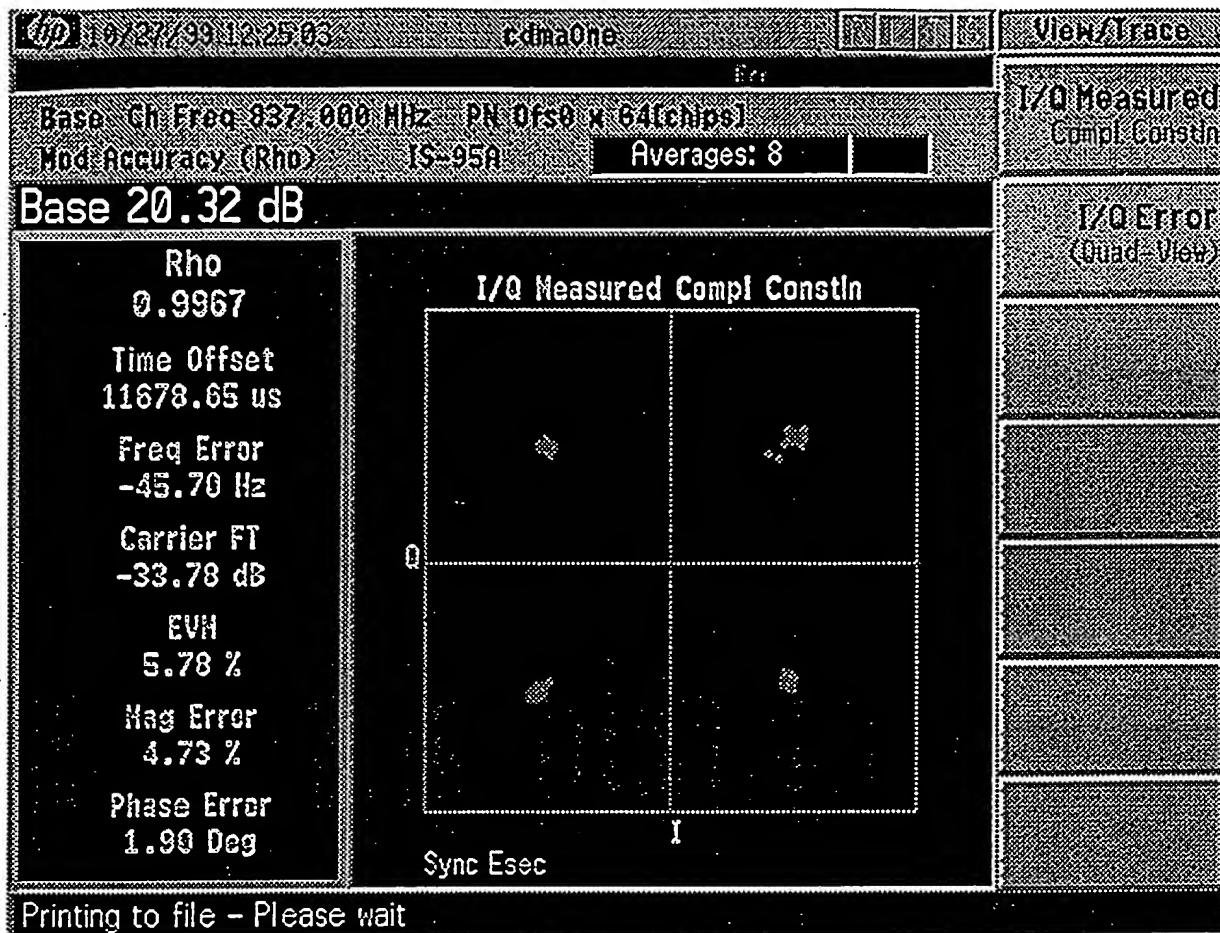
5002
↓



Base Station Constellation for Pilot Channel Test

FIG. 50

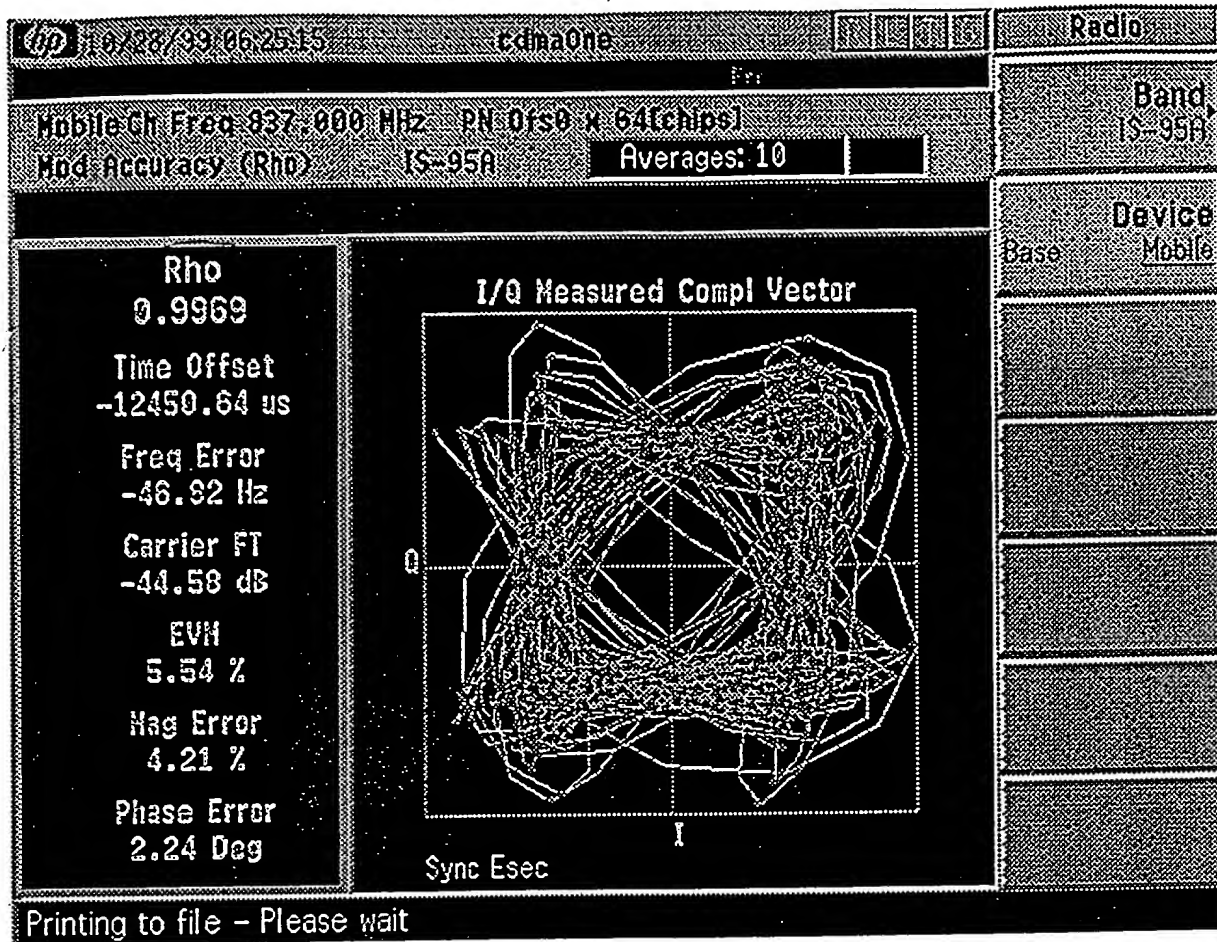
5102
↓



Base Station Sampled Constellation

FIG. 51

5262

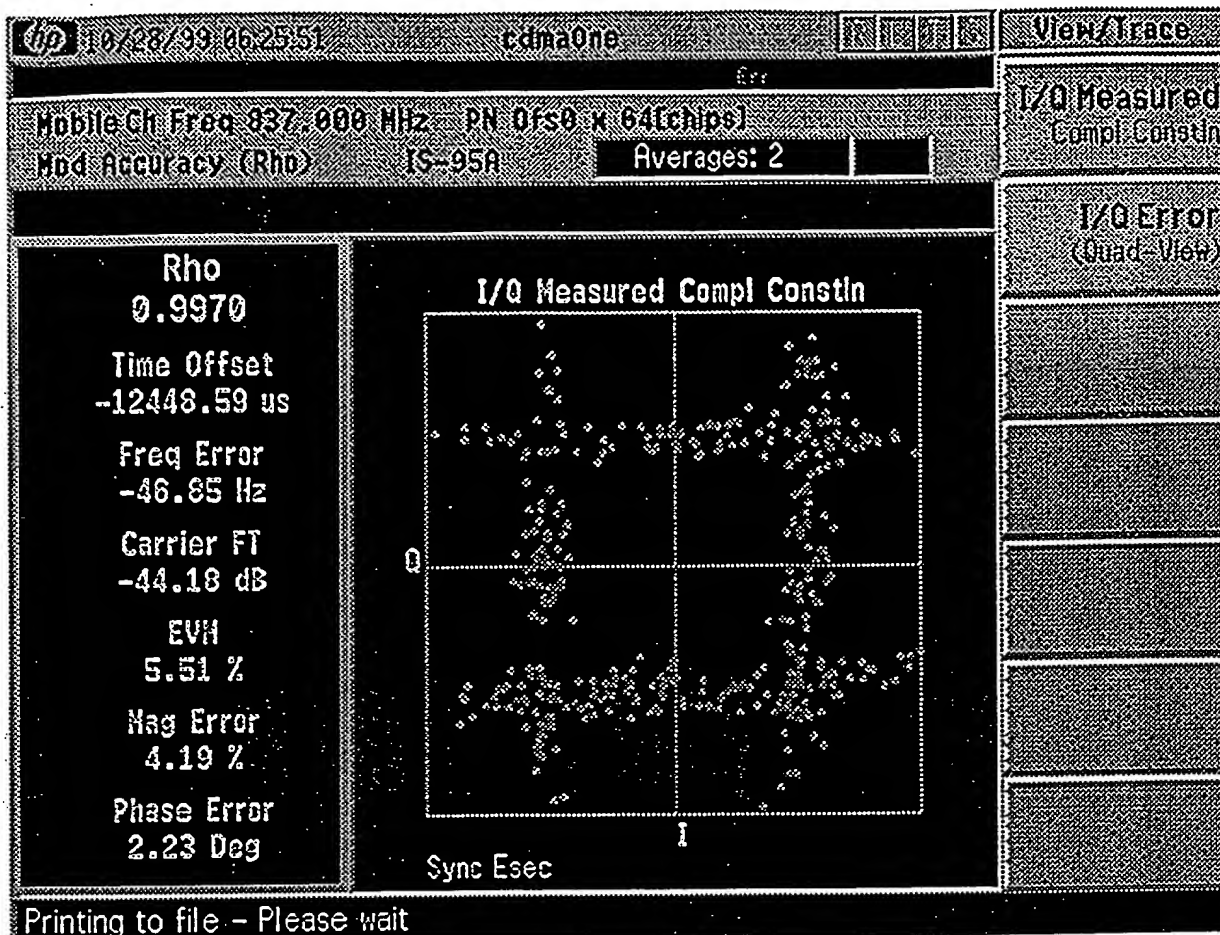


Mobile Station Constellation for Access Channel Test

FIG. 52

09525185.034100

5302
↓

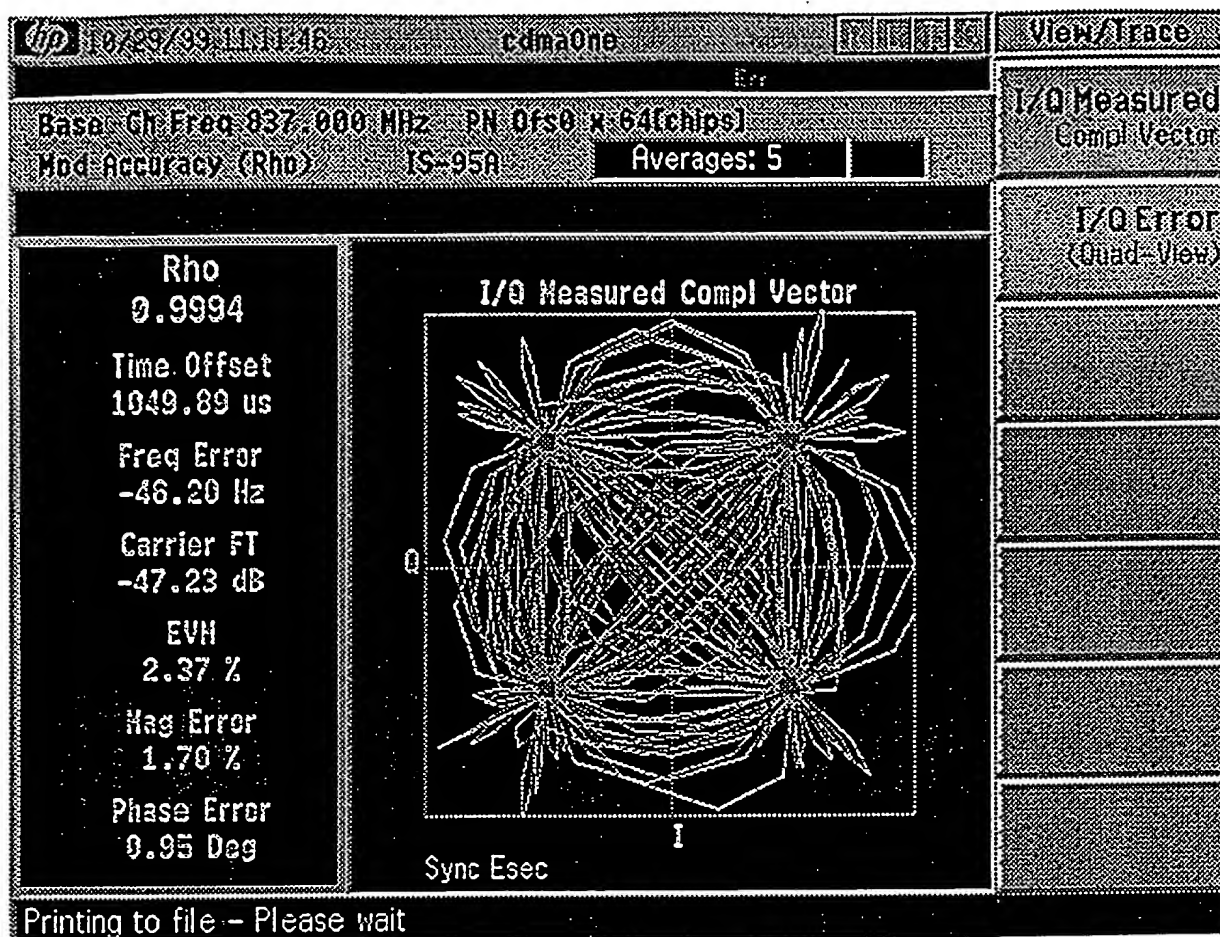


Mobile Station Sampled Constellation

FIG. 53

09525105.031100

5402
✓

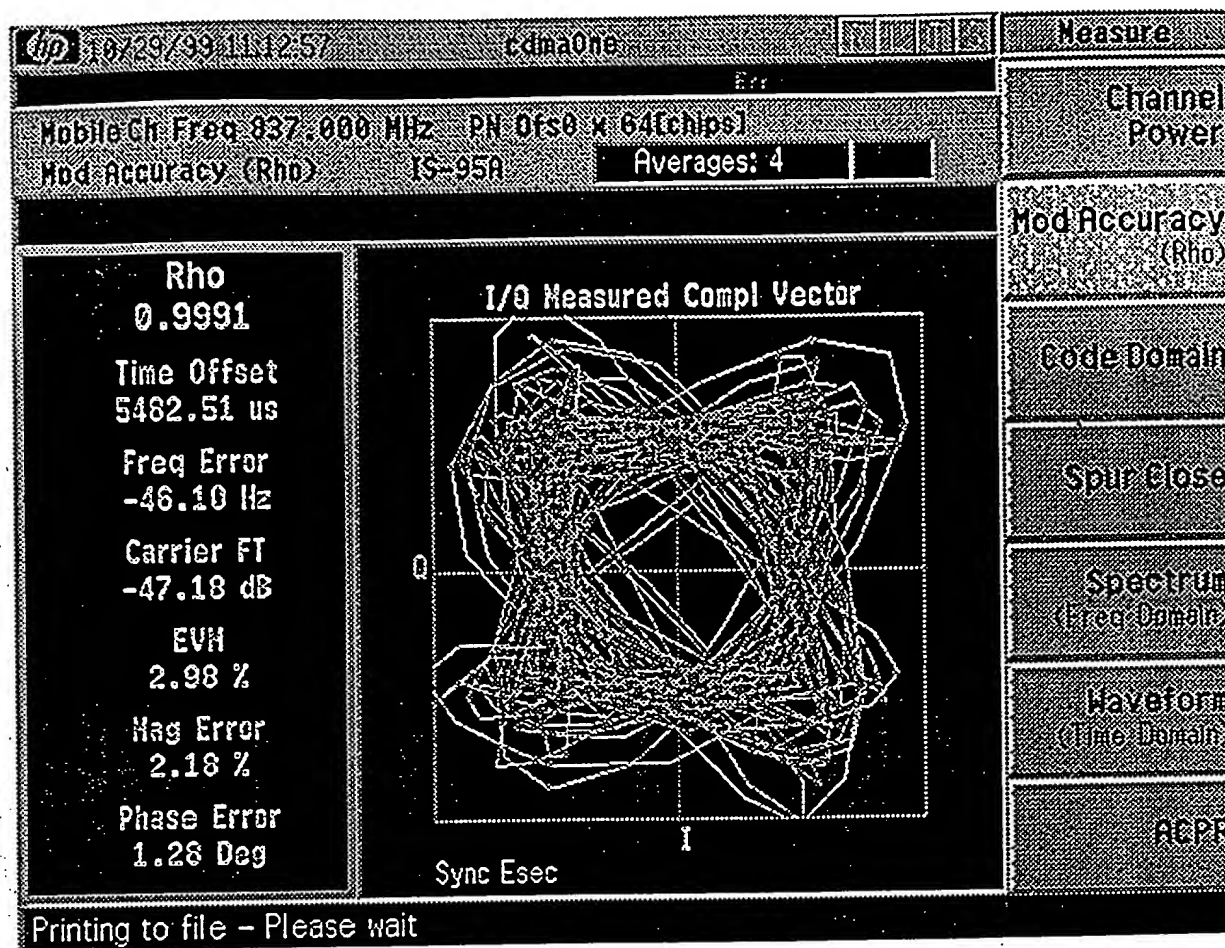


Base Station Constellation using only H/P Test Equipment

FIG. 54

09525185 034100

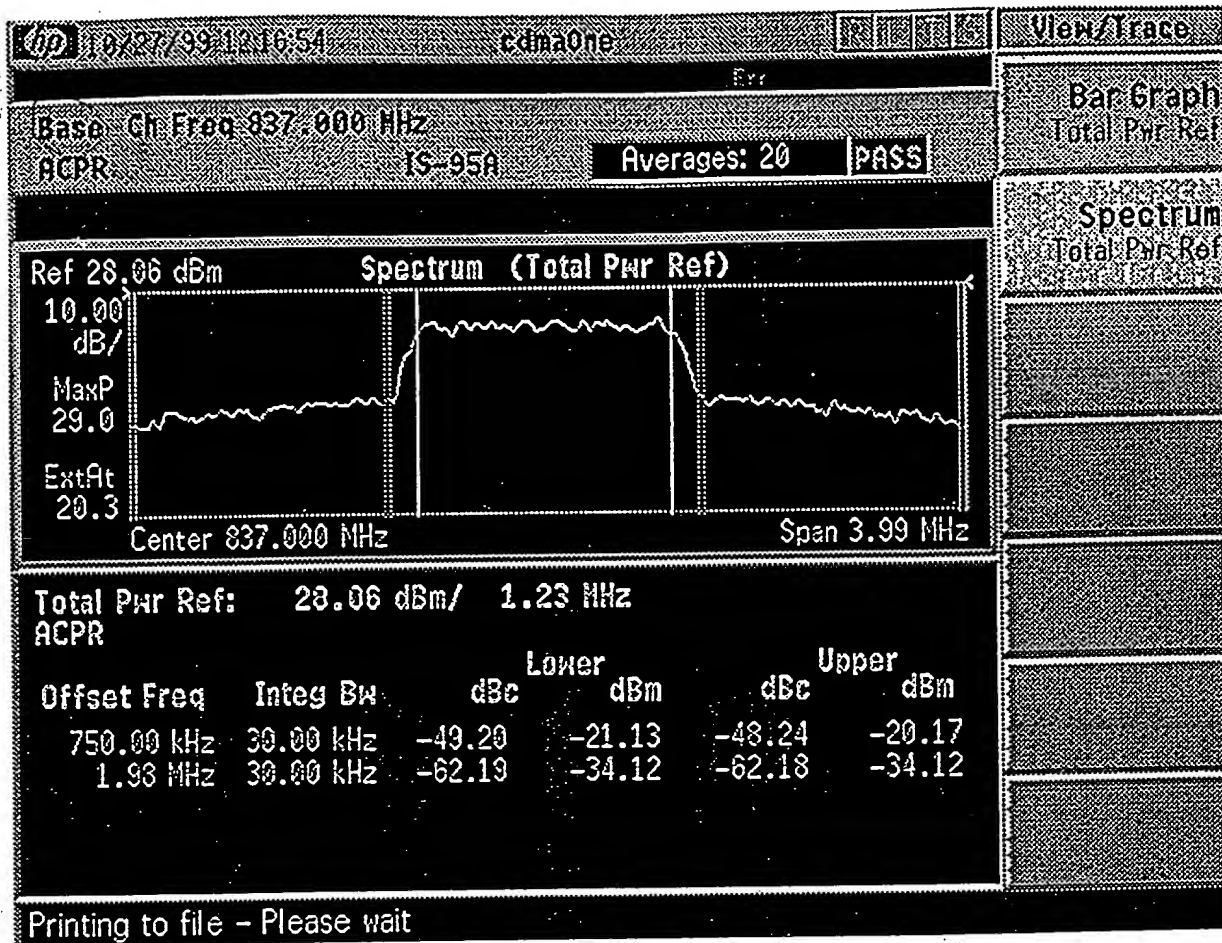
5502
↓



Mobile Constellation using only H/P Test Equipment

FIG. 55

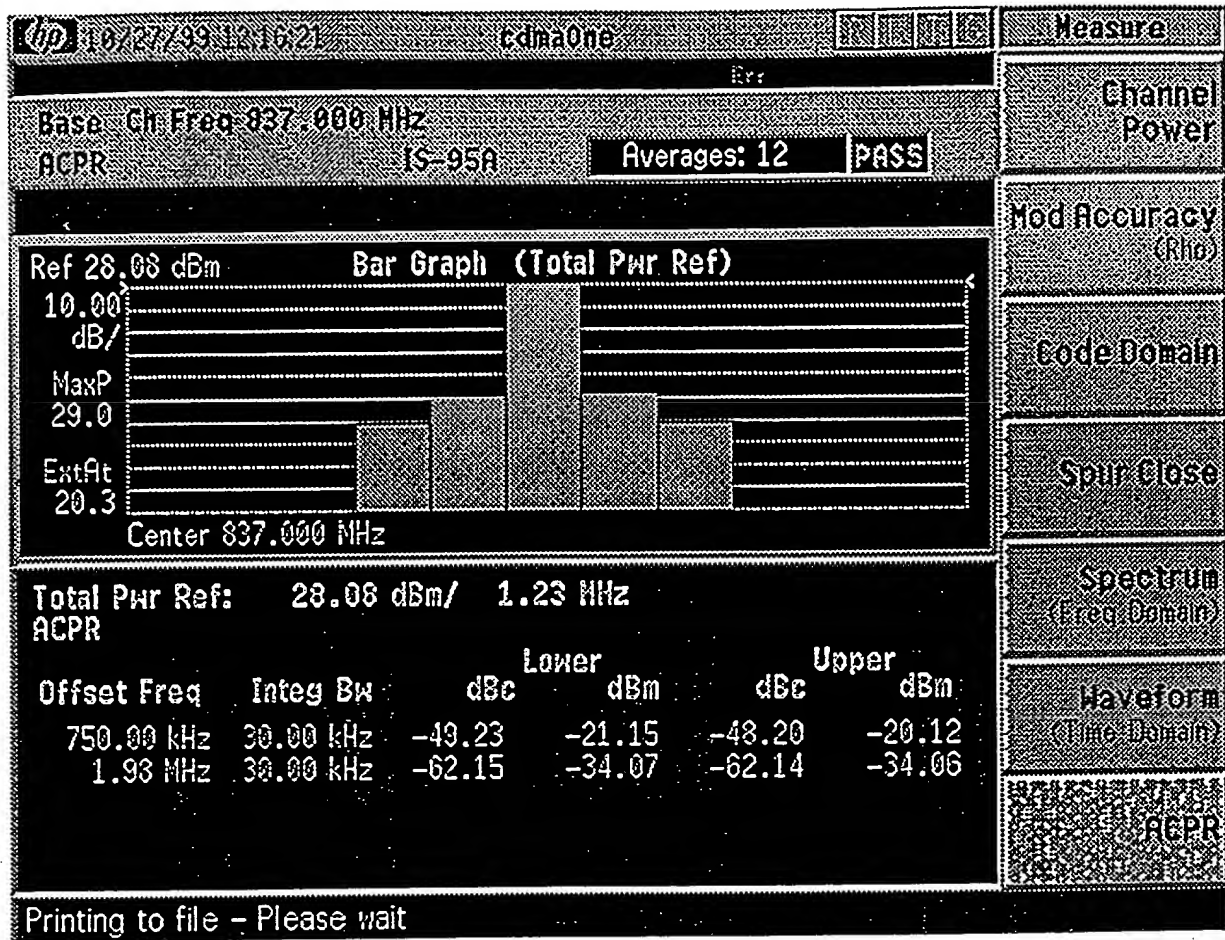
5602
↓



00525105 034100

FIG. 56

5702
↓

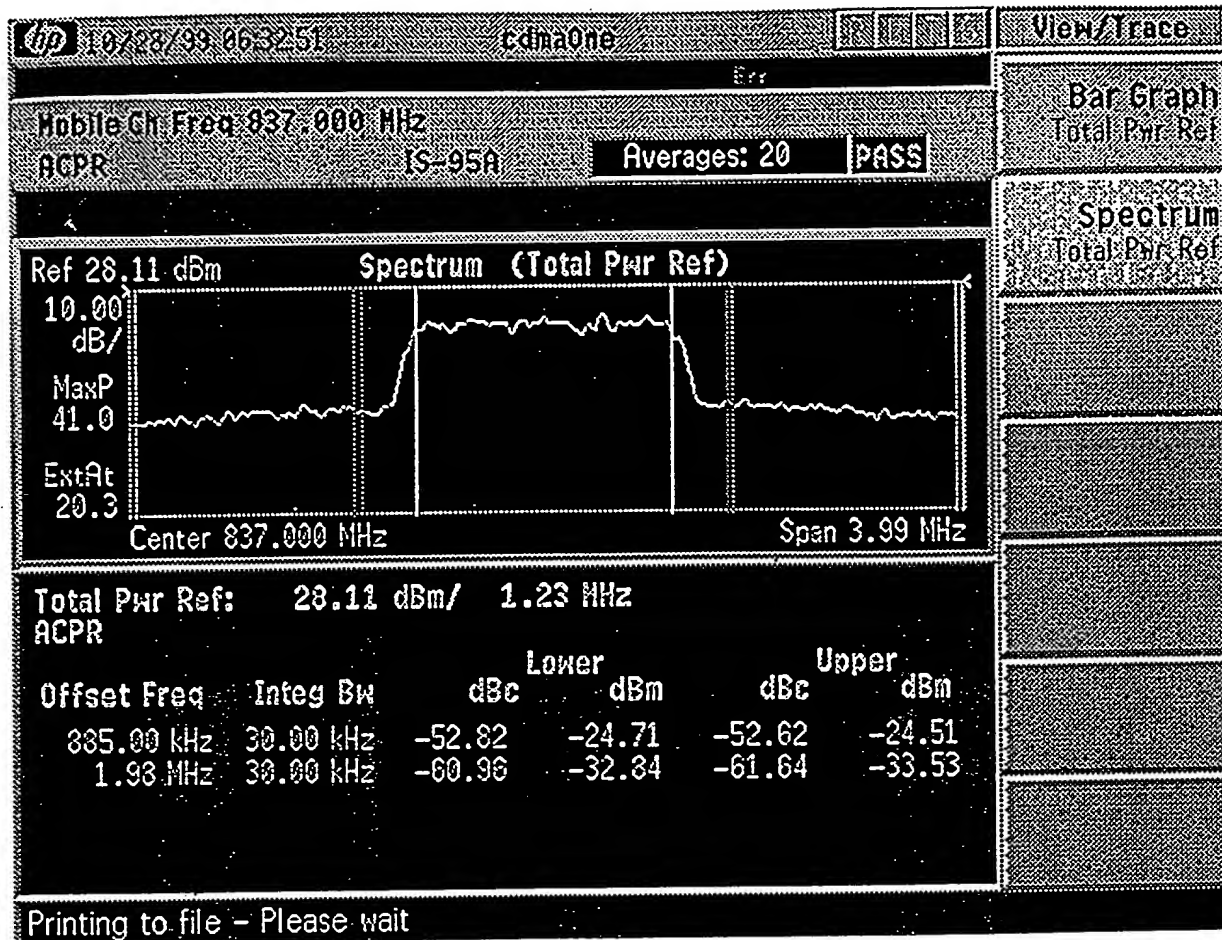


Base Station Spectral Response with Mask

FIG. 57

00525105-031100

5862



00525185-034400

FIG. 58

5902

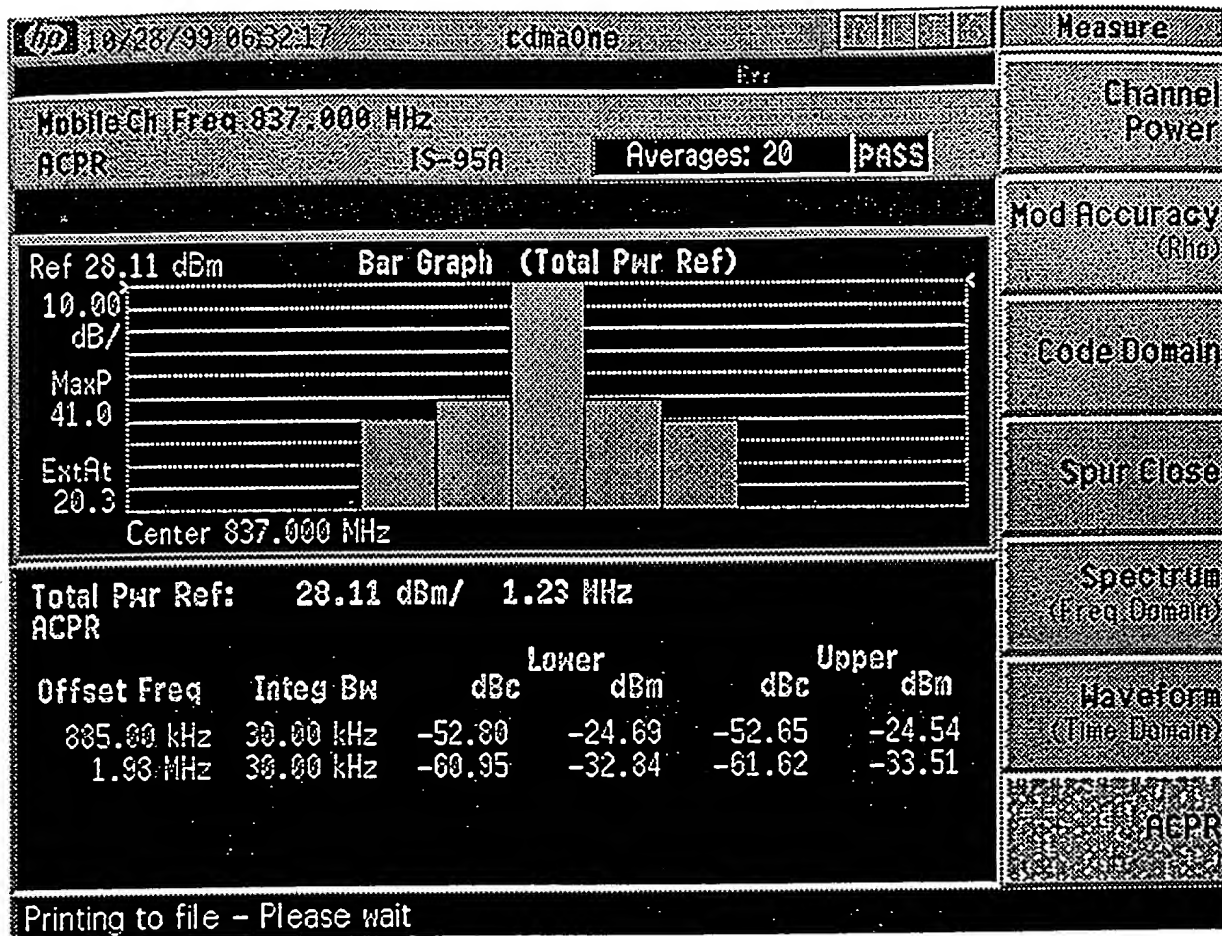
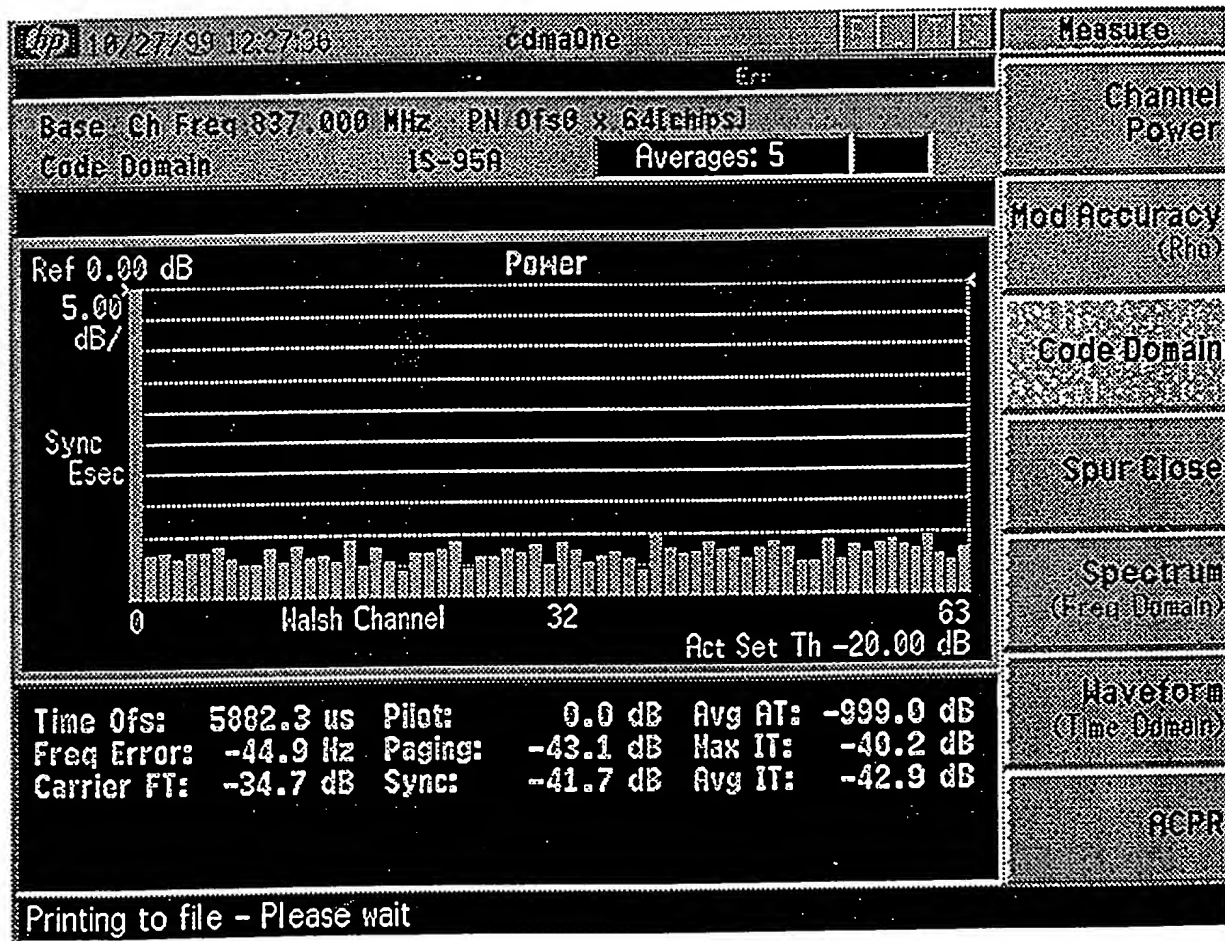


Figure 3.2-2 Mobile Station Spectral Response with Mask

FIG. 59

6002
↓

00525195 034400



CDMA Crosstalk

FIG. 60A

Sequence for IQ Input Level Variance

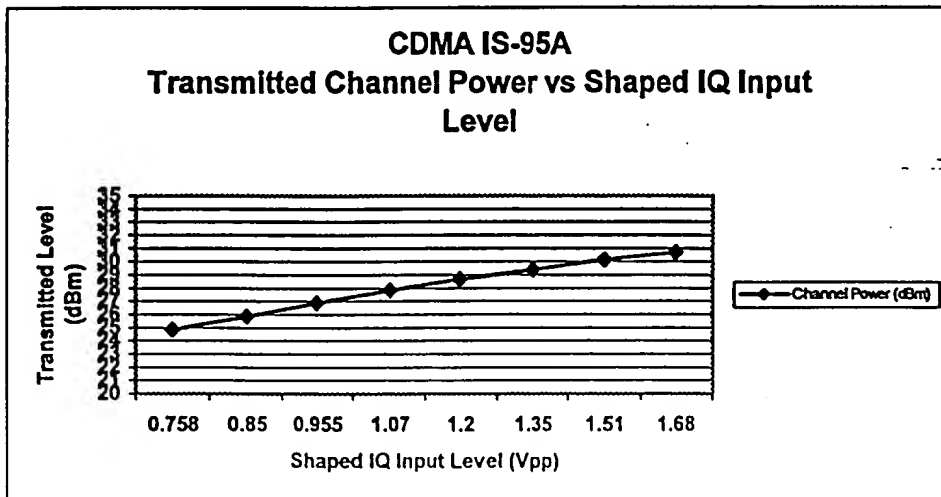
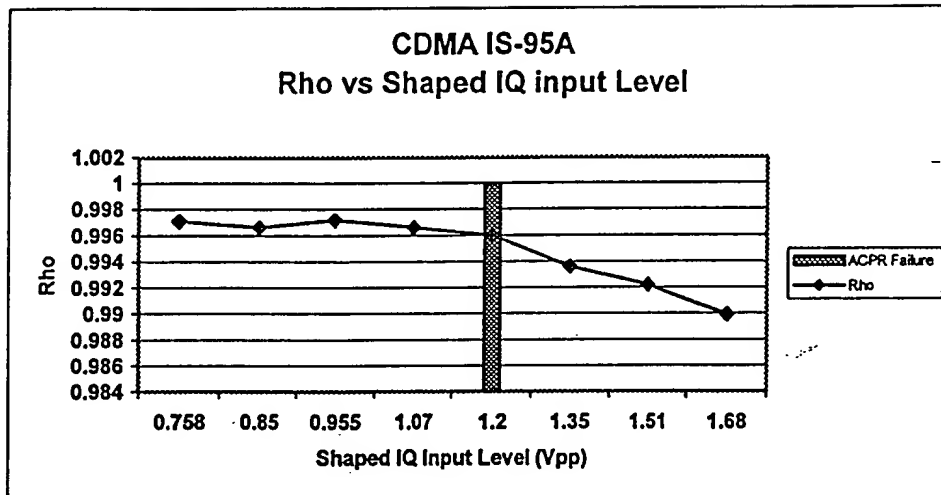


FIG. 60D

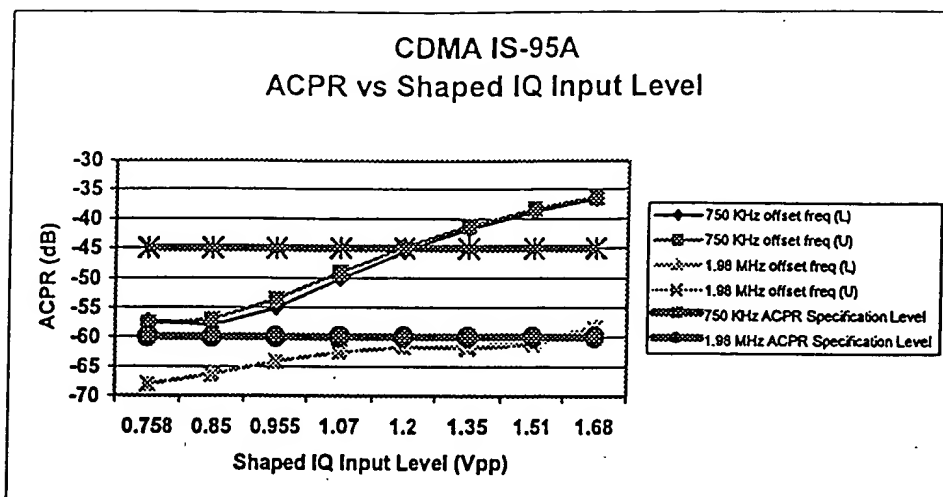


FIG. 60E

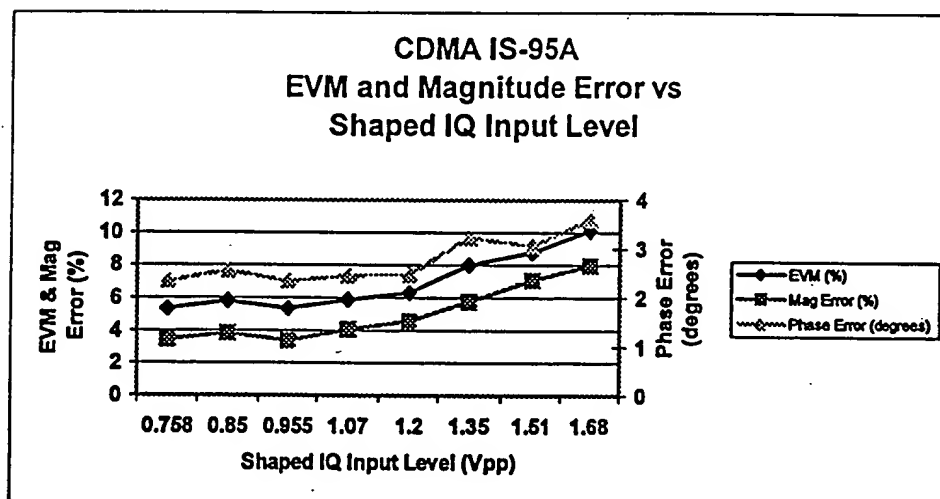
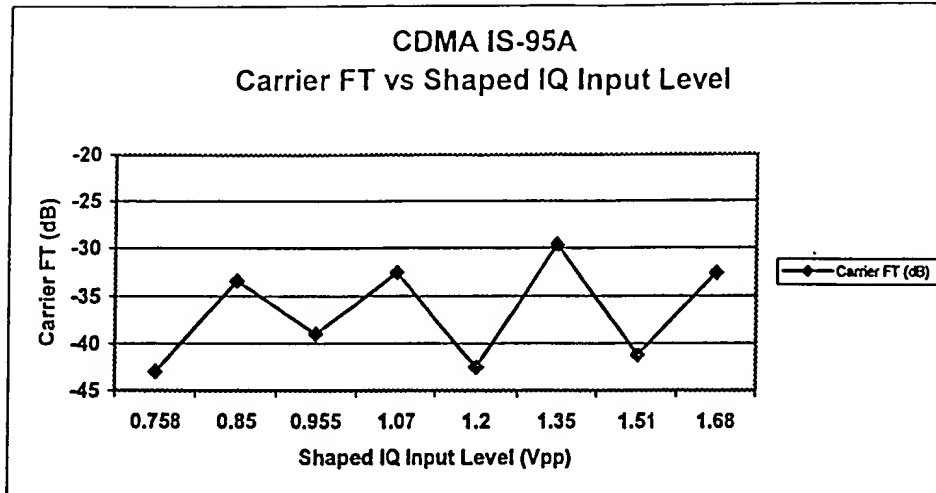


FIG. 60F



Sequence for LO Variance

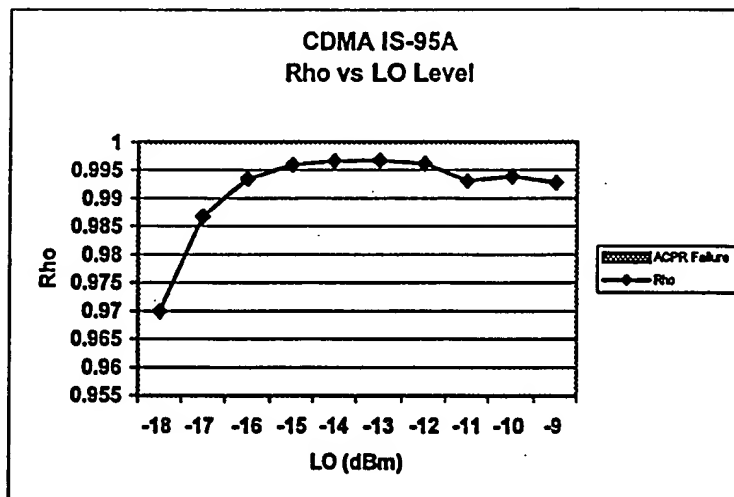


FIG. 60G

FIG. 604

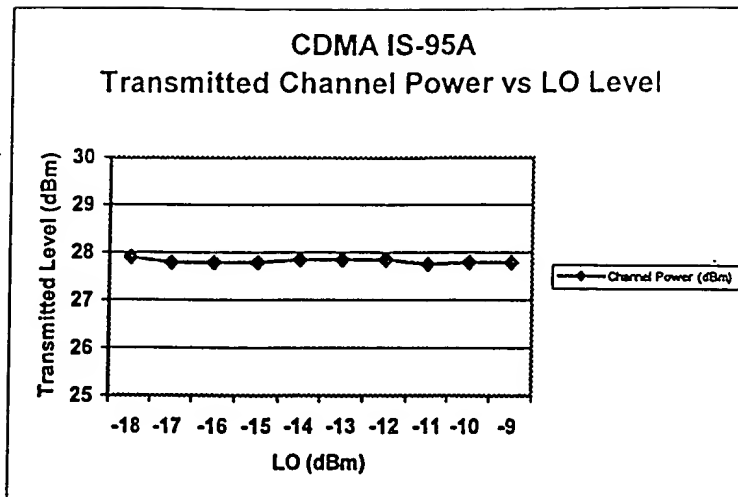


FIG. 601

FIG. 601

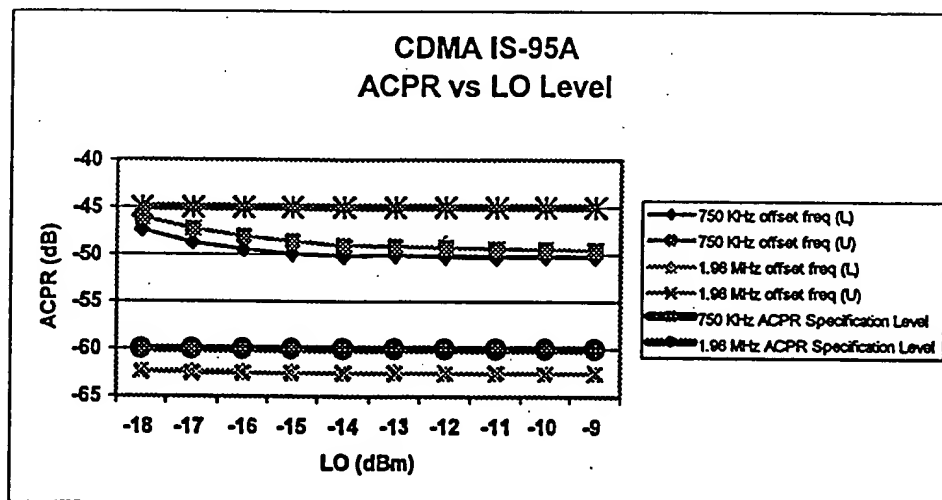


FIG. 60J

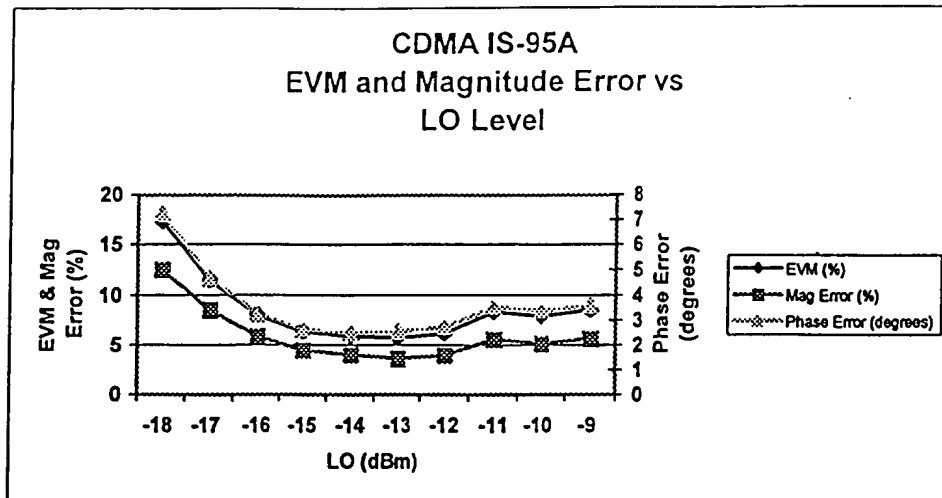


FIG. 60K

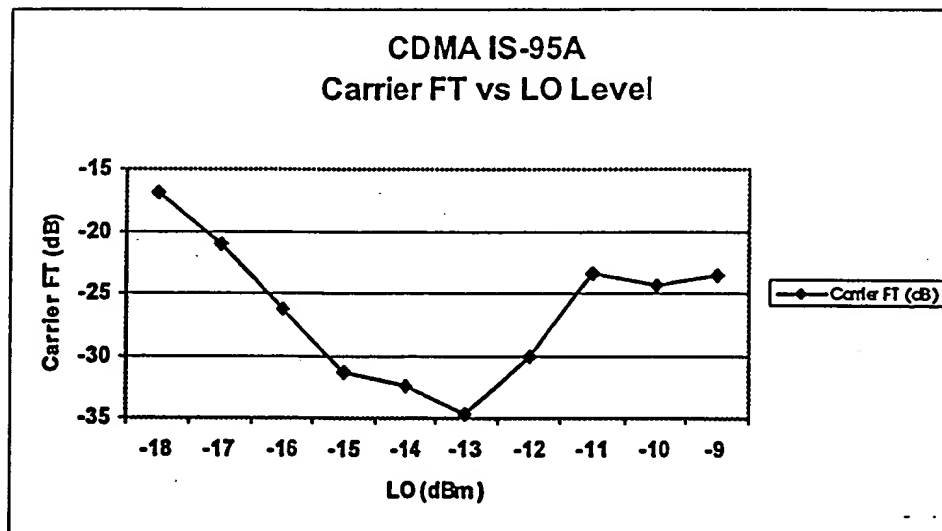


FIG. 60L

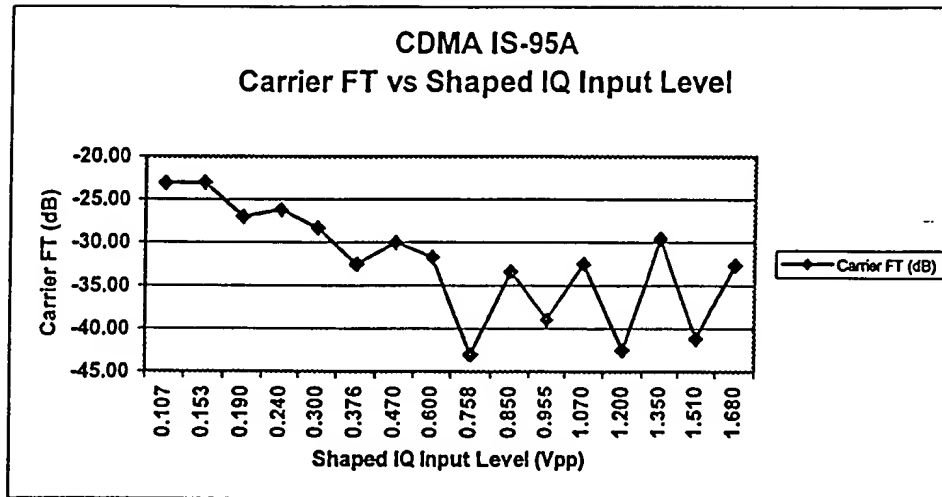


FIG. 60M

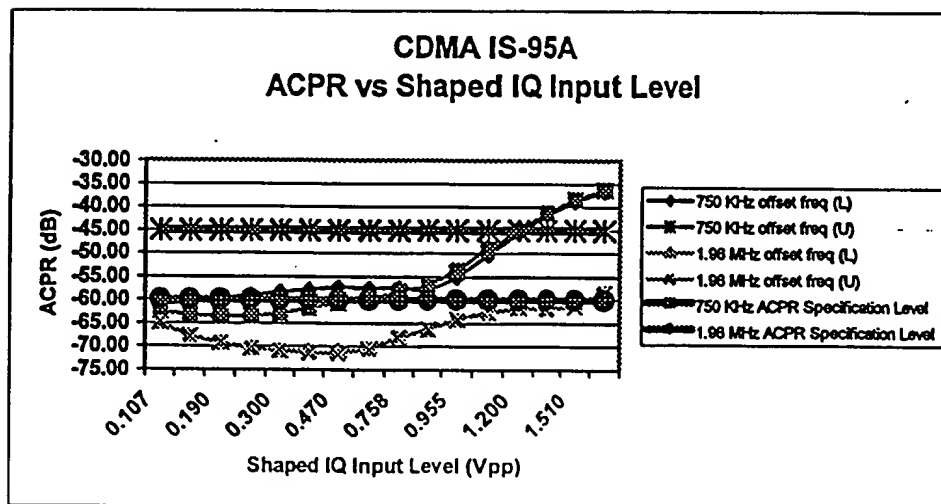


FIG. 60N

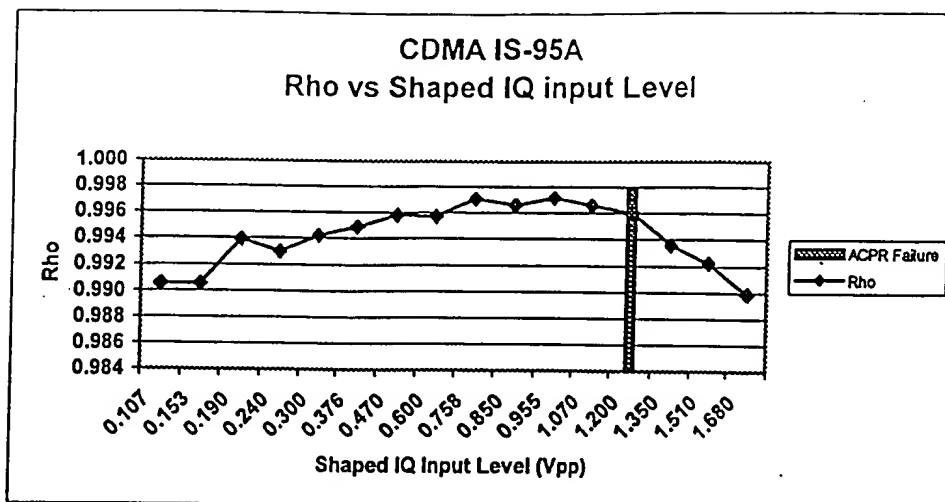
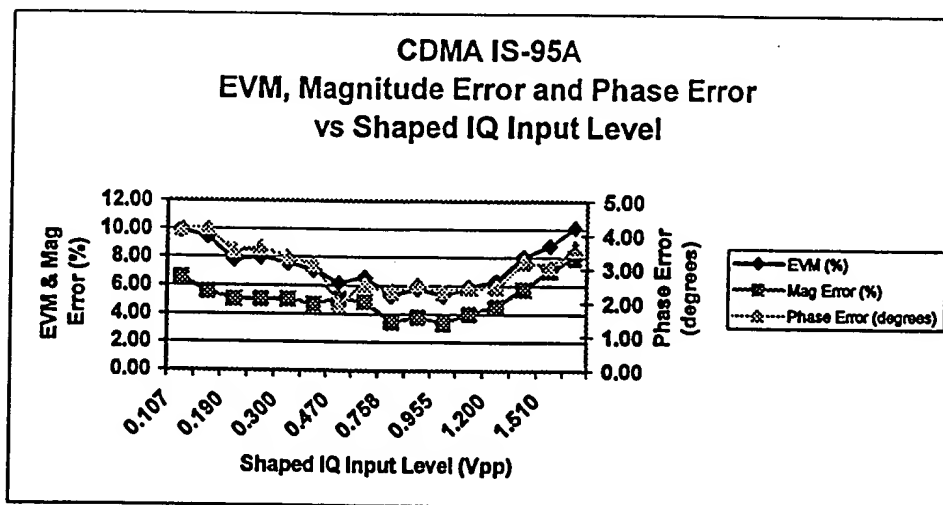


FIG. 60O



Sequence for IQ Input Level Variance

FIG. 60P

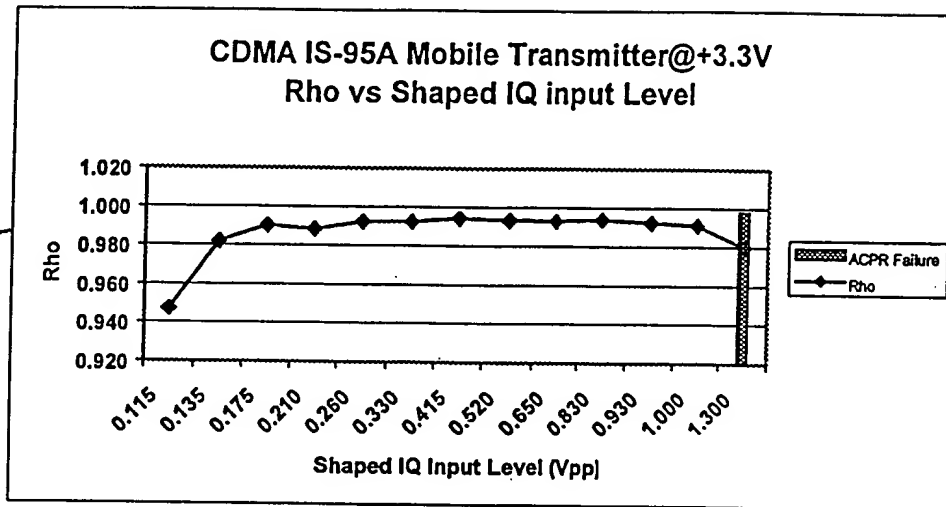


FIG. 60Q

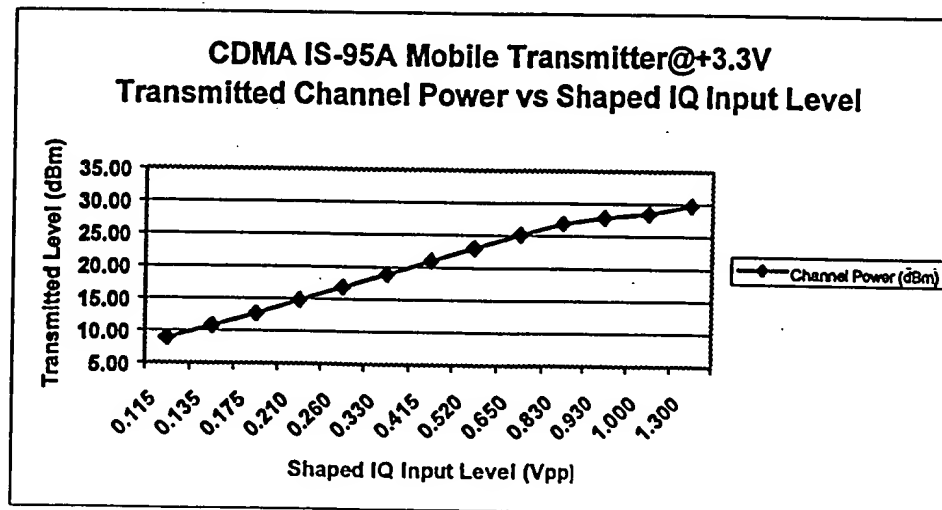


FIG. 60R

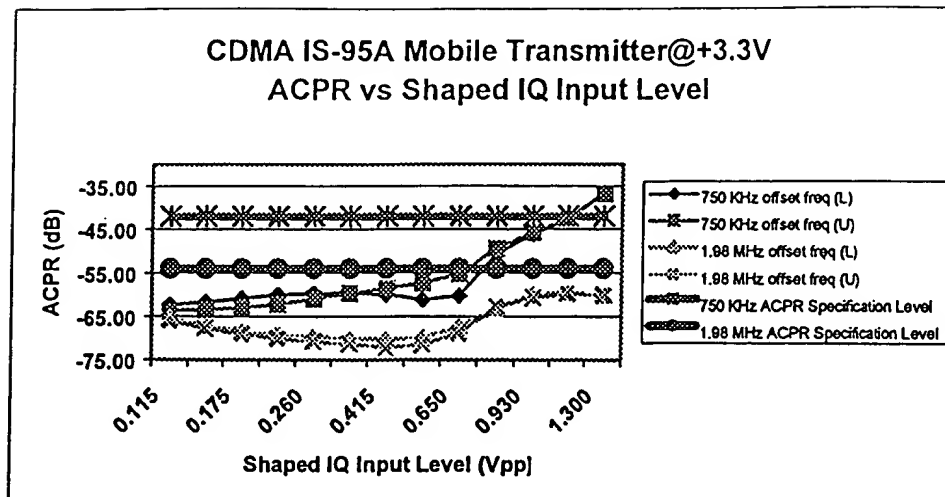


FIG. 60S

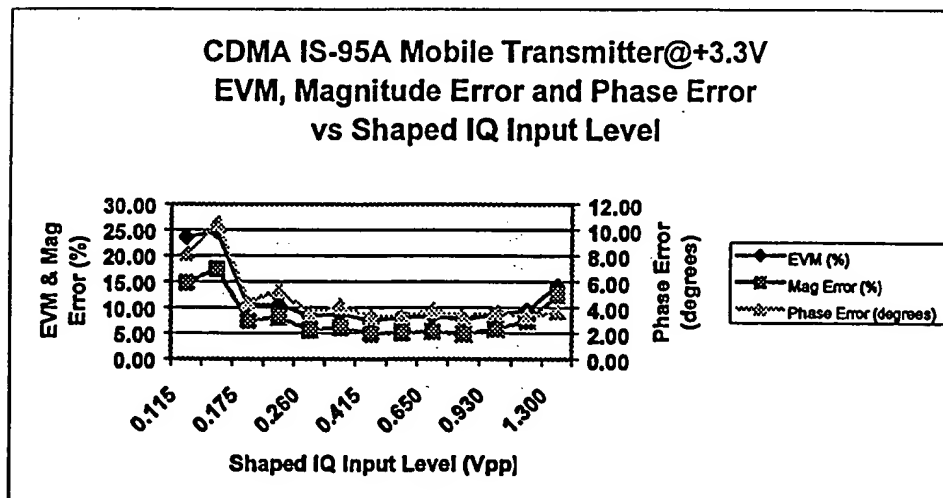


FIG. 60T

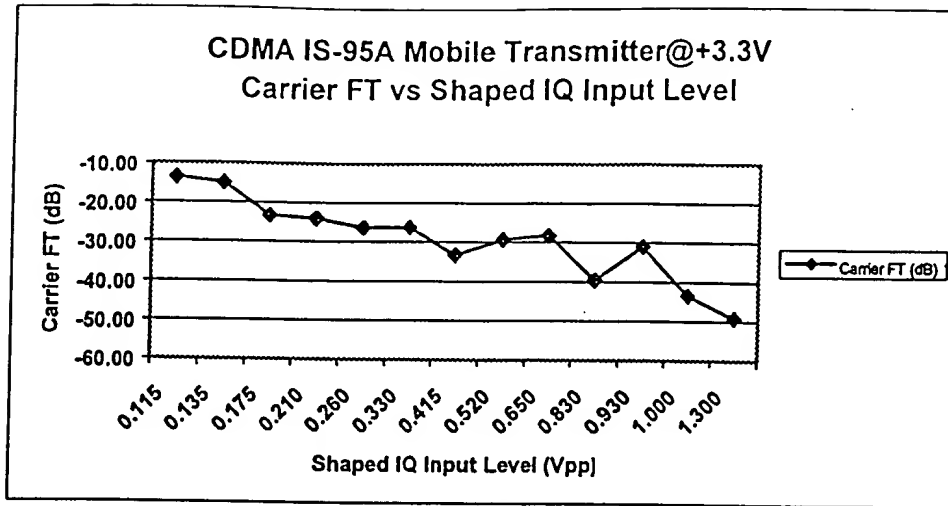


Figure 3.6-5

Sequence for LO Variance

FIG. 60U

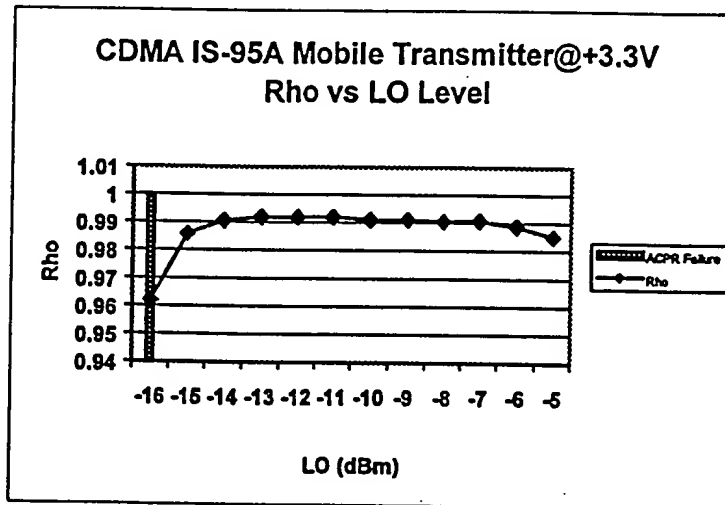


Figure 3.6-6

Q: Did you see him go back to the car?
A: Yes.
Q: And he got out of the car?
A: Yes.
Q: And he went back to the car?
A: Yes.
Q: And he got out of the car?
A: Yes.
Q: And he went back to the car?
A: Yes.

Fig. 60w

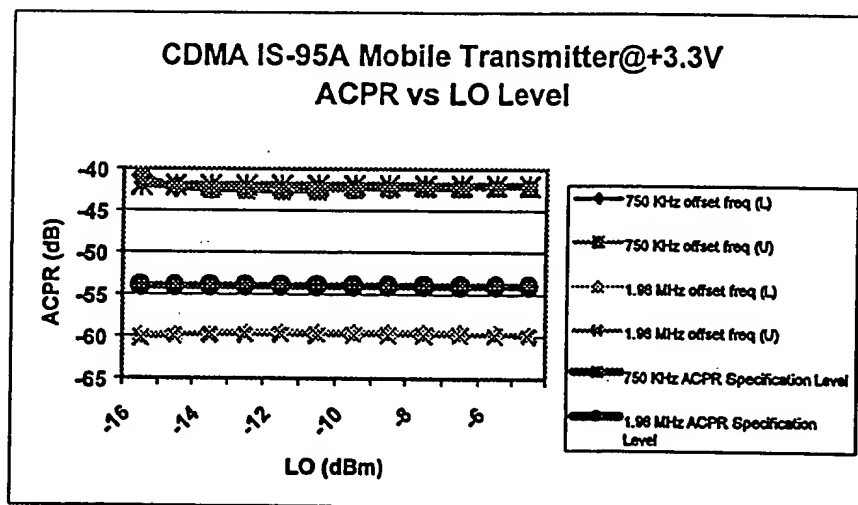
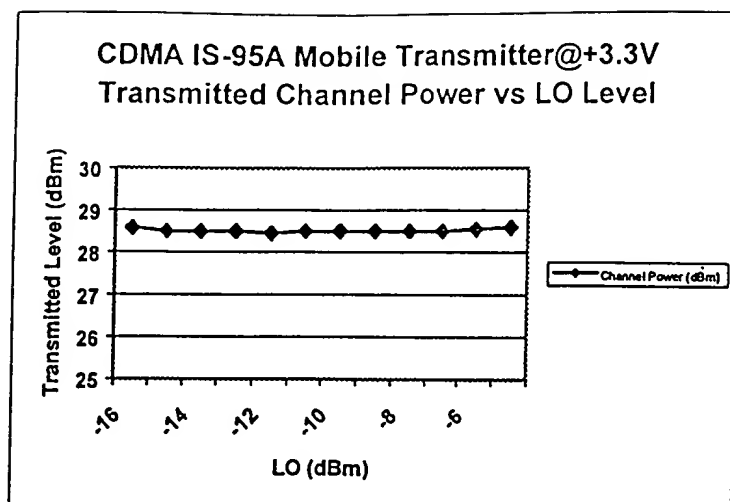
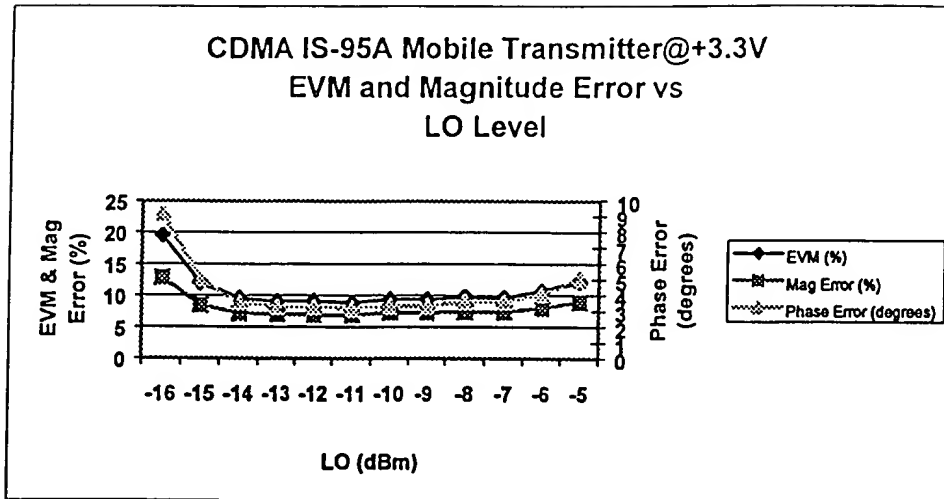
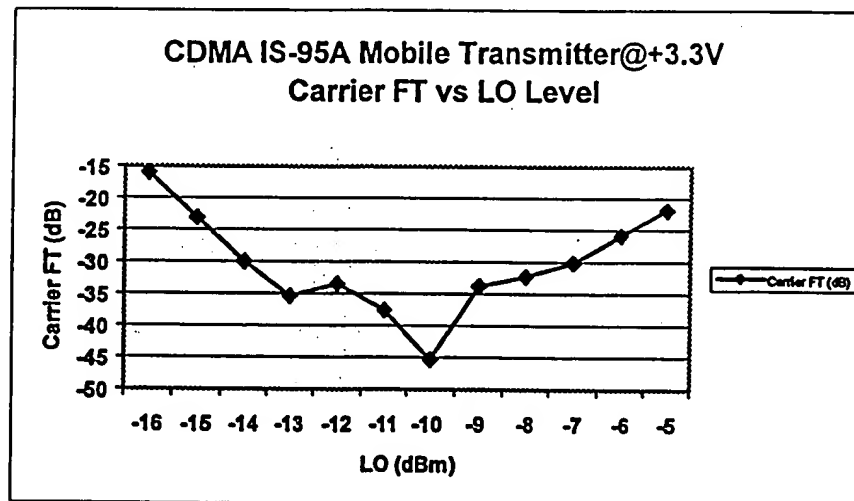


FIG. 60X



CDMA IS-95A Mobile Transmitter@+3.3V

FIG. 60Y



09525405 "031400

<i>Quantity</i>	<i>Description</i>	<i>Voltage</i>	<i>Total Current</i>	<i>Power</i>
2	D2D Cores	3.3	4mA	13.2mW
2	Baseband Interface Circuits with/BW Limit	3.3	6mA	21.8mW
1	Clock Circuit	3.3	5mA	20.0mW
			<i>Sub Total</i>	54.0mW

FIG. 602

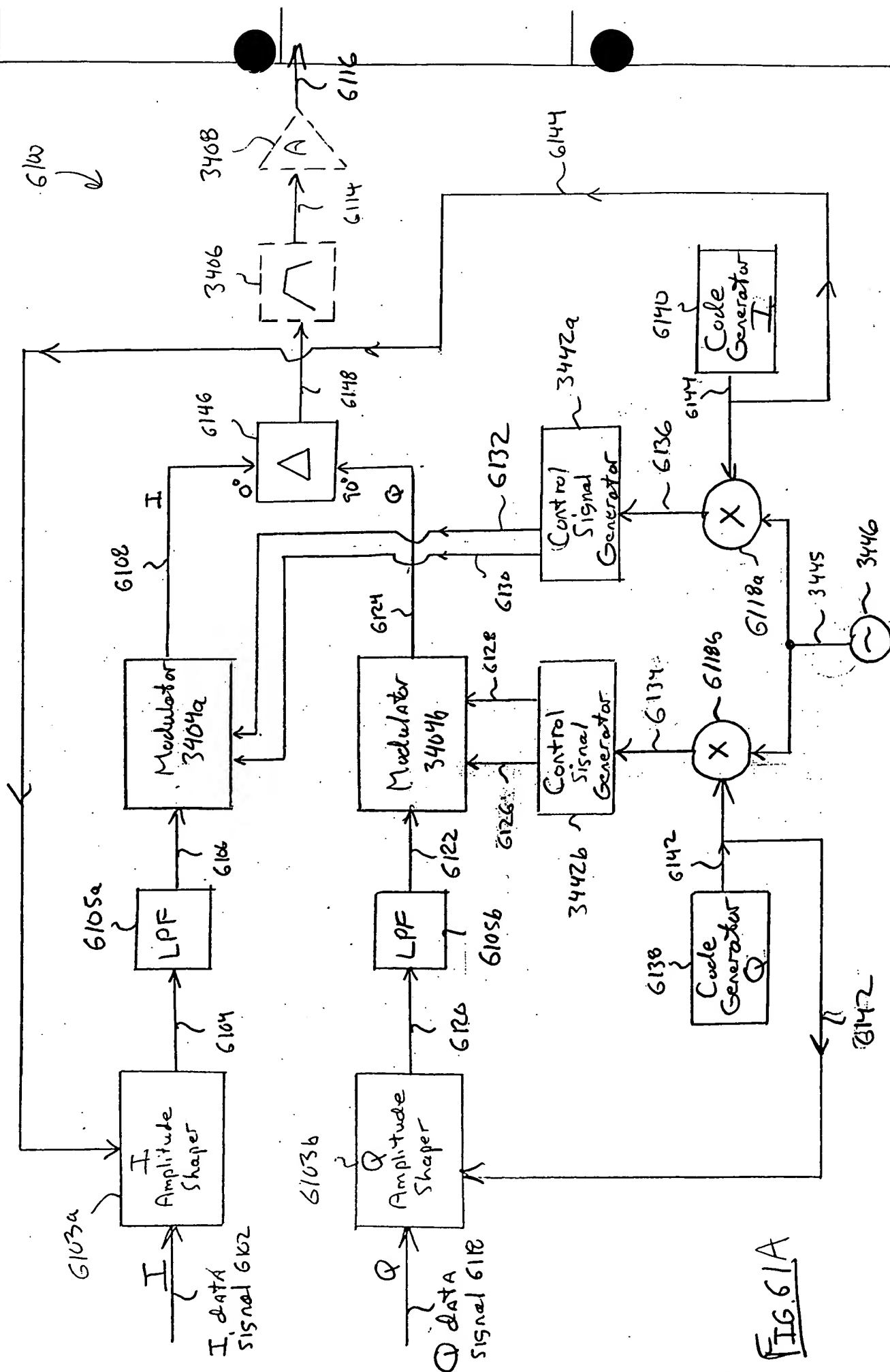


FIG. 61A

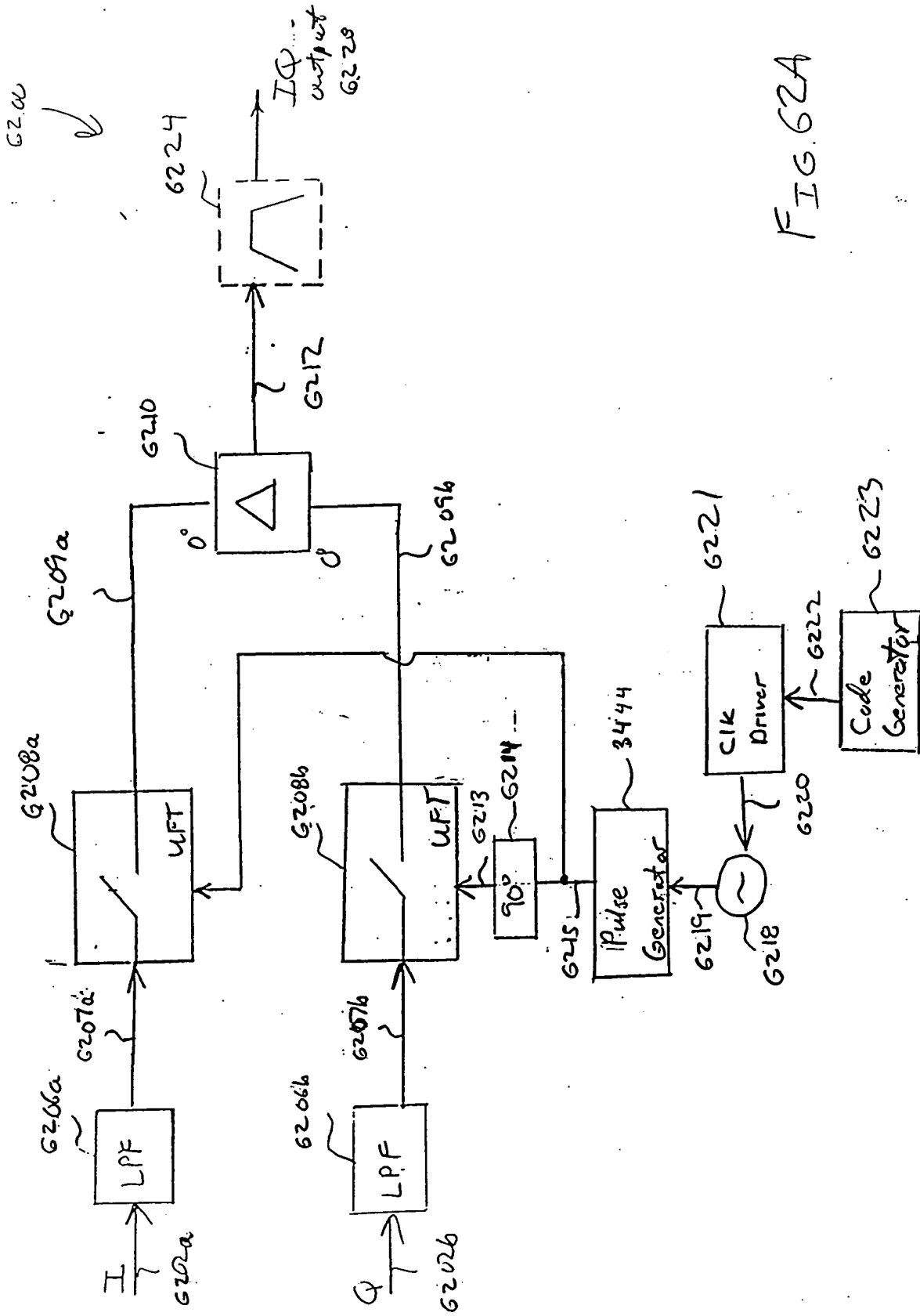


FIG. 62A

6202a
 6202b
 6206a
 6206b
 6207a
 6207b
 6209a
 6209b
 6210
 6212
 6213
 6214
 6215
 6216
 6217
 6218
 6219
 6220
 6221
 6222
 6223
 6224



63026 →

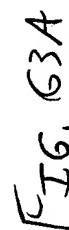
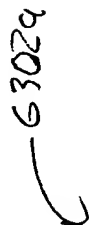
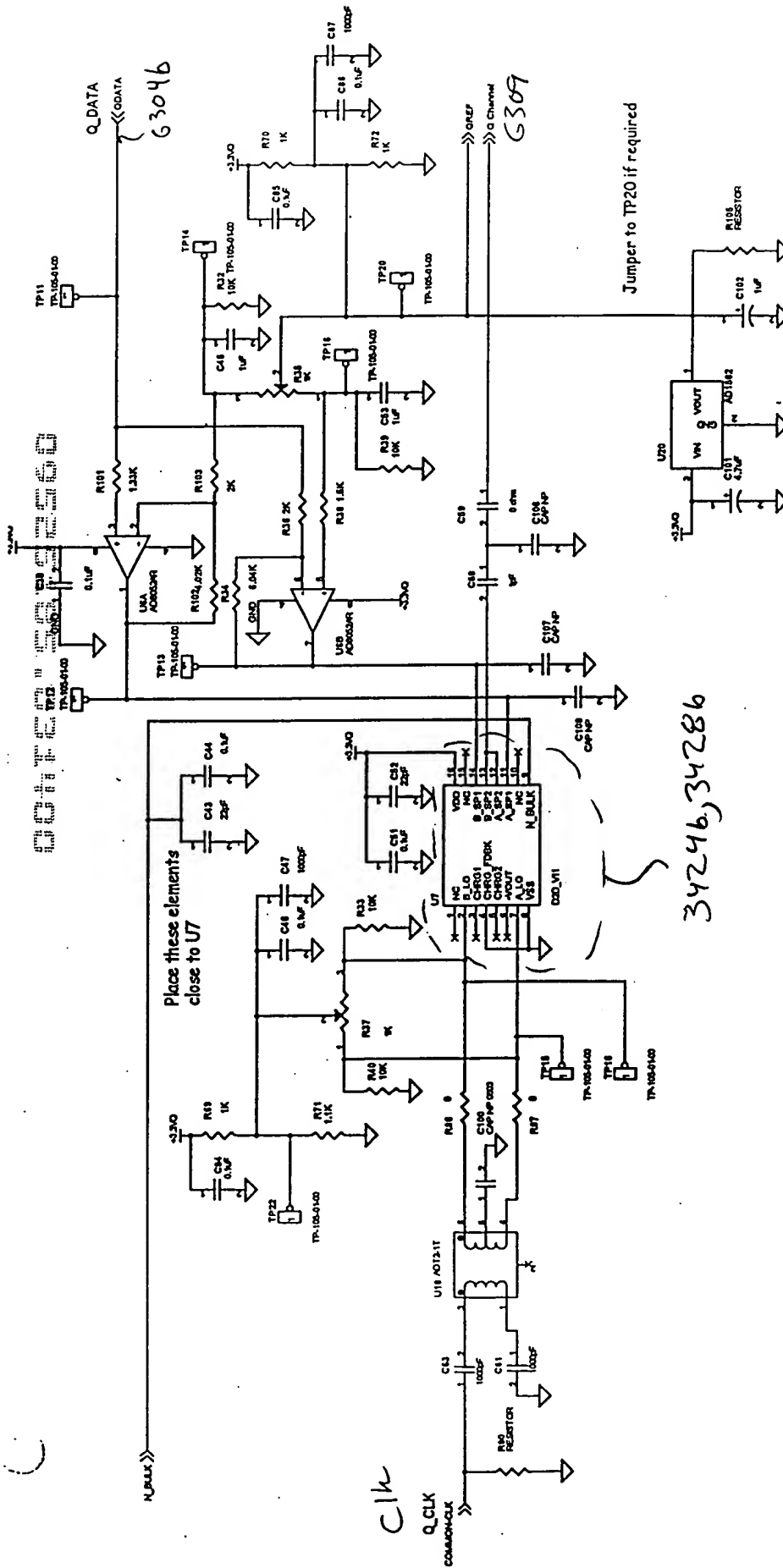


FIG. 63A



Q Channel
6308

Fig. 63C

DATE: 5/25/50

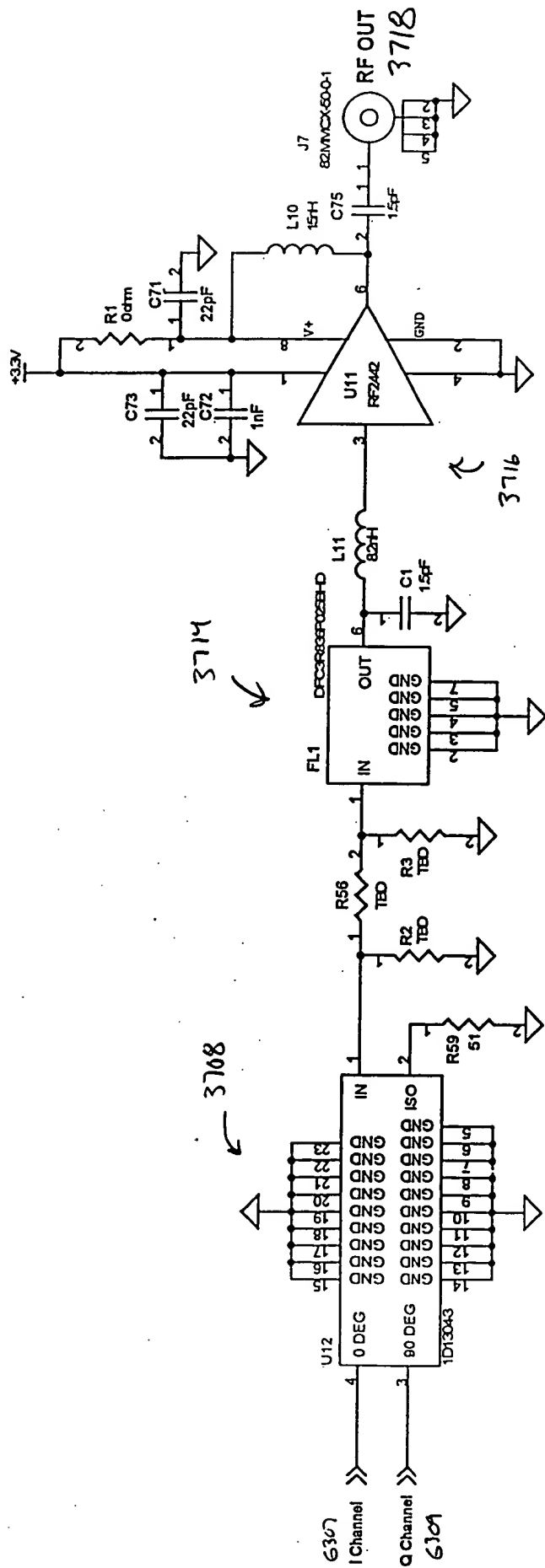


FIG. G30

Combiner
G310

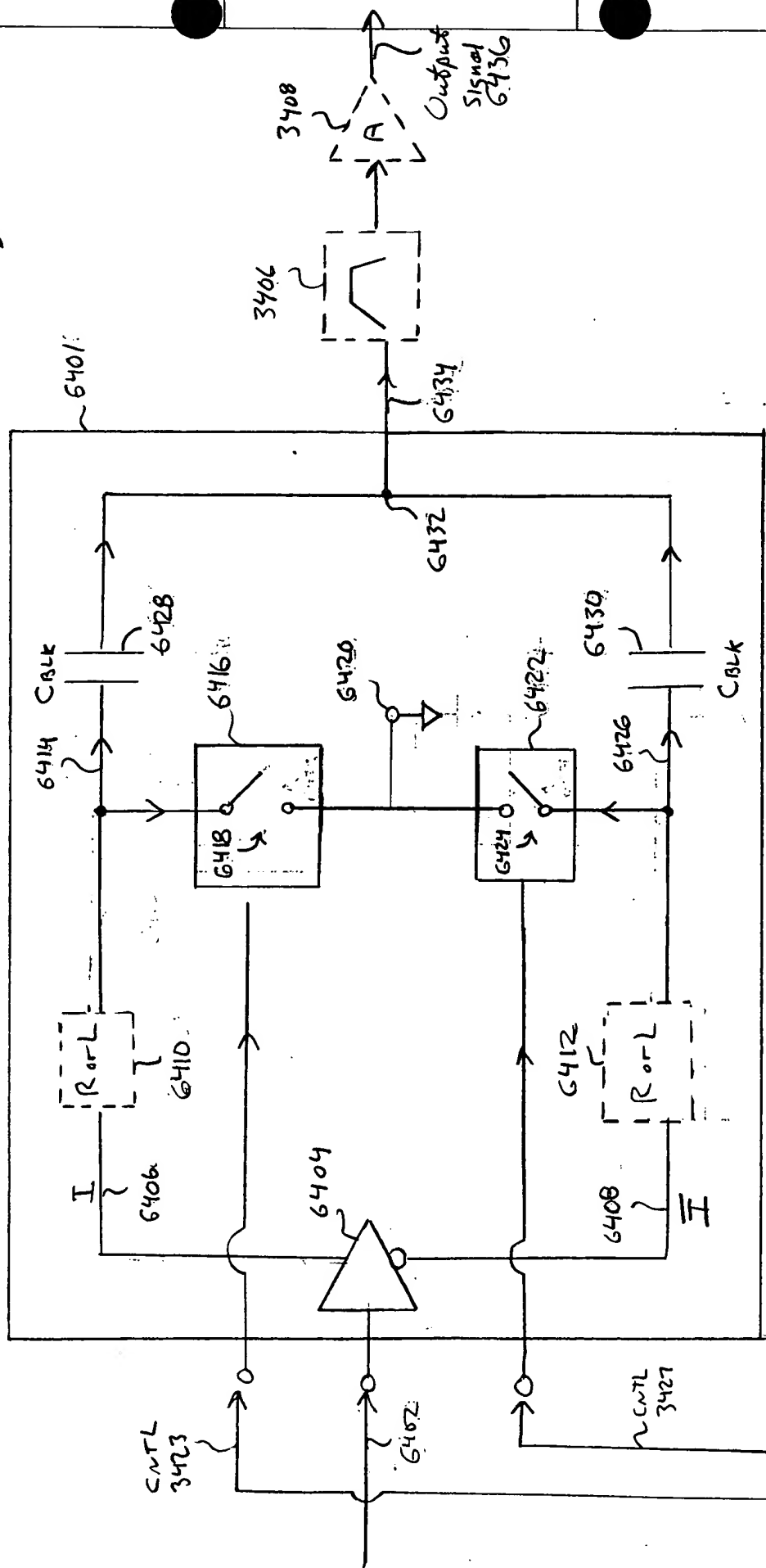


FIG. 64A

3442

CNTL
Signal
generator

6400

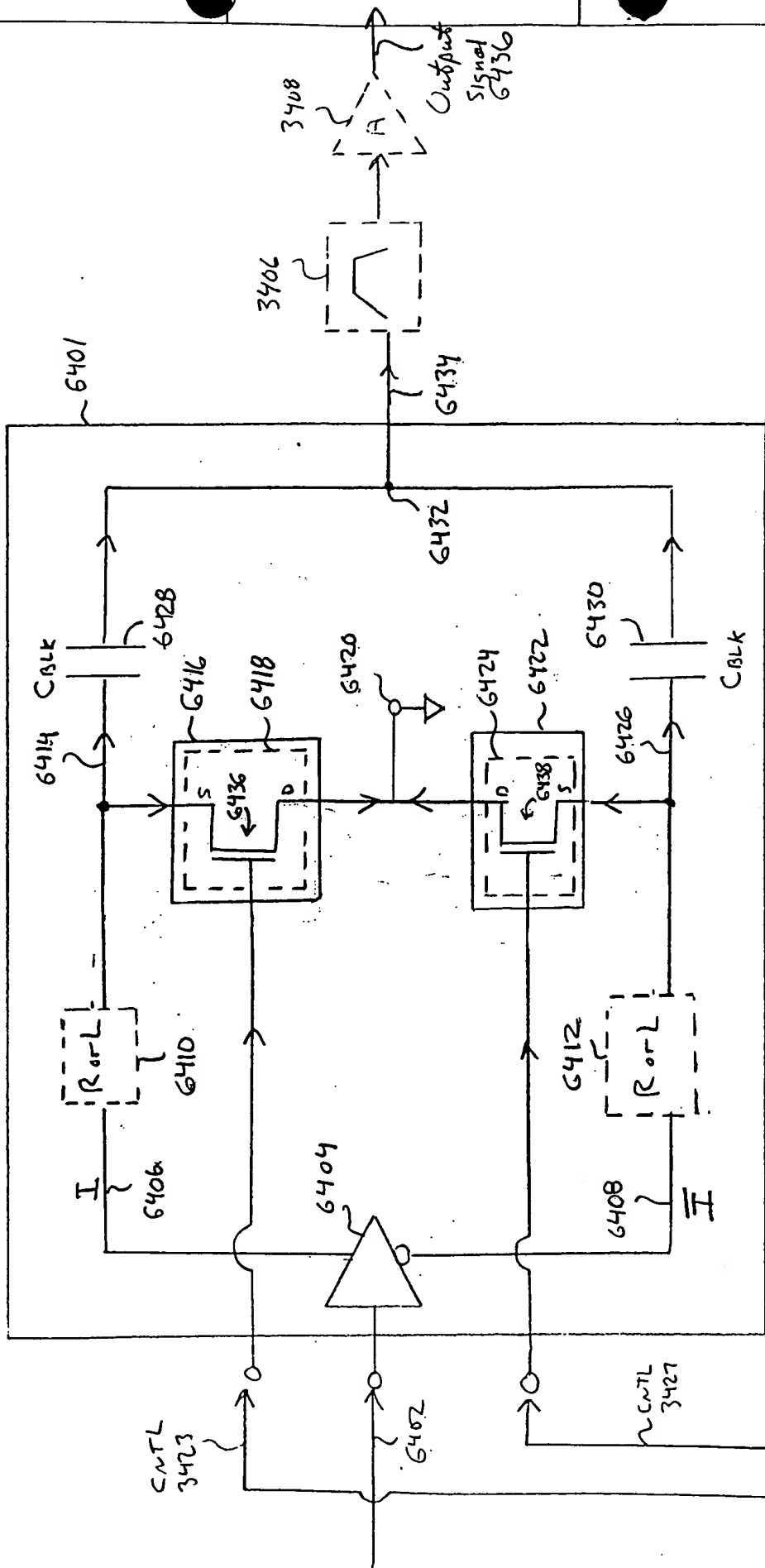


FIG. 64D

~3442

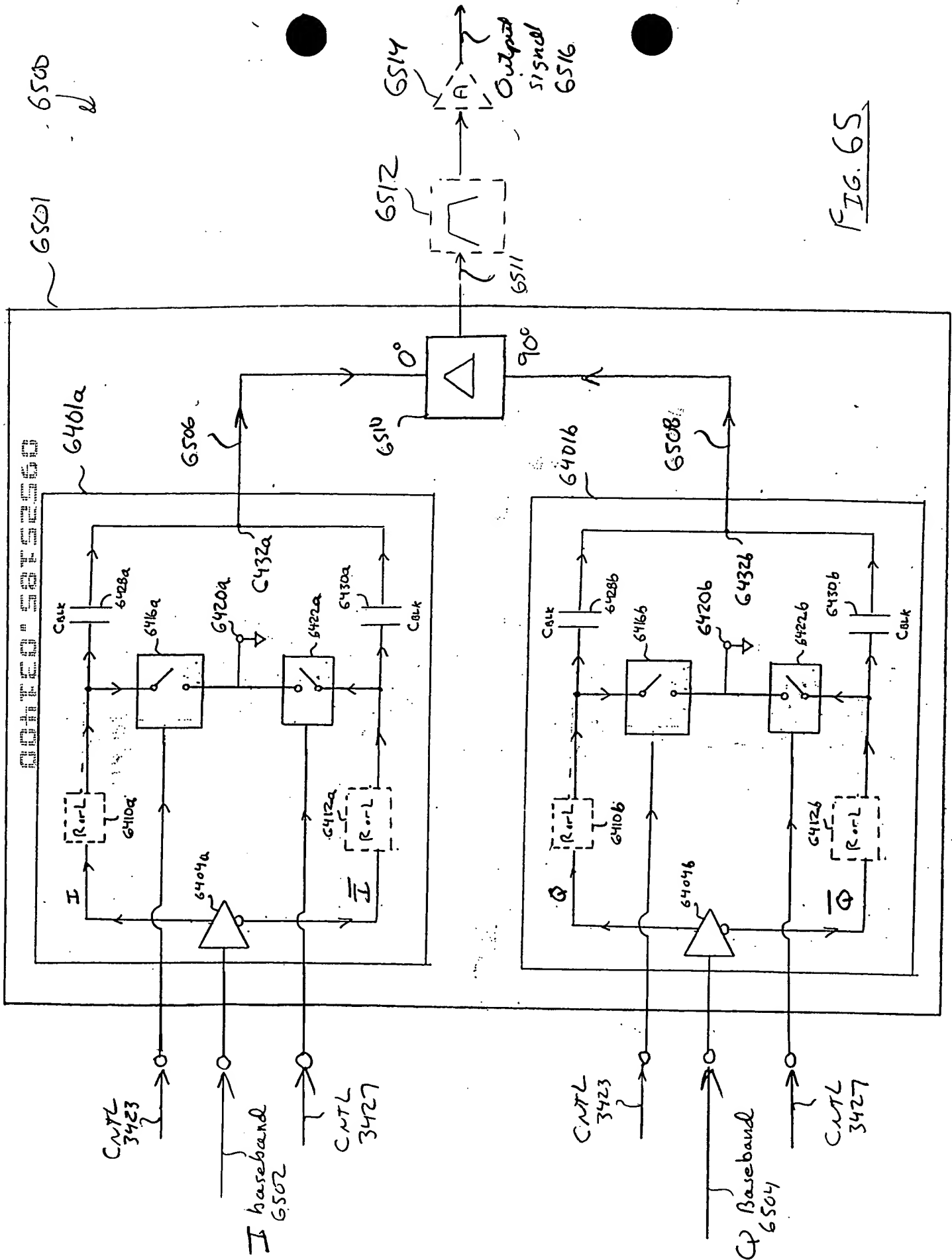


FIG. 65

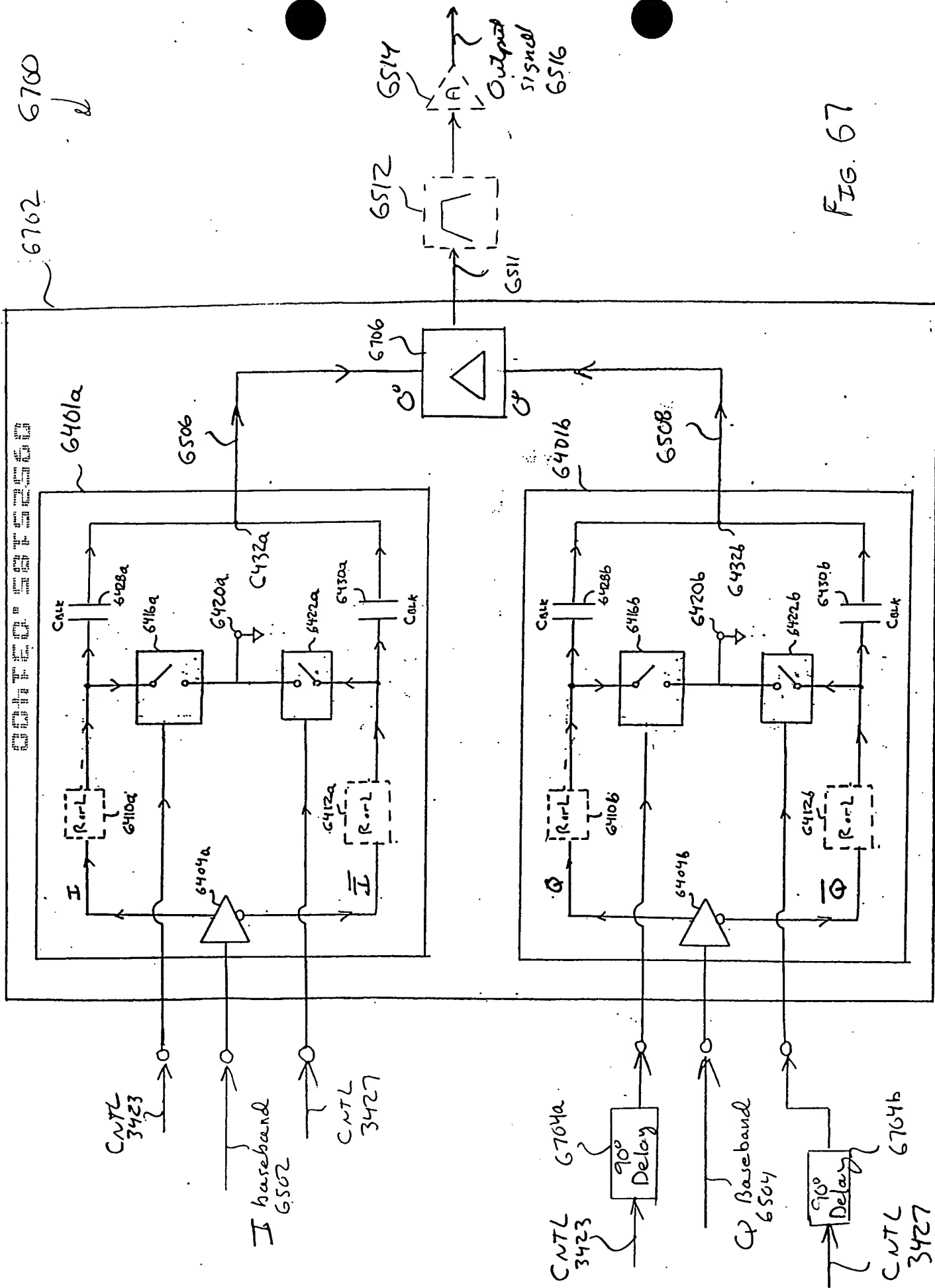


FIG. 67

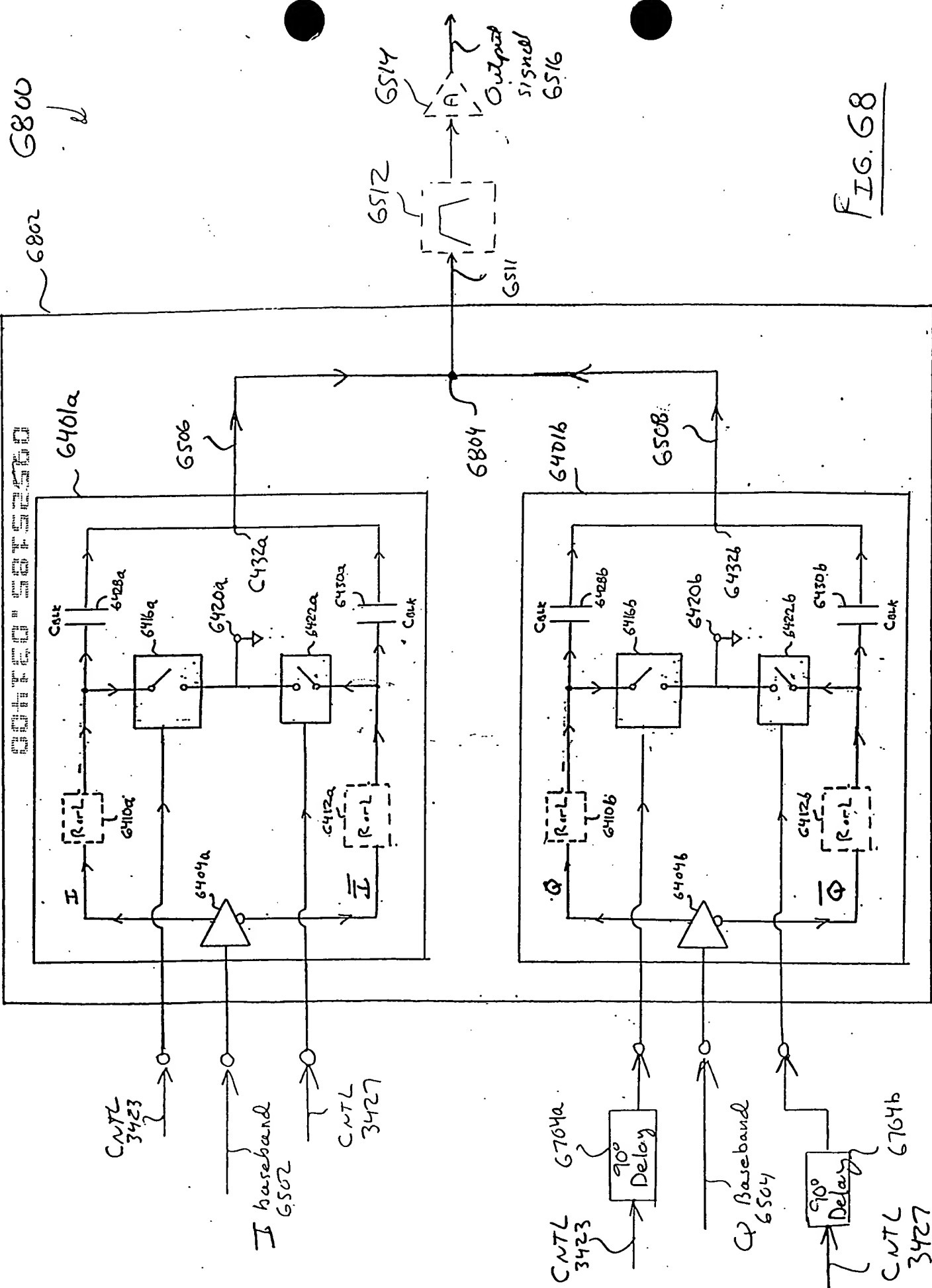


FIG. 68

6900 6902 6904 6906 6908 6910 6912 6914 6916

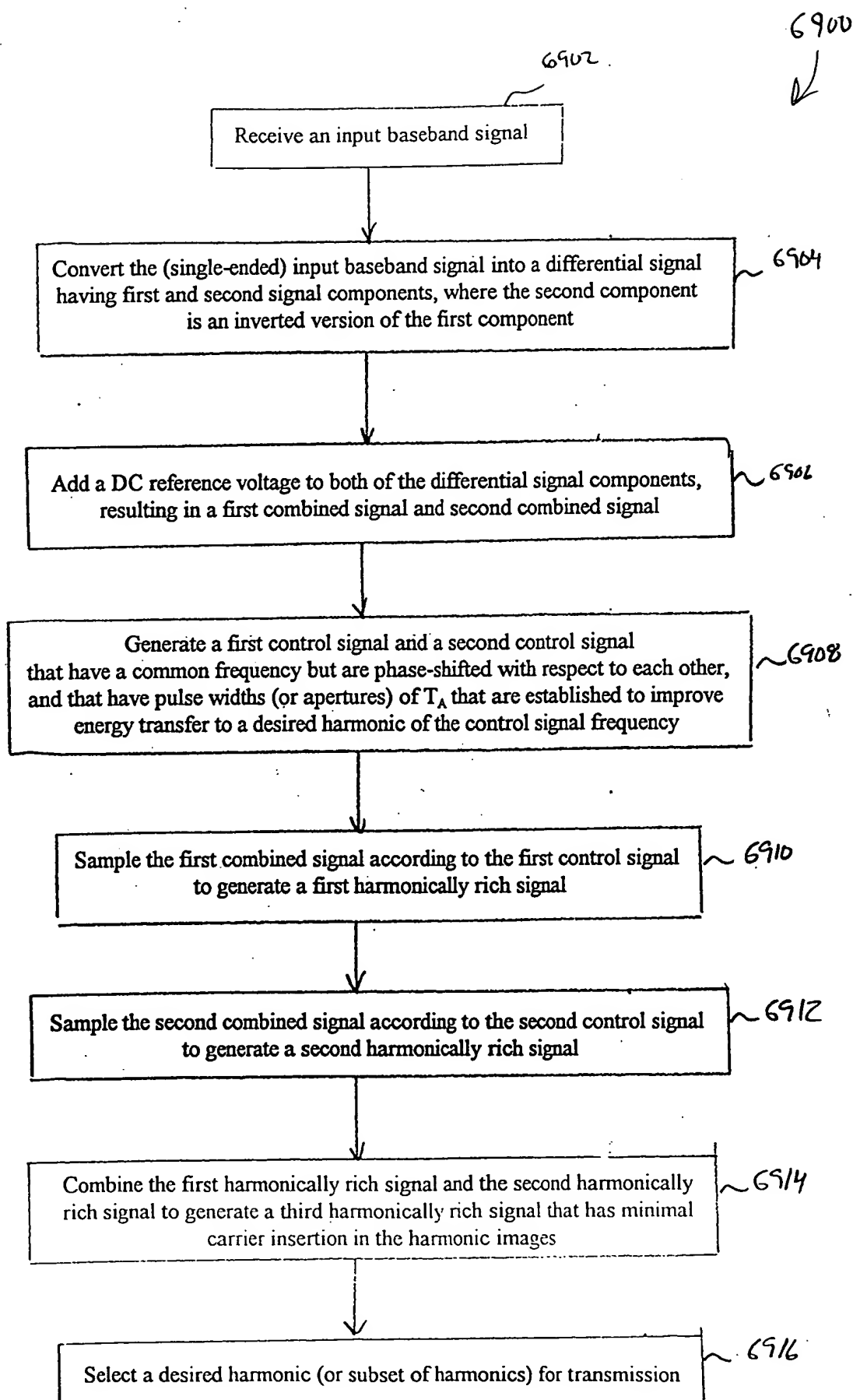


FIG. 69

09523135 034400

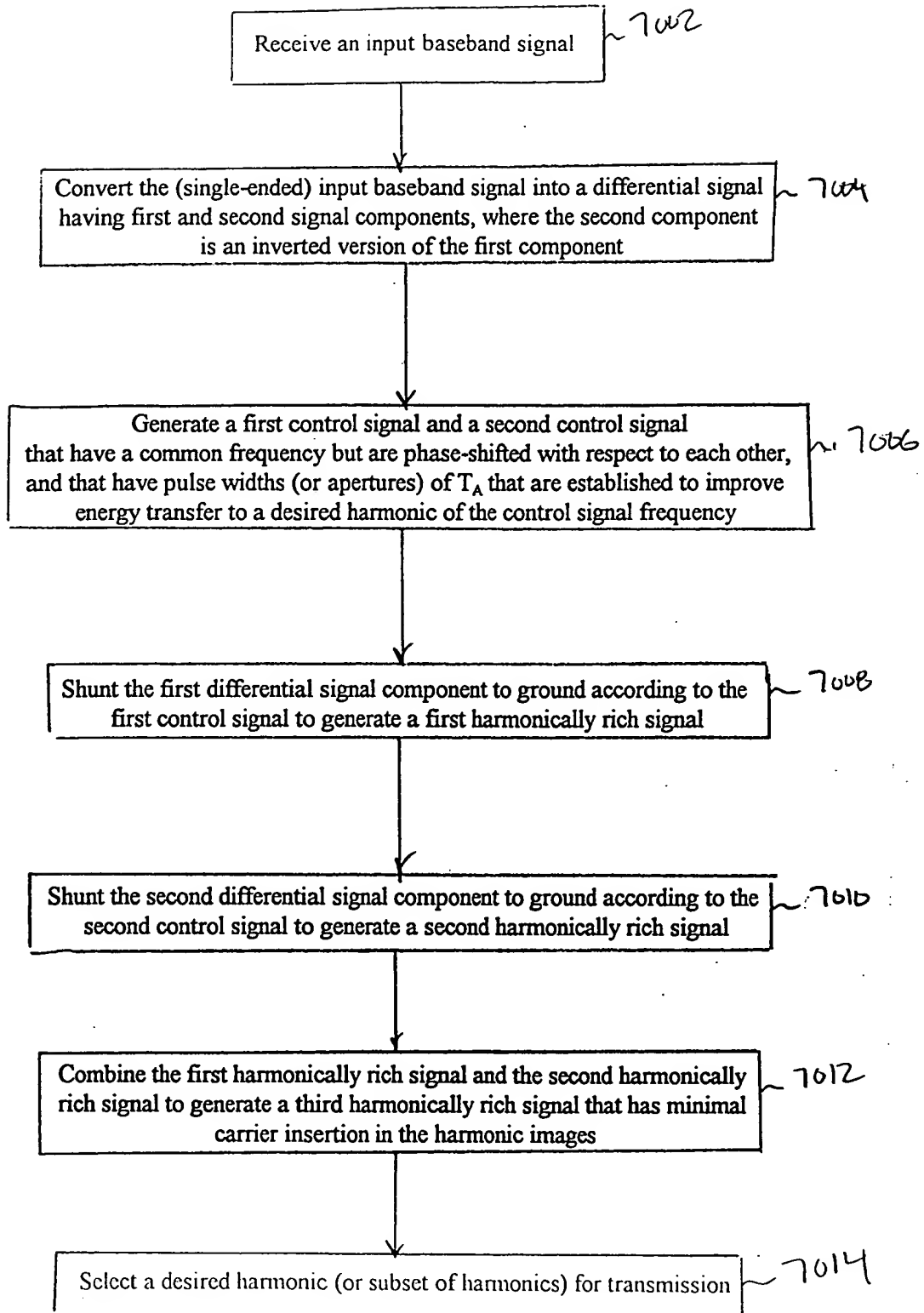


FIG. 70

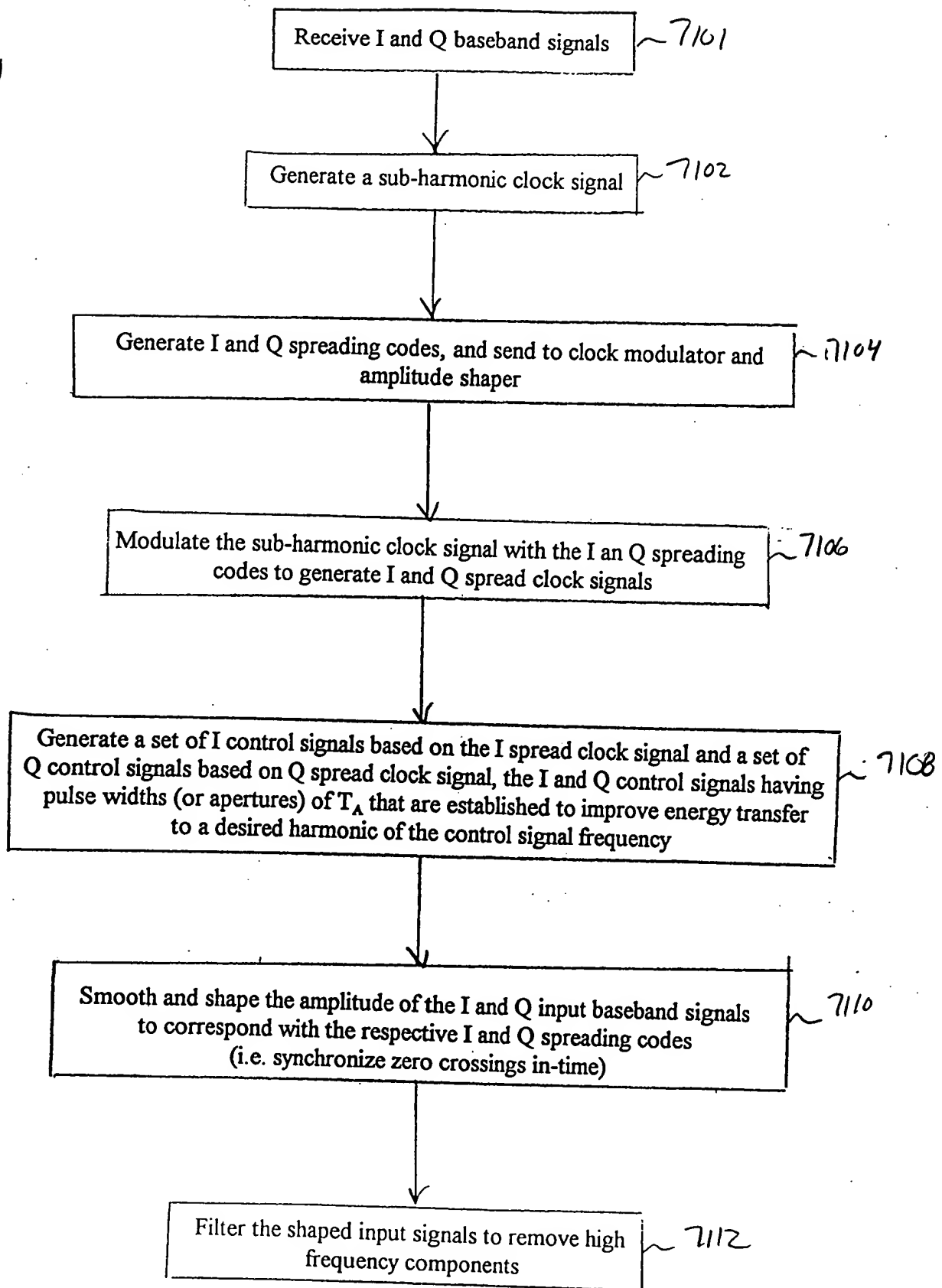
[illegible]

FIG. 71A

7100
(cont.)

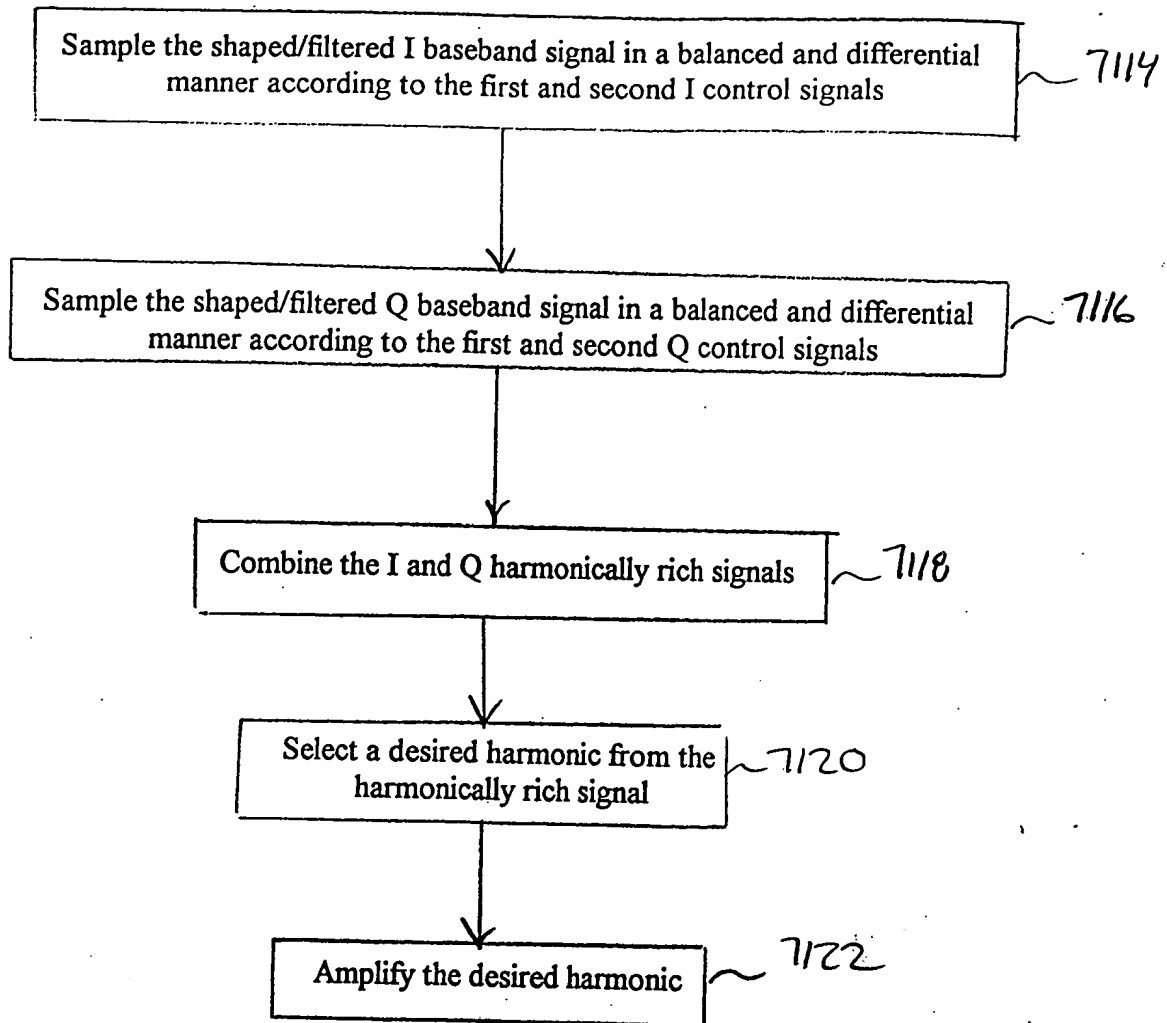


FIG. 71B

7200
↓

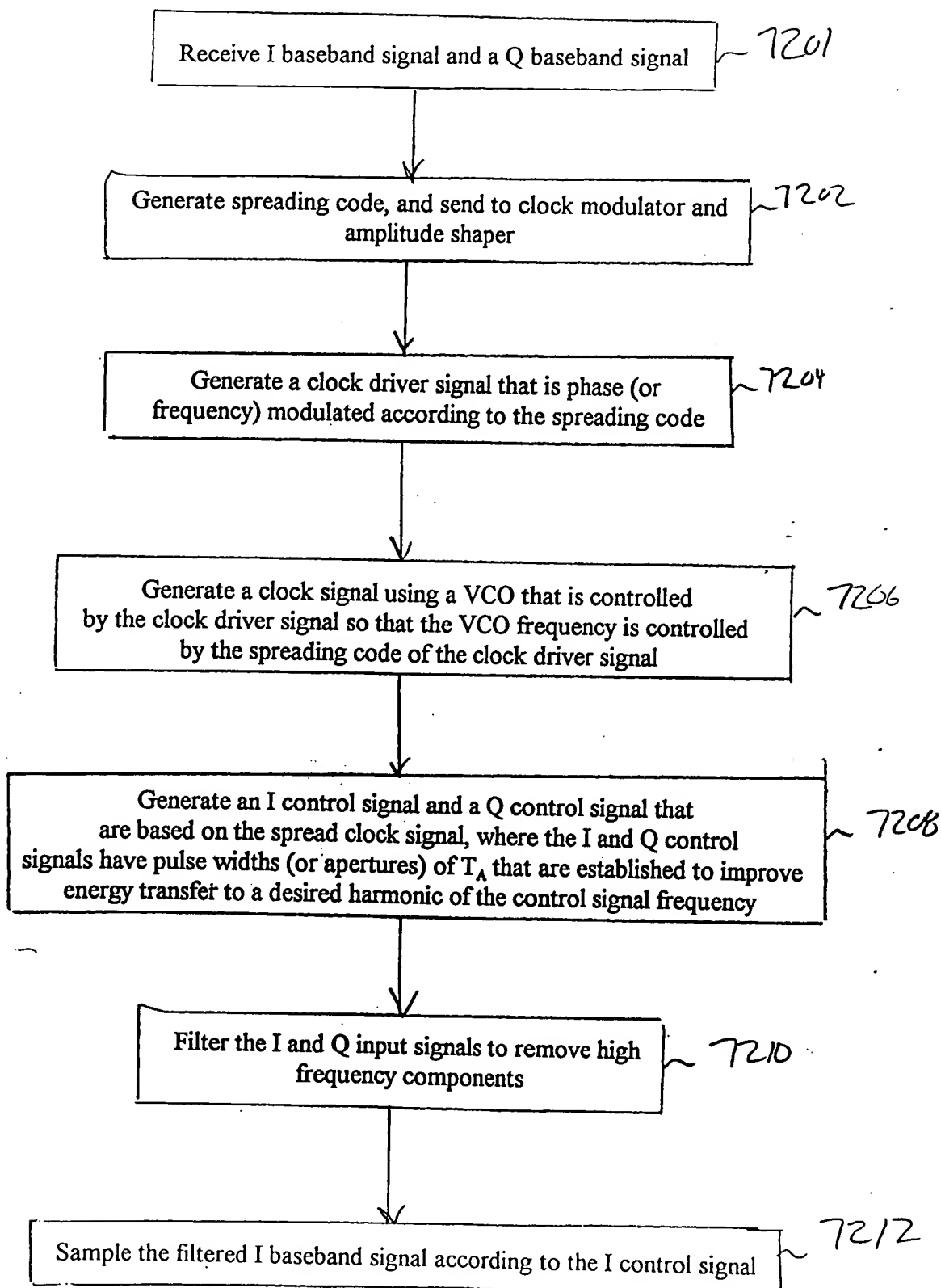
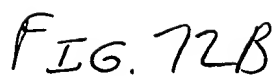
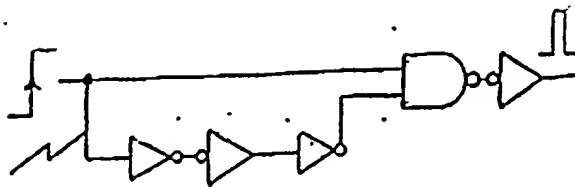


FIG. 72A

THE **NEW** **YORK** **PUBLIC** **LIBRARY**



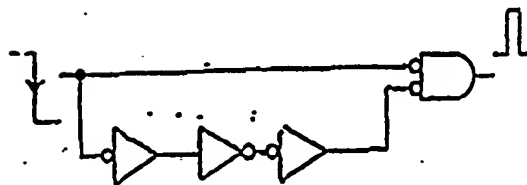
7312
↓



A. rising edge pulse generator

FIG. 73D

7316
↓



B. falling-edge pulse generator

FIG. 73E

7400



Accept the input spread spectrum signal
having center frequency f_c

~7401

Divide the input spread spectrum signal
into an I spread spectrum signal and
a Q spread spectrum signal

~7402

Generate a sub-harmonic oscillating signal
having a frequency f_c/n

~7404

Generate a spreading code,
such as a PN code

~7406

Modulate the oscillating signal
with the spreading code, resulting in spread oscillating signal

~7408

Generate a spread control signal
from the spread oscillating signal, the spread control
signal having non-negligible pulse widths or apertures

~7410

FIG. 74A

↓

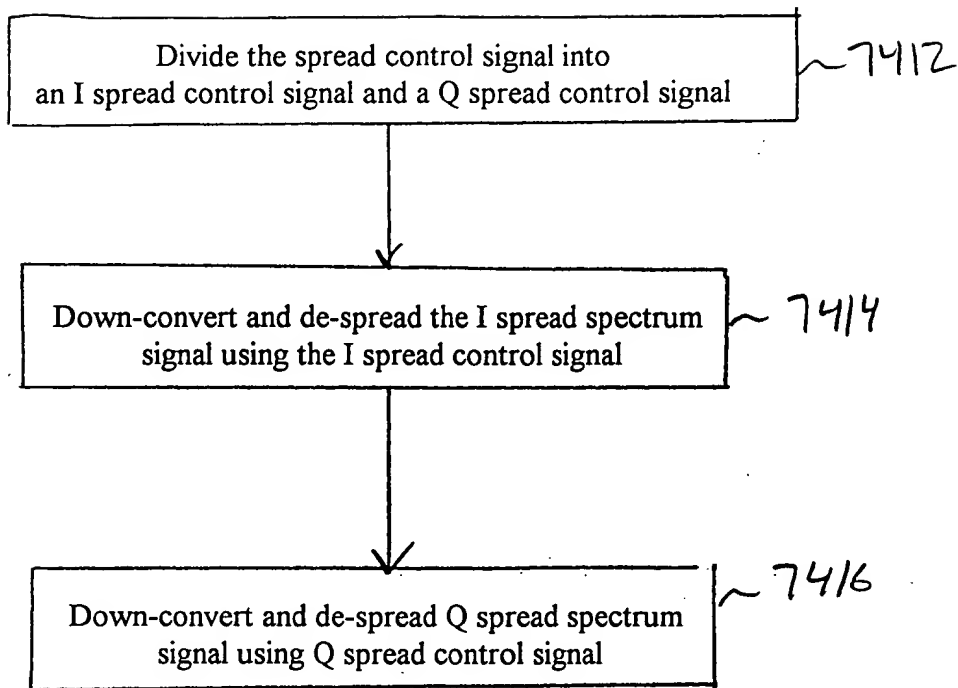


FIG. 74B

1. The first part of the document is a letter from the President of the United States to the Congress, dated January 1, 1861. It is a formal communication, and it is written in a very formal style. The President is addressing the Congress, and he is talking about the state of the Union. He is talking about the progress of the country, and he is talking about the challenges that the country is facing. He is also talking about the role of the President, and he is talking about the responsibilities of the Congress.





COPIES OF THE

7660

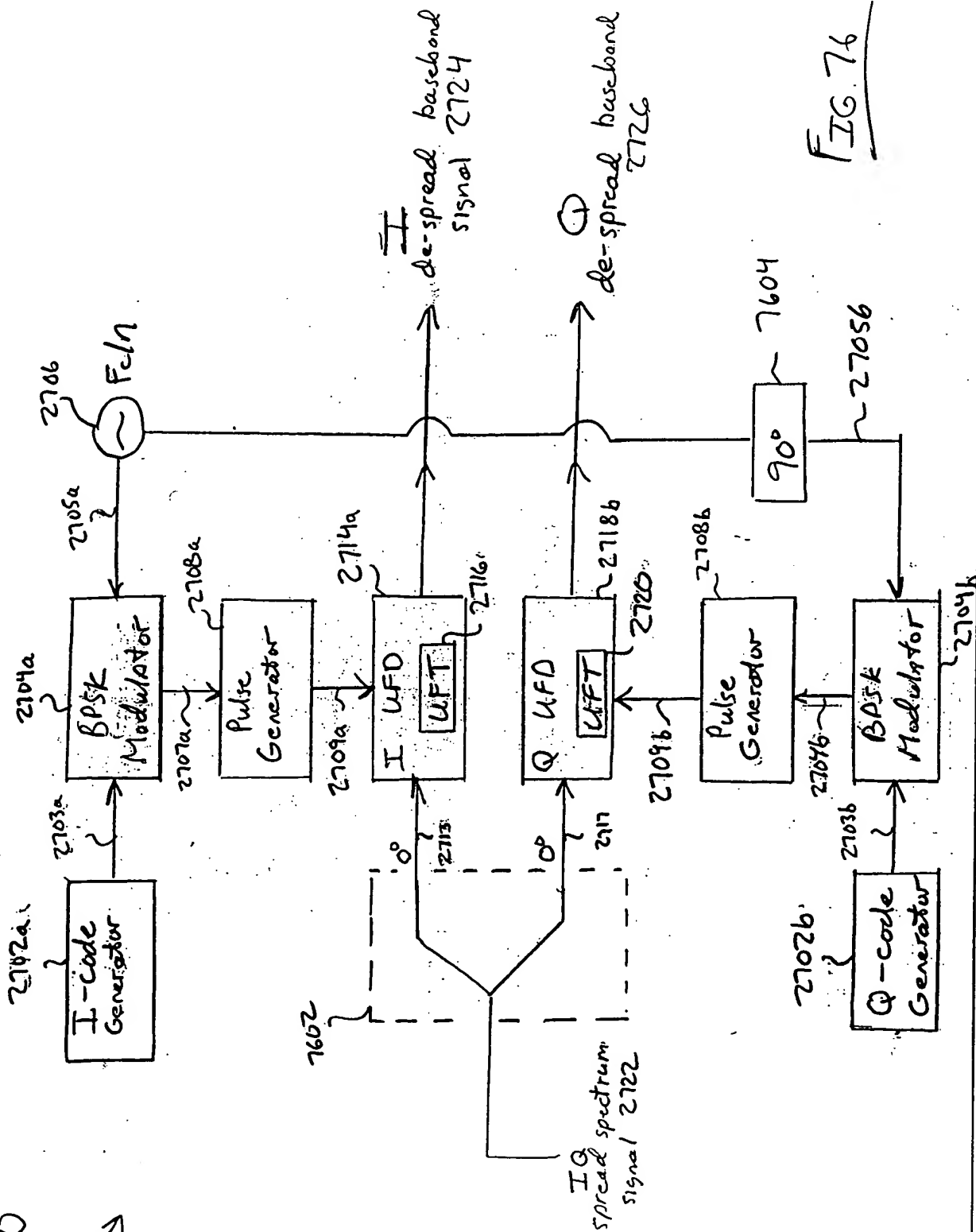


FIG. 76

7700
↓

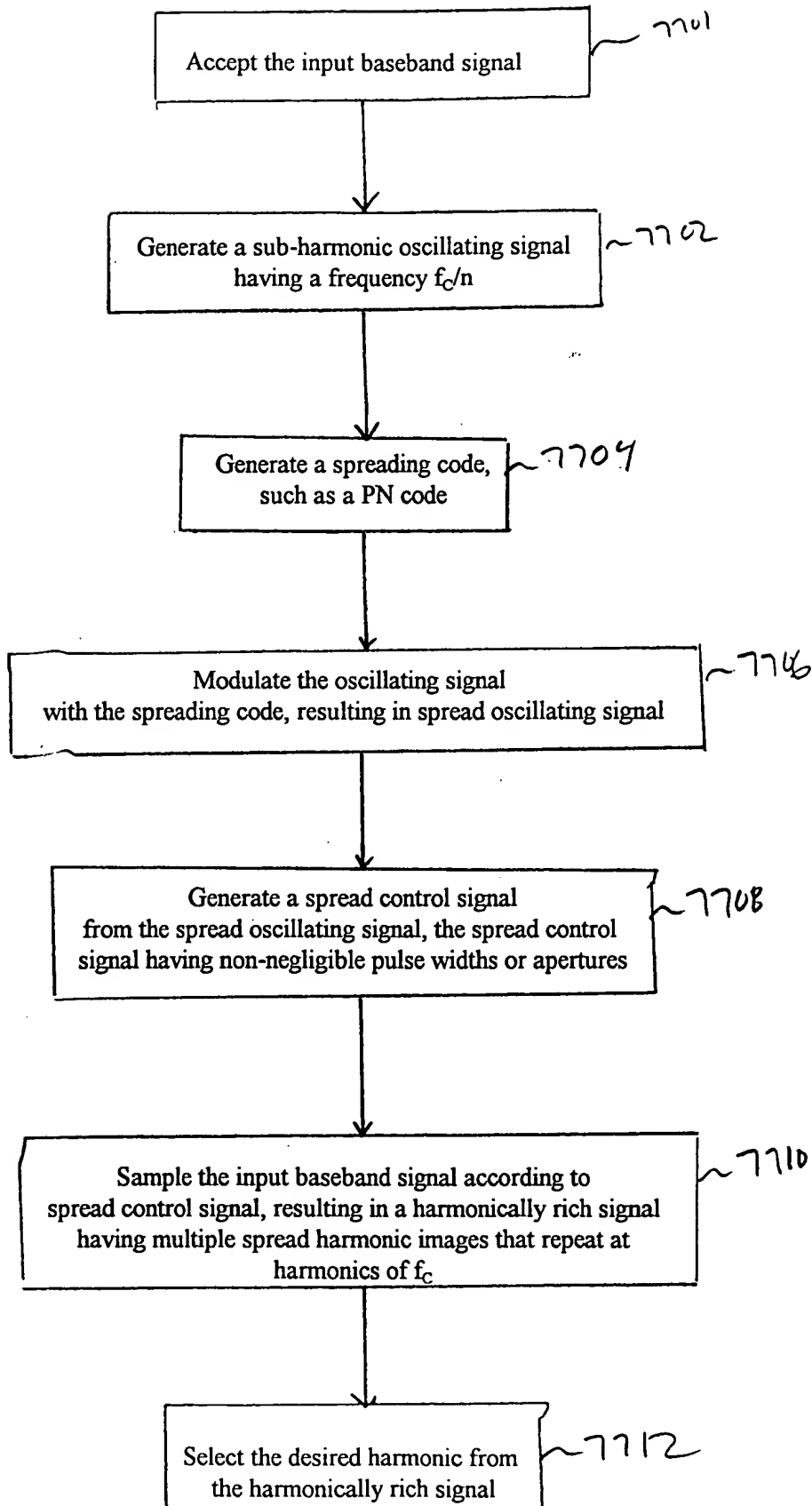


FIG. 77

095405.03400

7800
↓

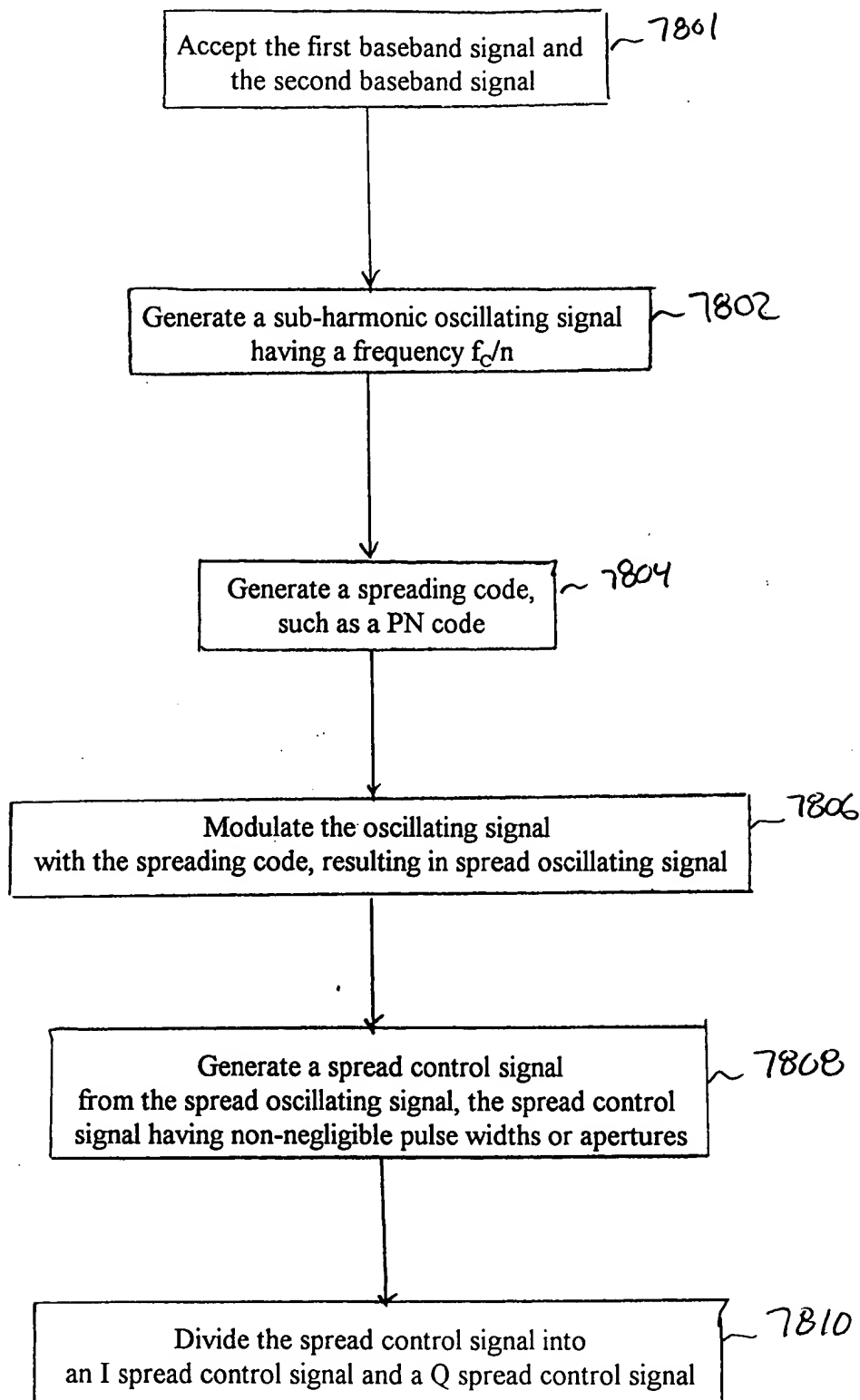
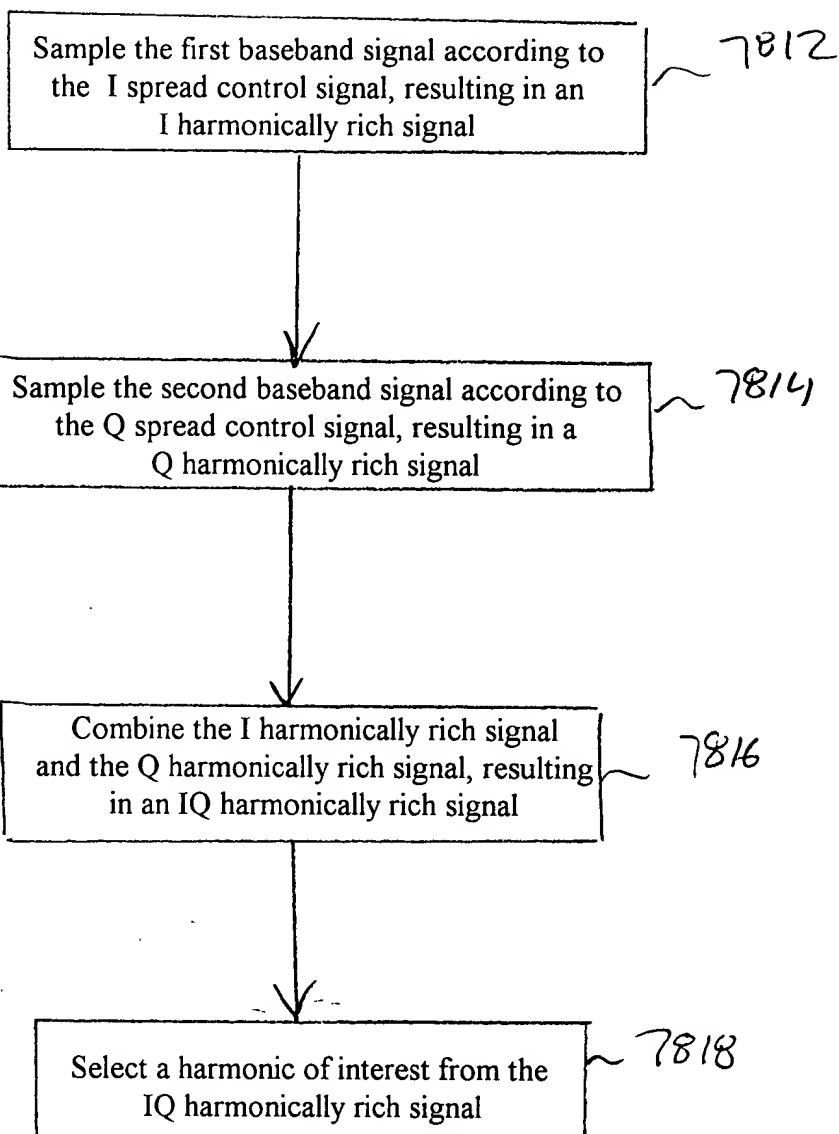


FIG. 78A

7800
(cont.)



004F00" 50452520

FIG. 78B

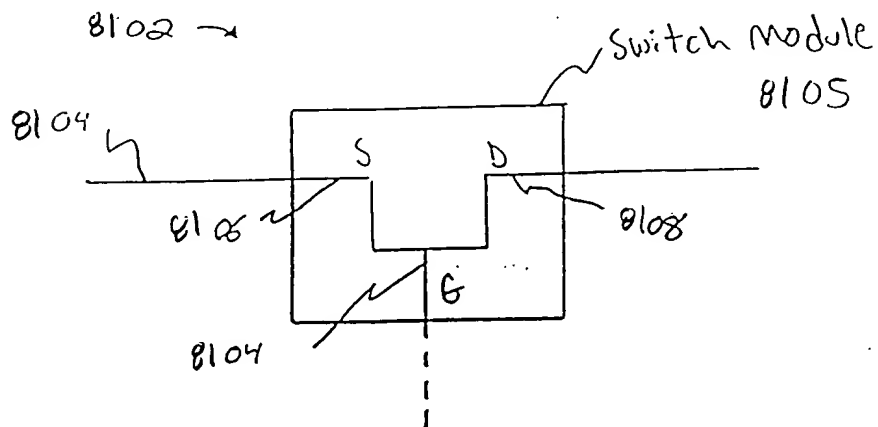


FIG. 81A

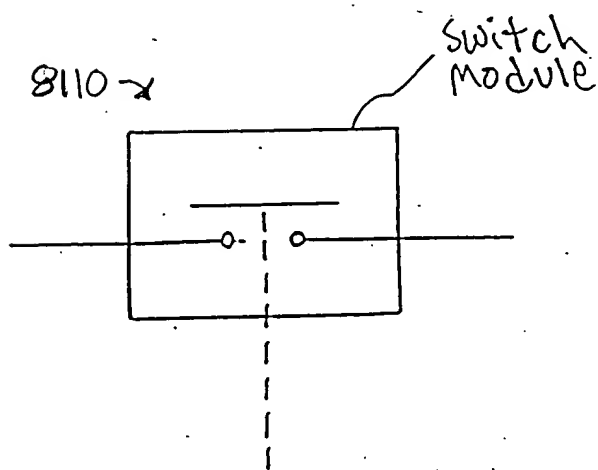


FIG. 81B

42-01 10-4111 (1) 415C 1 SQUARE
42-02 10-4111 (1) 415C 1 SQUARE
42-03 10-4111 (1) 415C 1 SQUARE
42-04 10-4111 (1) 415C 1 SQUARE
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42-98 10-4111 (1) 415C 1 SQUARE
42-99 10-4111 (1) 415C 1 SQUARE
42-100 10-4111 (1) 415C 1 SQUARE

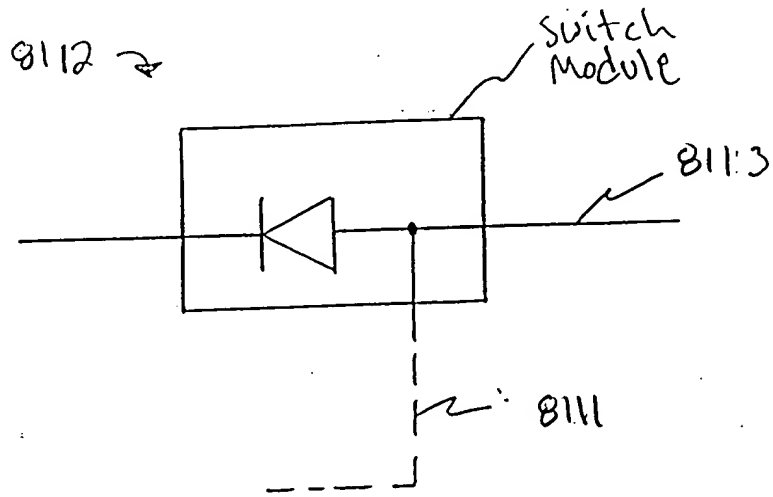


FIG. 81C

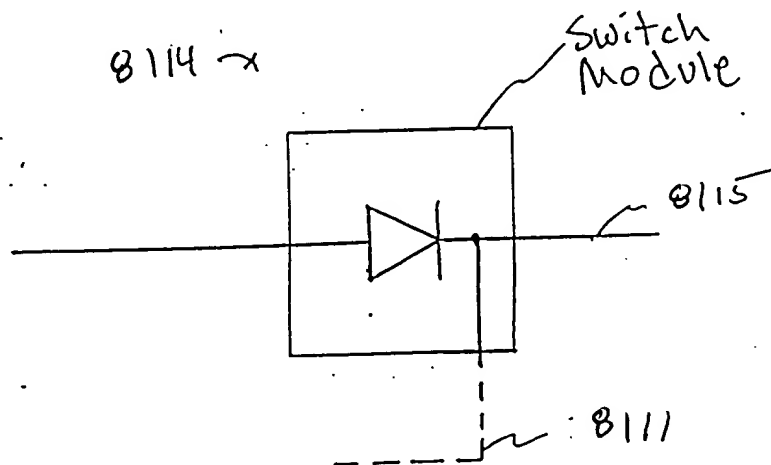


FIG. 81D

1975	1976	1977	1978	1979	1980	1981	1982	1983	1984	1985	1986	1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100	2101	2102	2103	2104	2105	2106	2107	2108	2109	2110	2111	2112	2113	2114	2115	2116	2117	2118	2119	2120	2121	2122	2123	2124	2125	2126	2127	2128	2129	2130	2131	2132	2133	2134	2135	2136	2137	2138	2139	2140	2141	2142	2143	2144	2145	2146	2147	2148	2149	2150	2151	2152	2153	2154	2155	2156	2157	2158	2159	2160	2161	2162	2163	2164	2165	2166	2167	2168	2169	2170	2171	2172	2173	2174	2175	2176	2177	2178	2179	2180	2181	2182	2183	2184	2185	2186	2187	2188	2189	2190	2191	2192	2193	2194	2195	2196	2197	2198	2199	2200	2201	2202	2203	2204	2205	2206	2207	2208	2209	2210	2211	2212	2213	2214	2215	2216	2217	2218	2219	2220	2221	2222	2223	2224	2225	2226	2227	2228	2229	2230	2231	2232	2233	2234	2235	2236	2237	2238	2239	2240	2241	2242	2243	2244	2245	2246	2247	2248	2249	2250	2251	2252	2253	2254	2255	2256	2257	2258	2259	2260	2261	2262	2263	2264	2265	2266	2267	2268	2269	2270	2271	2272	2273	2274	2275	2276	2277	2278	2279	2280	2281	2282	2283	2284	2285	2286	2287	2288	2289	2290	2291	2292	2293	2294	2295	2296	2297	2298	2299	2300	2301	2302	2303	2304	2305	2306	2307	2308	2309	2310	2311	2312	2313	2314	2315	2316	2317	2318	2319	2320	2321	2322	2323	2324	2325	2326	2327	2328	2329	2330	2331	2332	2333	2334	2335	2336	2337	2338	2339	2340	2341	2342	2343	2344	2345	2346	2347	2348	2349	2350	2351	2352	2353	2354	2355	2356	2357	2358	2359	2360	2361	2362	2363	2364	2365	2366	2367	2368	2369	2370	2371	2372	2373	2374	2375	2376	2377	2378	2379	2380	2381	2382	2383</
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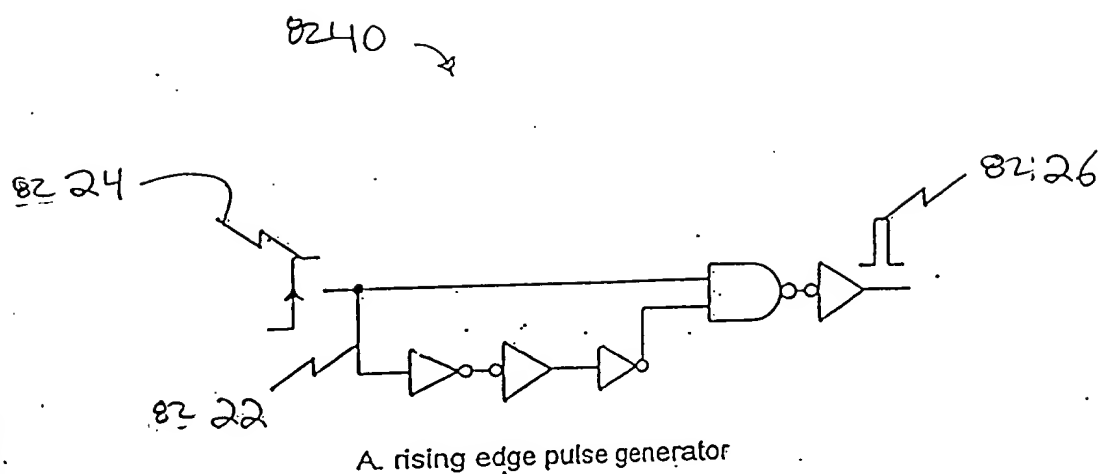


FIG. 82A

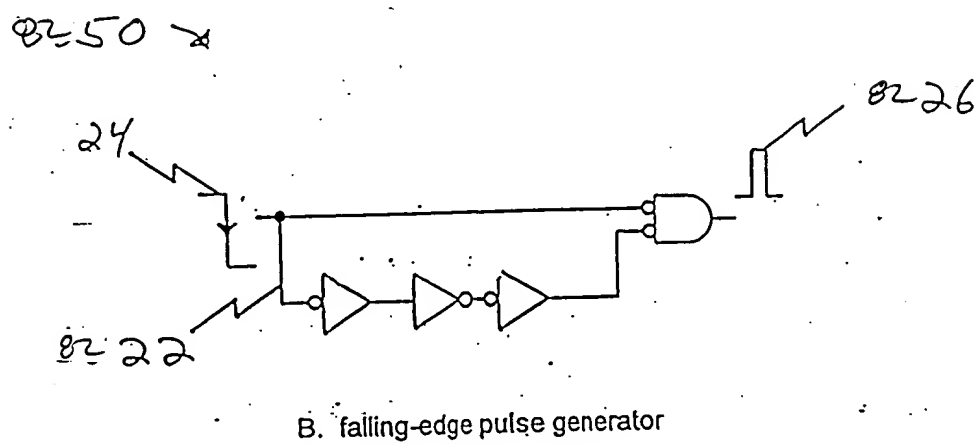
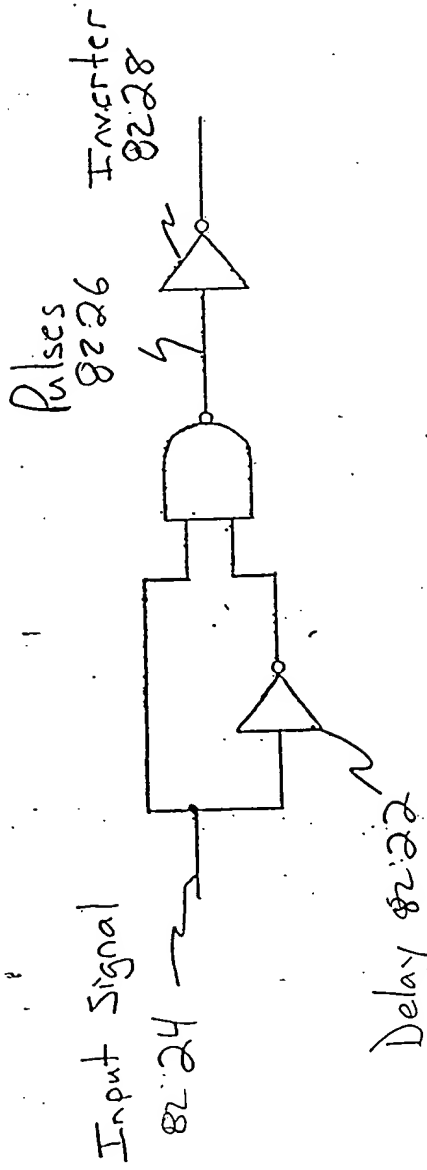


FIG. 82B

0220



-substantial equivalence in logic only is necessary.
-u7 shown for polarity consistency with
ckt examples described elsewhere.

FIG 82C

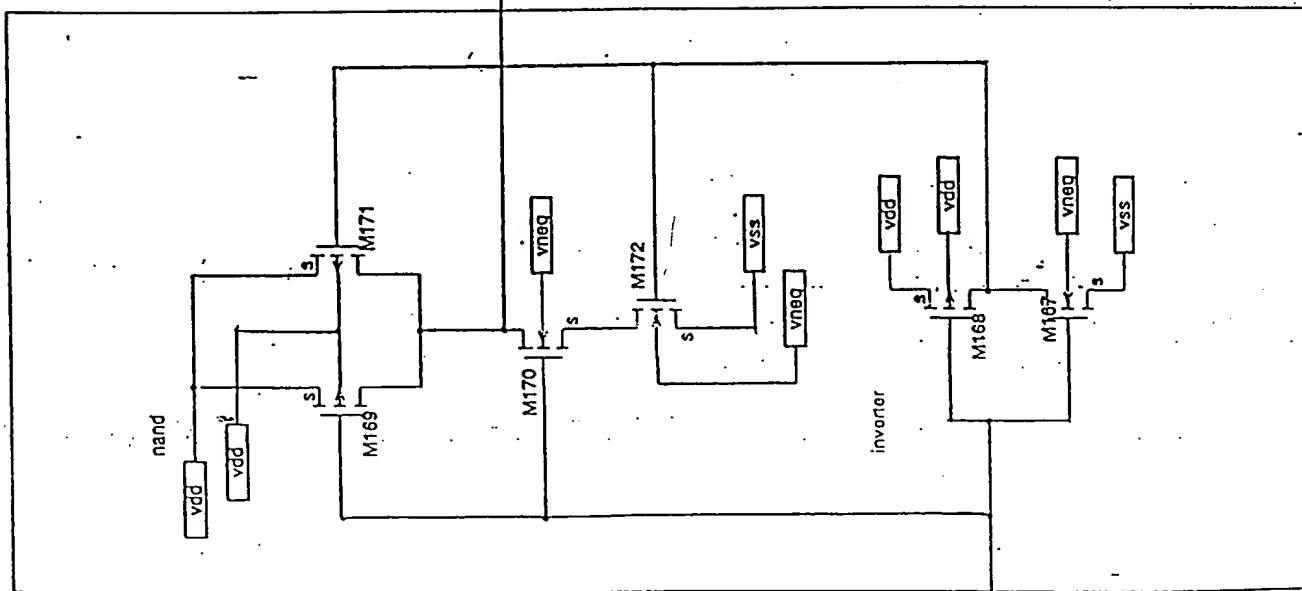


FIG 820

Input
Signal
8228

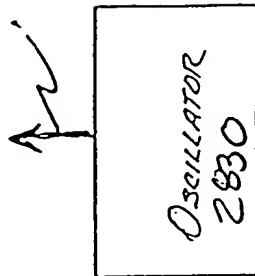


FIG. 821E

Pulses
8226

Input
Signal
8224

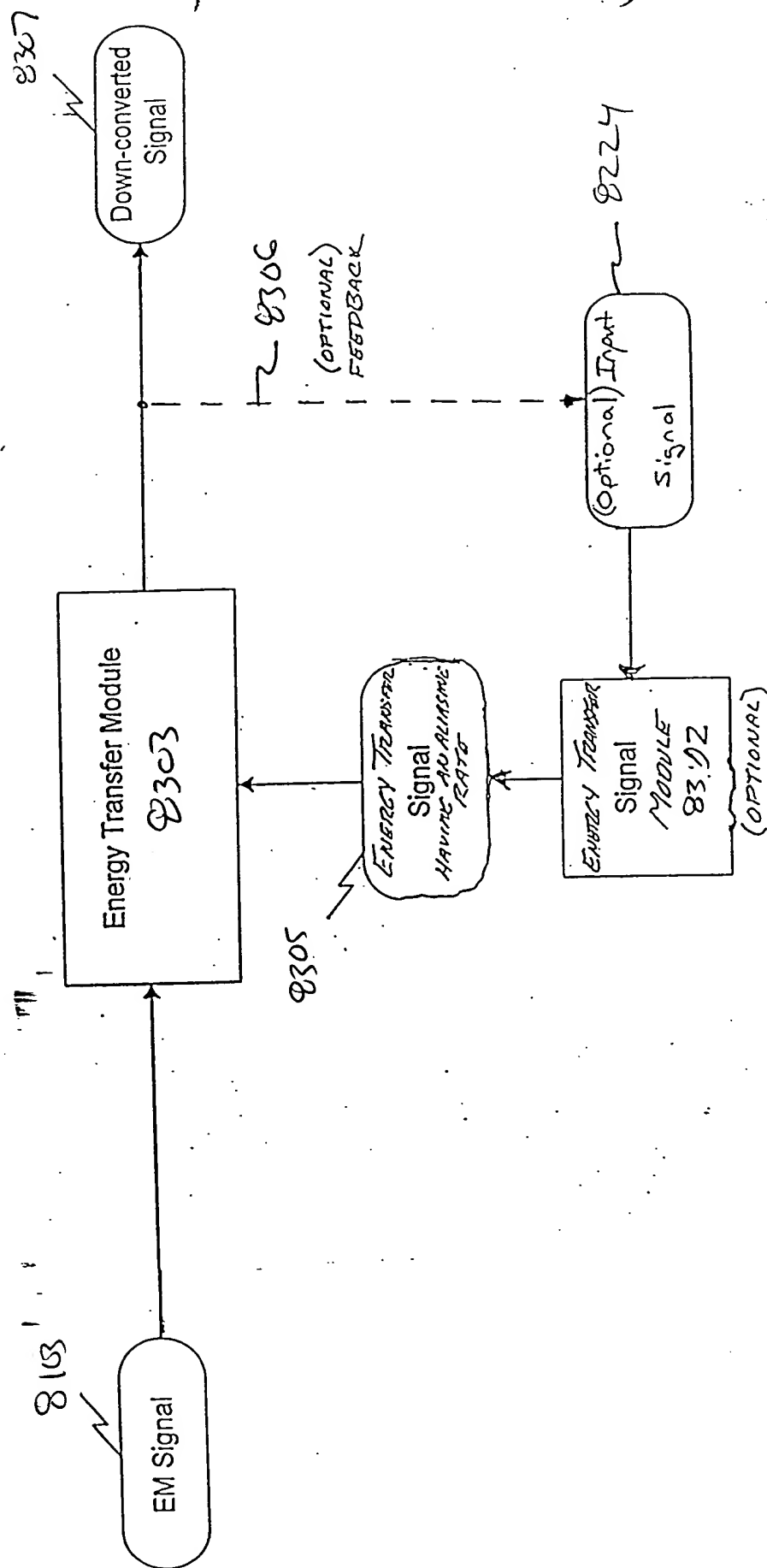
[illegible]

FIG. 83

2022

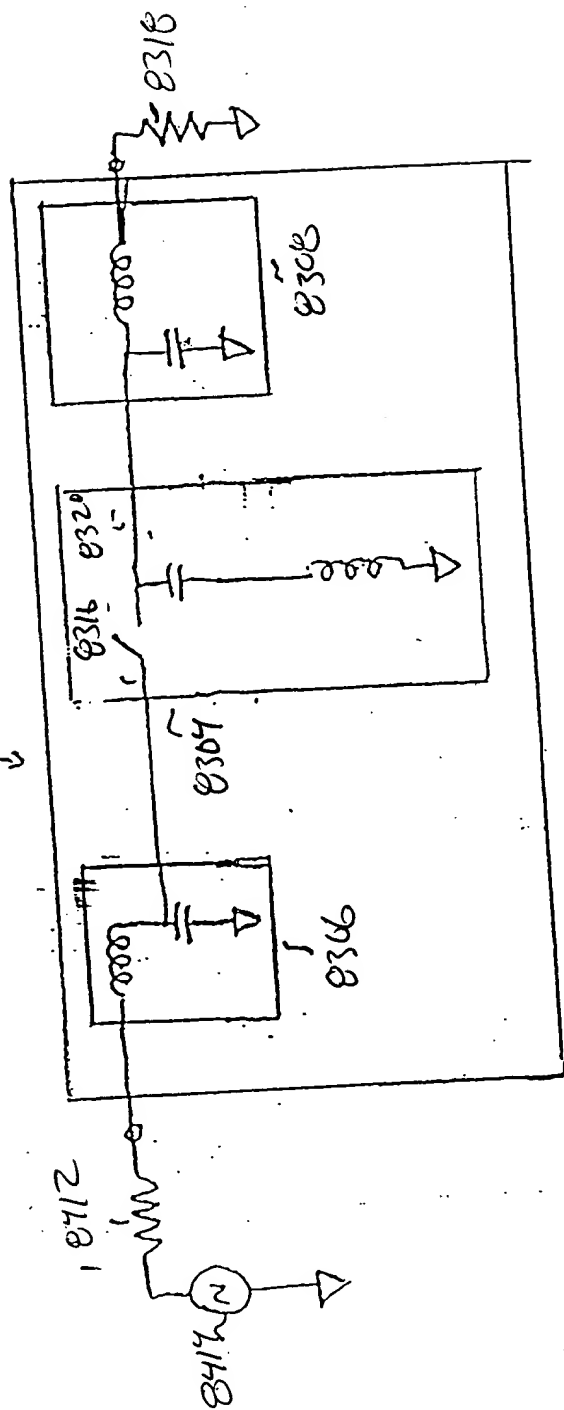


Fig. 84 - Impedance Matched Aliasing Module

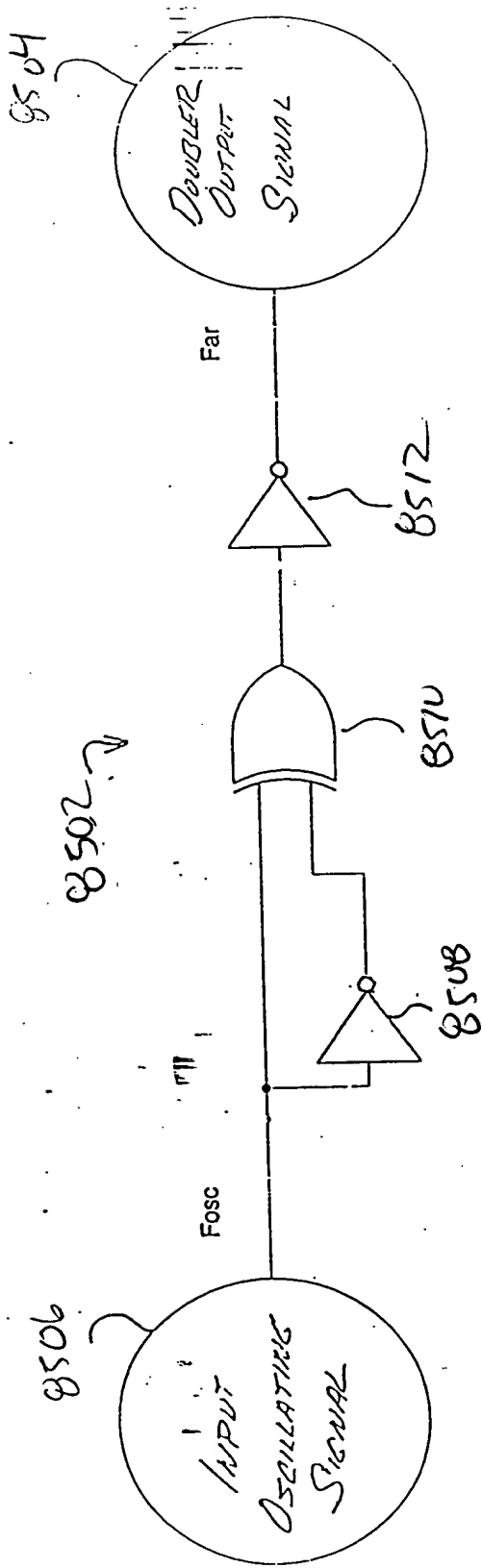


FIG. 85A

FIG. 85B

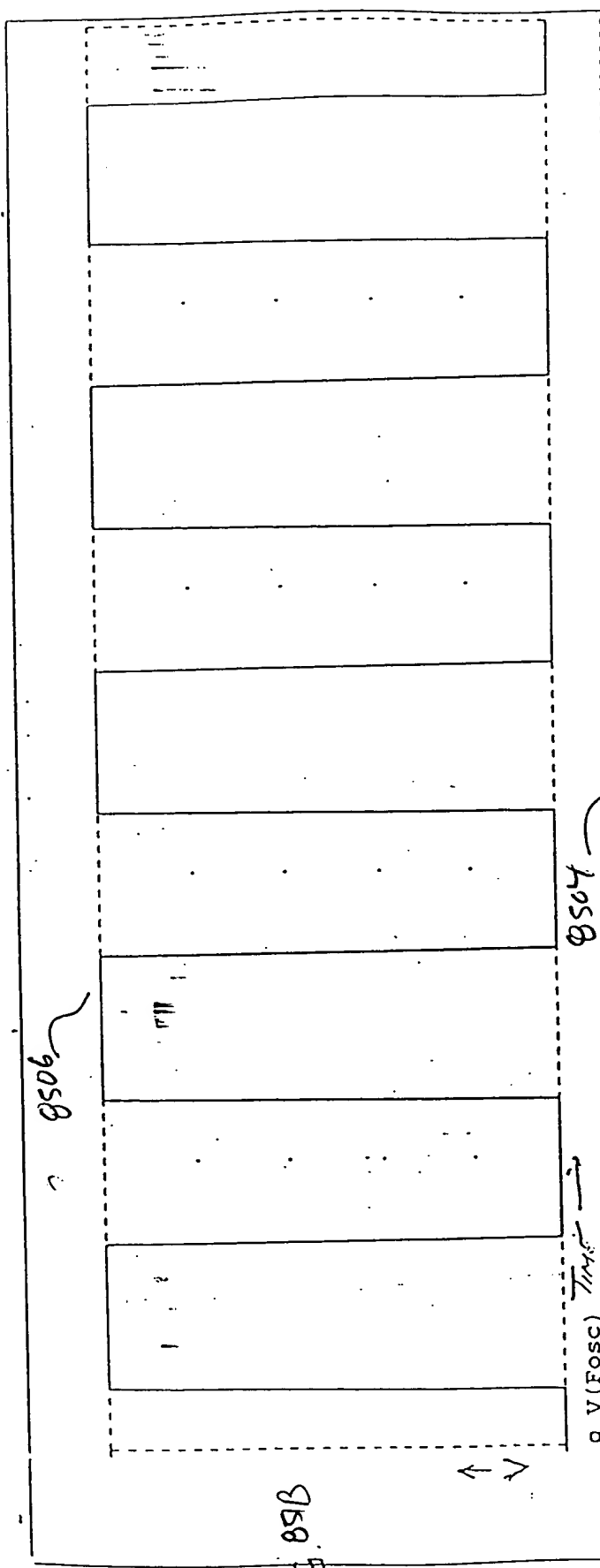
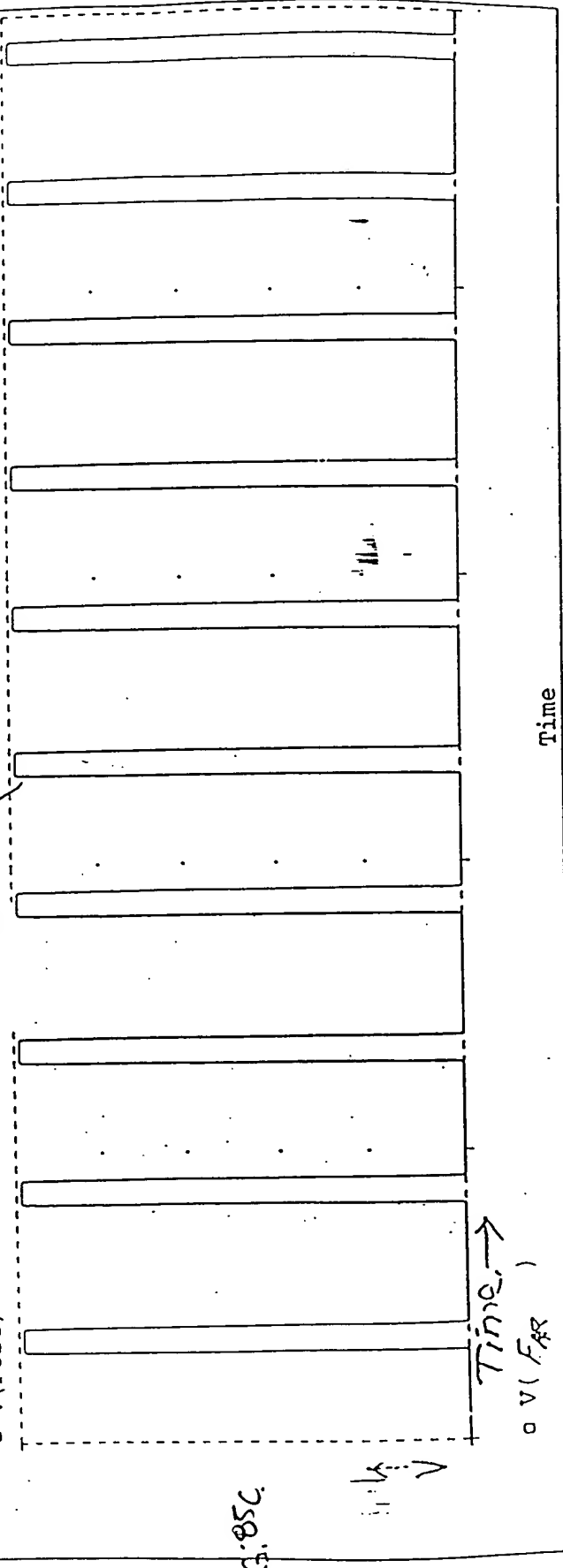


FIG. 85C



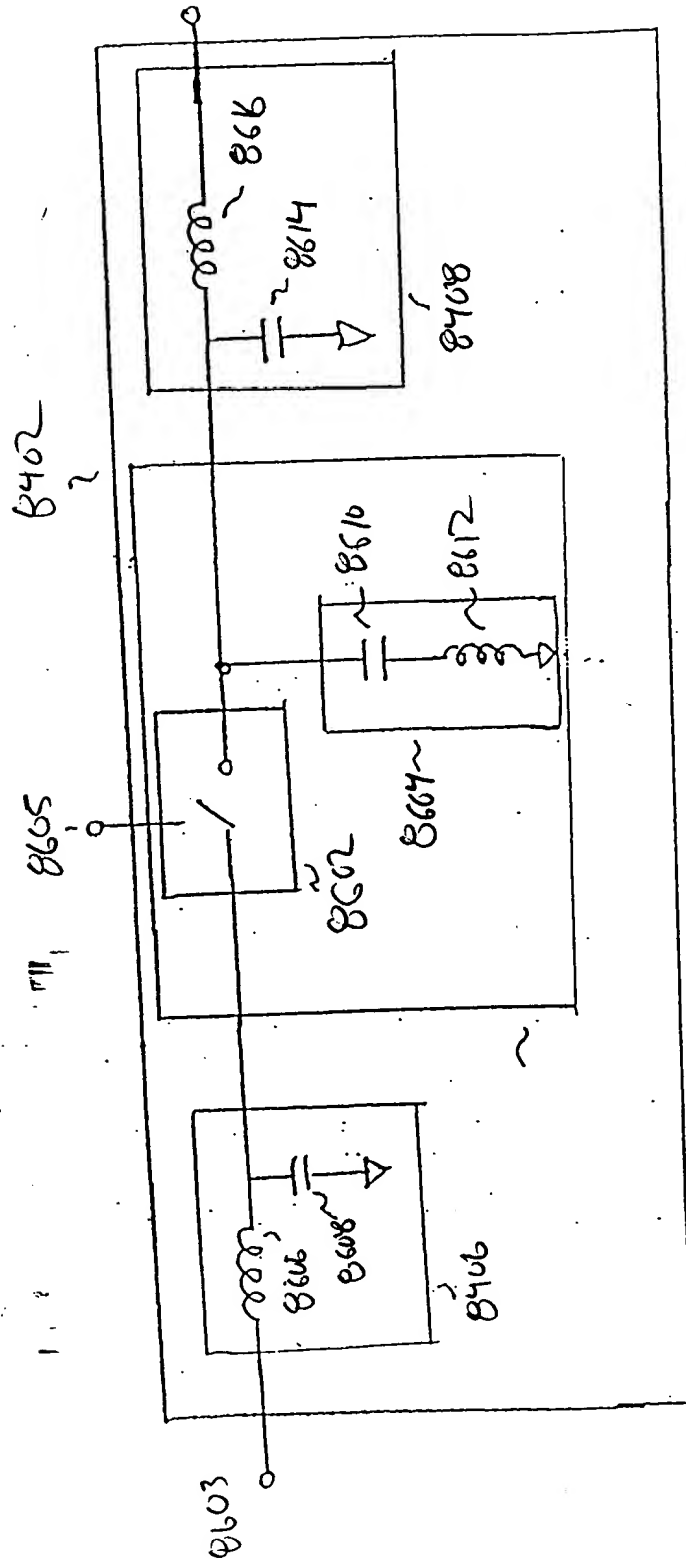


Fig 86 - Alasing Module

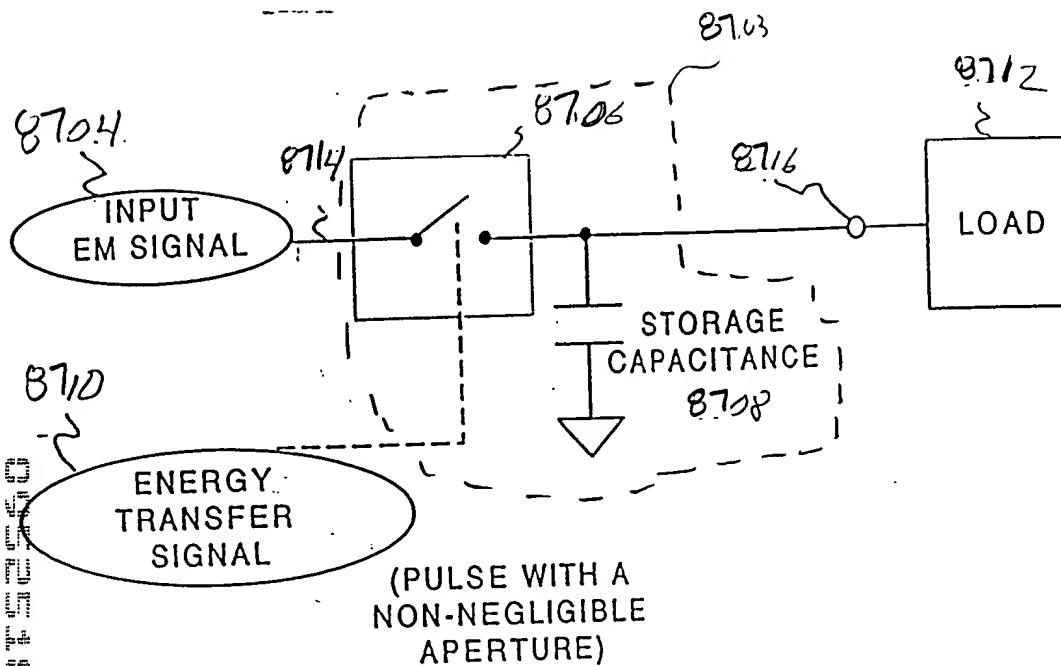


FIG 87A

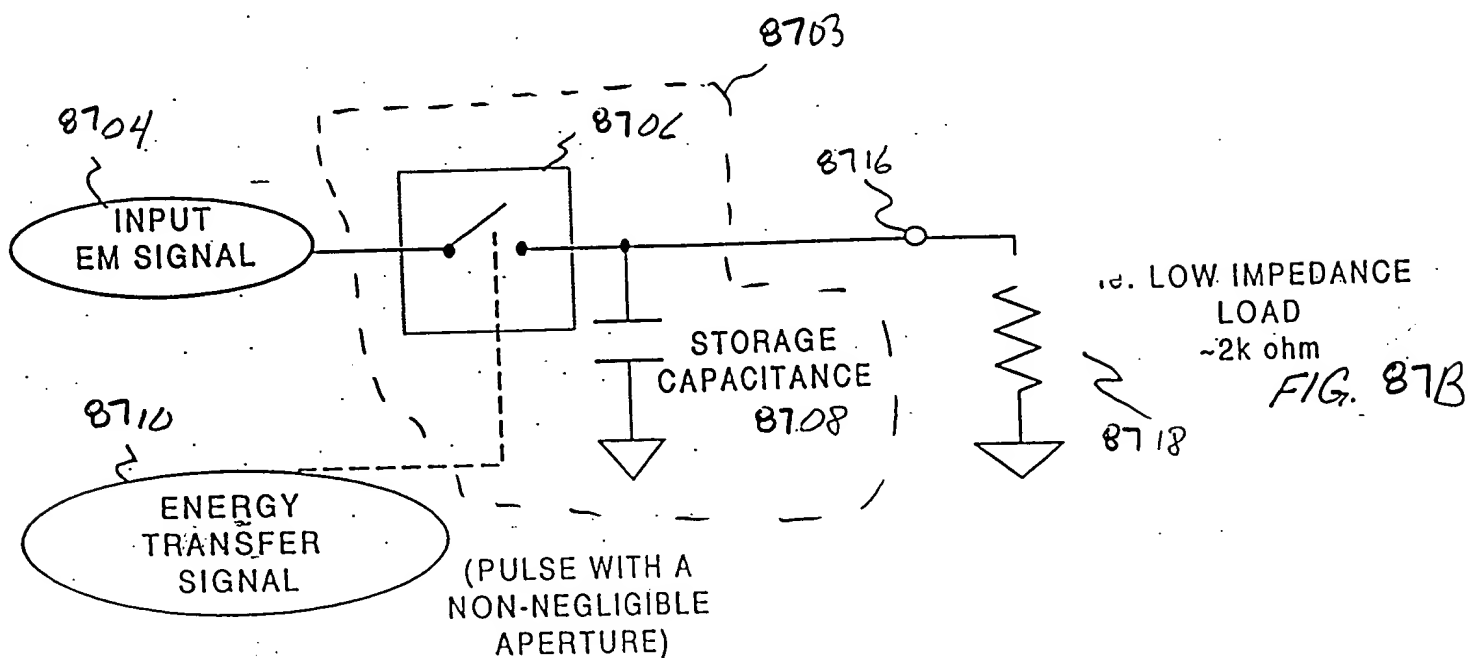


FIG. 87B

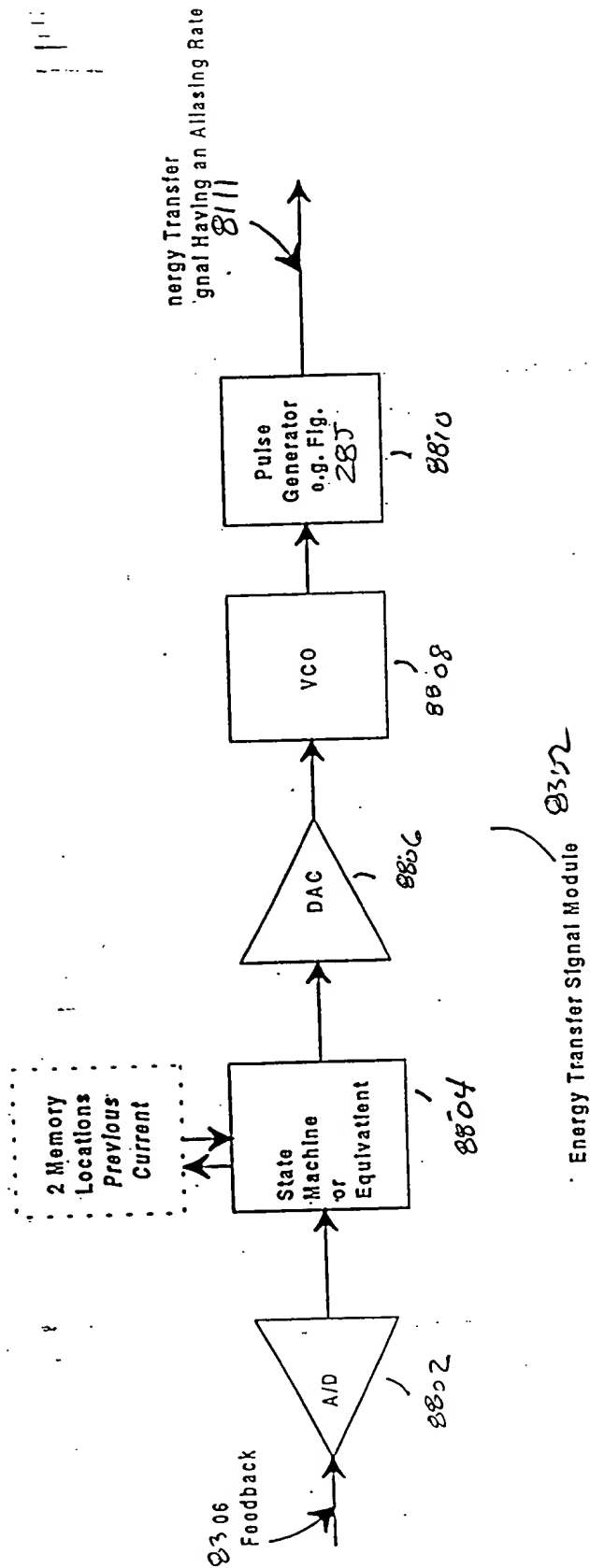
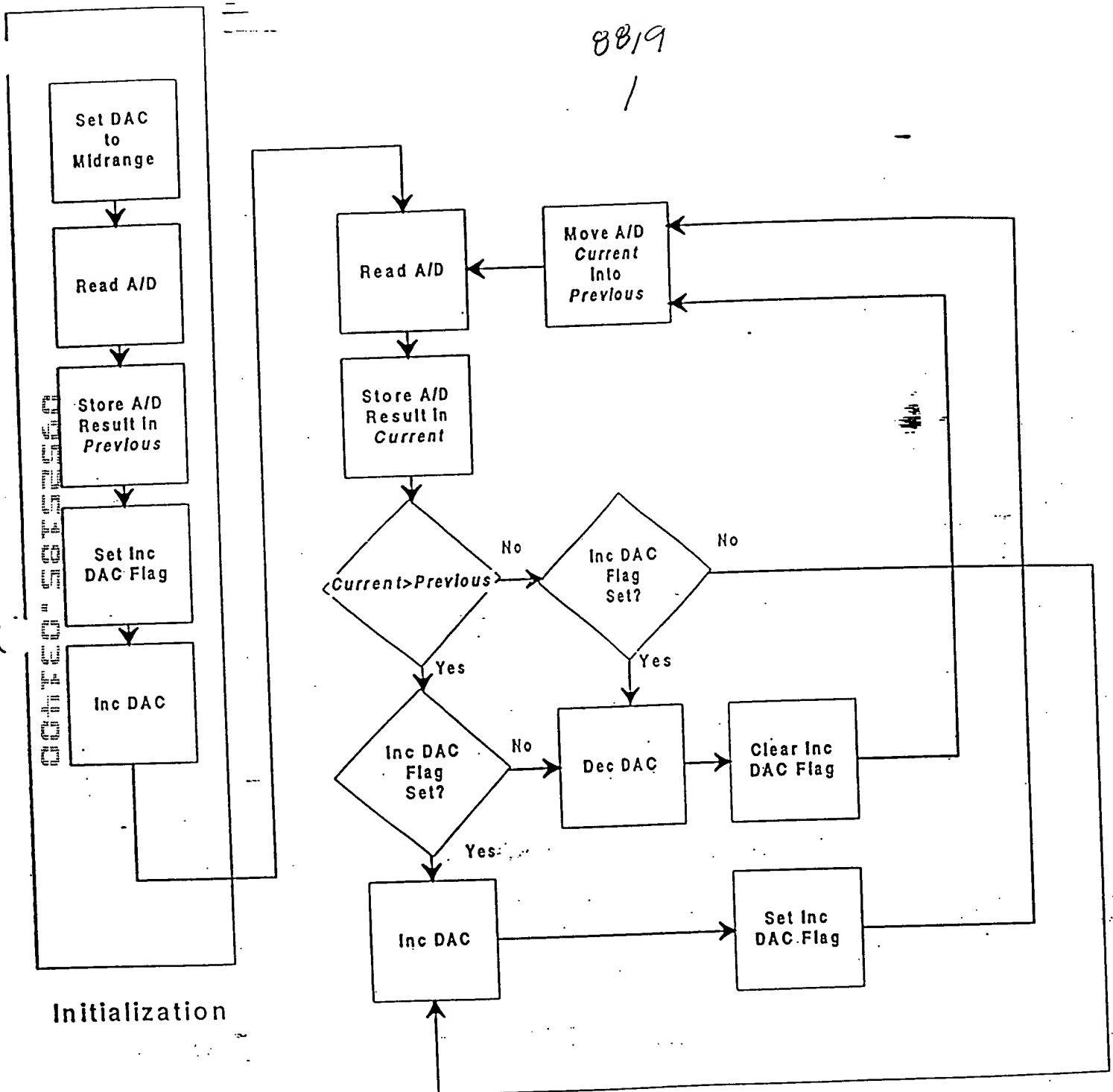


FIG. 88A

88/9

1

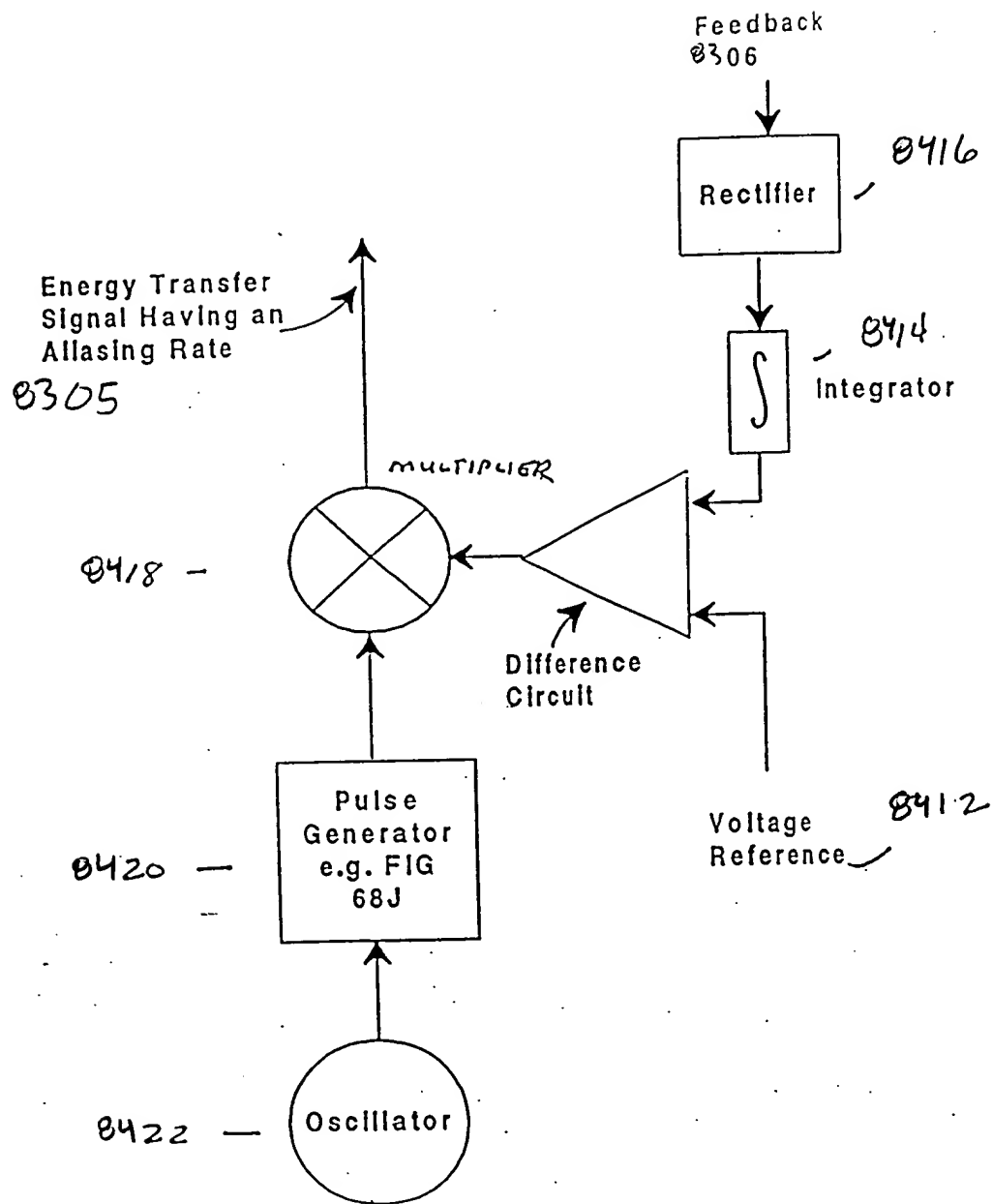


Initialization

State Machine Flowchart

FIG. 88B

S



Energy Transfer Signal Module 8302

FIG. 88C

DATE: 08/05/00

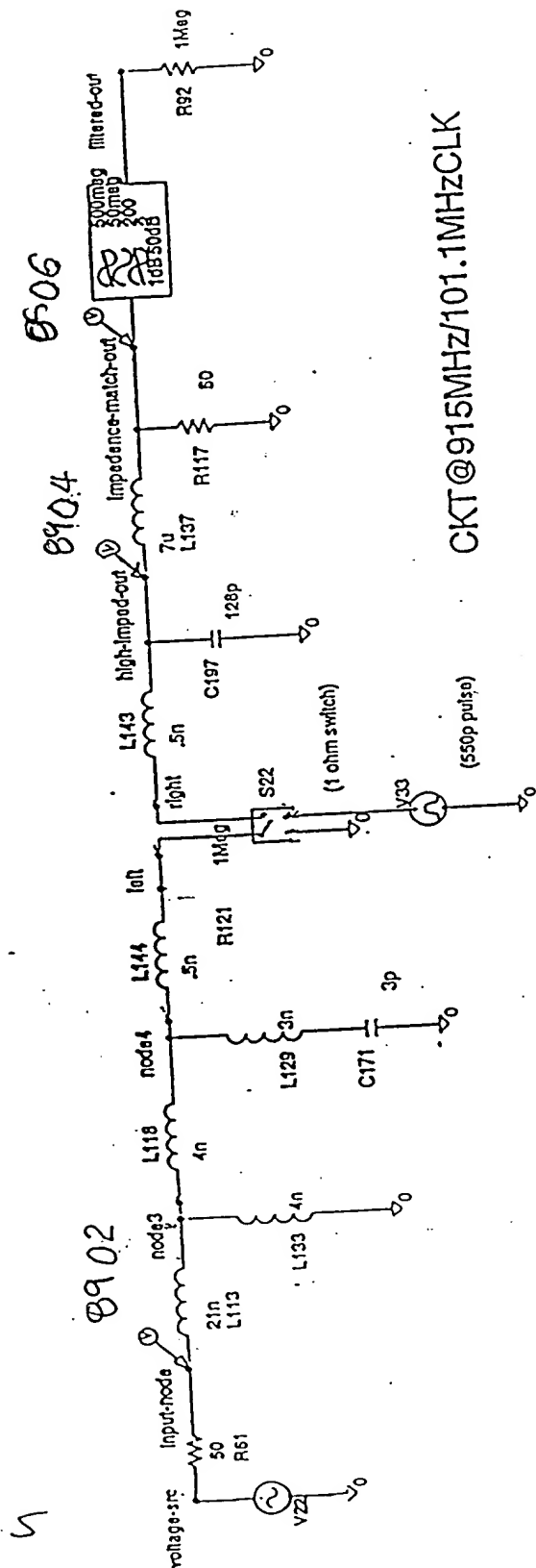


Fig. 89

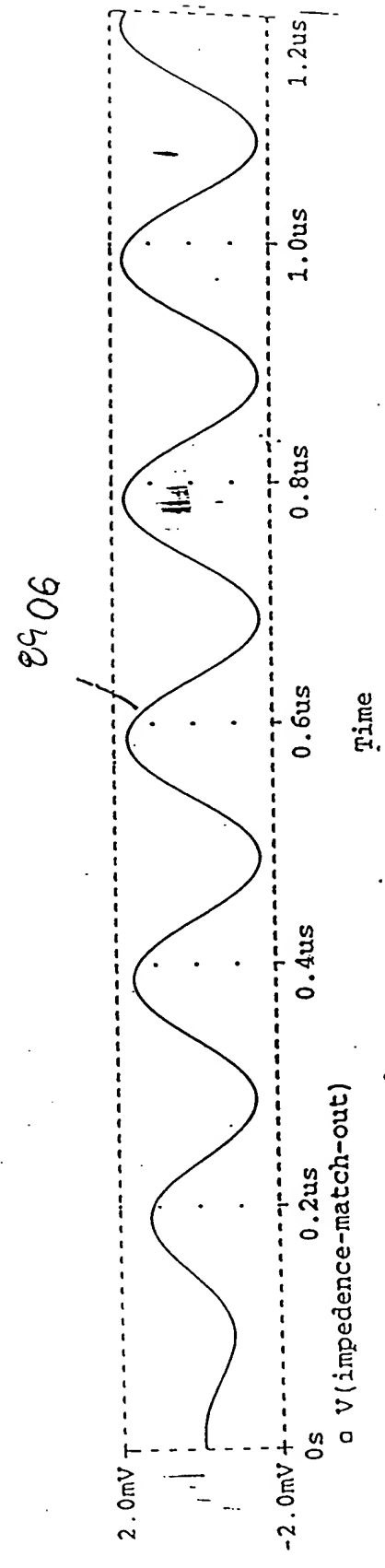
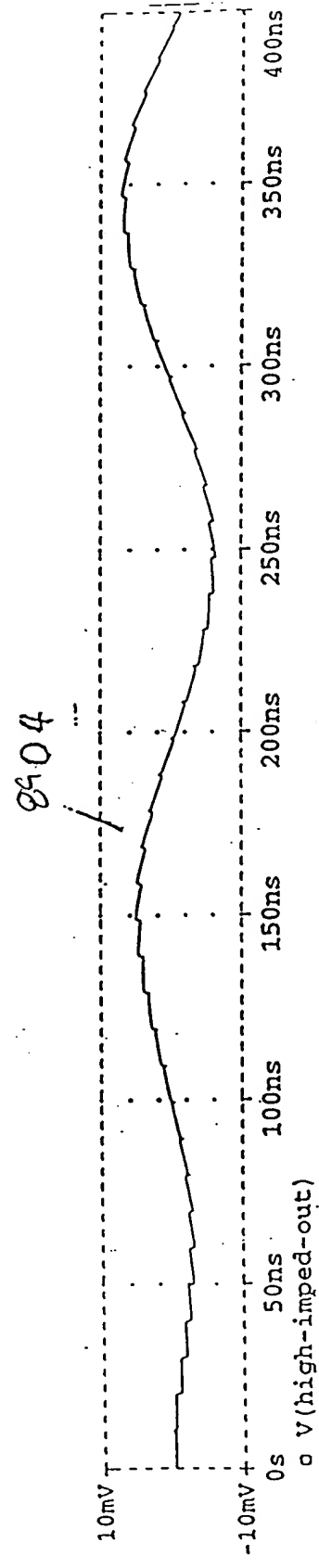
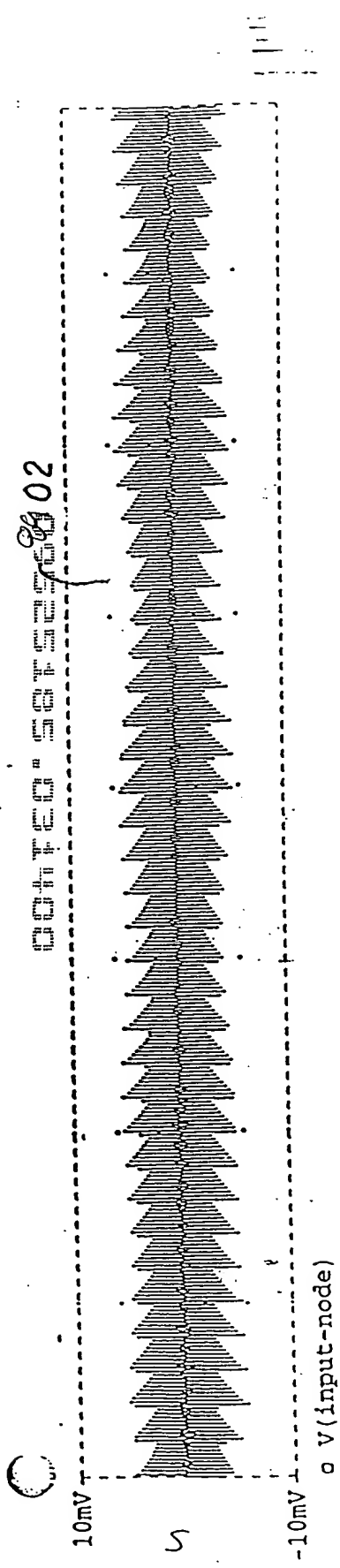


Fig. 90

2/16

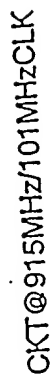


Fig 11

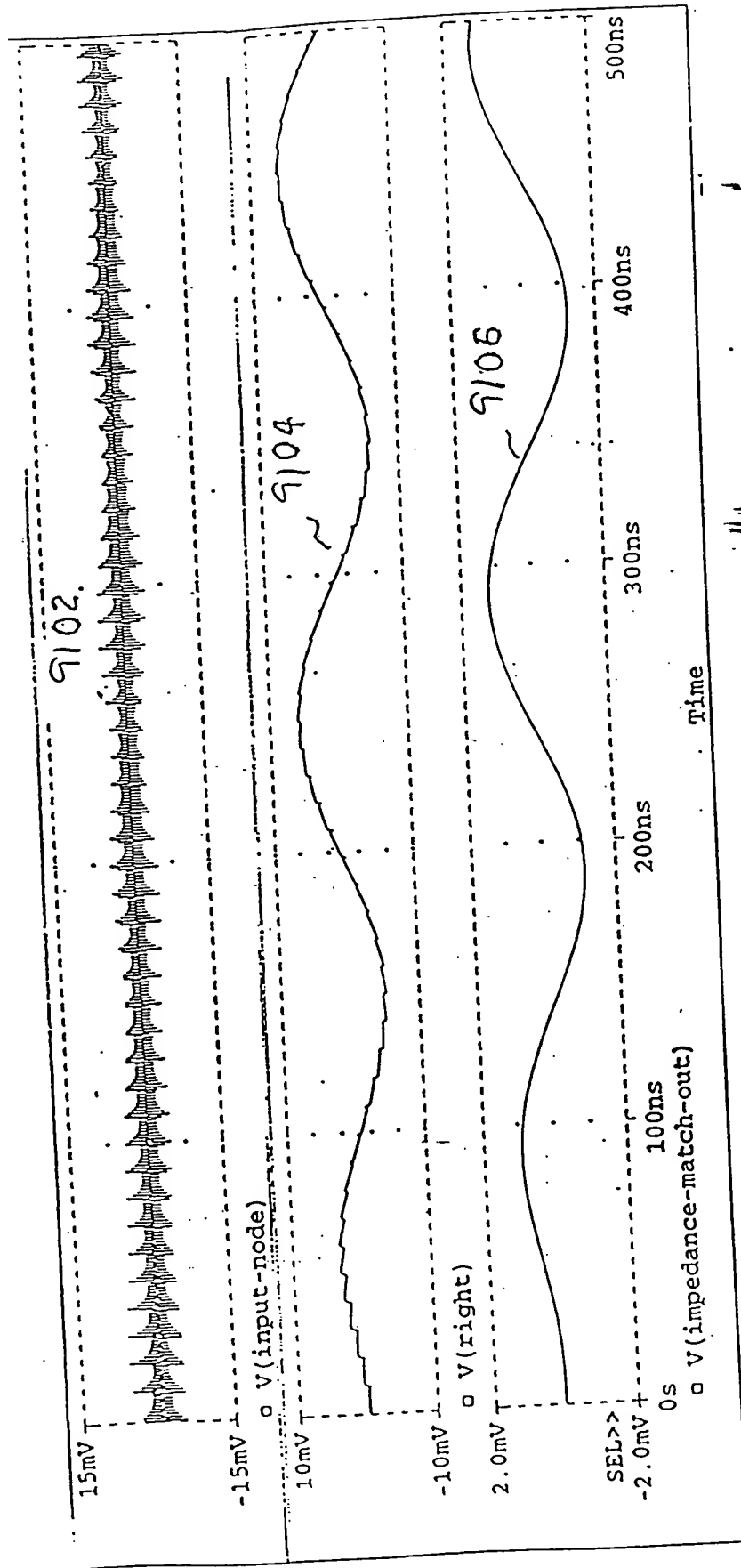


Fig 92

3



Fig. 93

001160 58152500

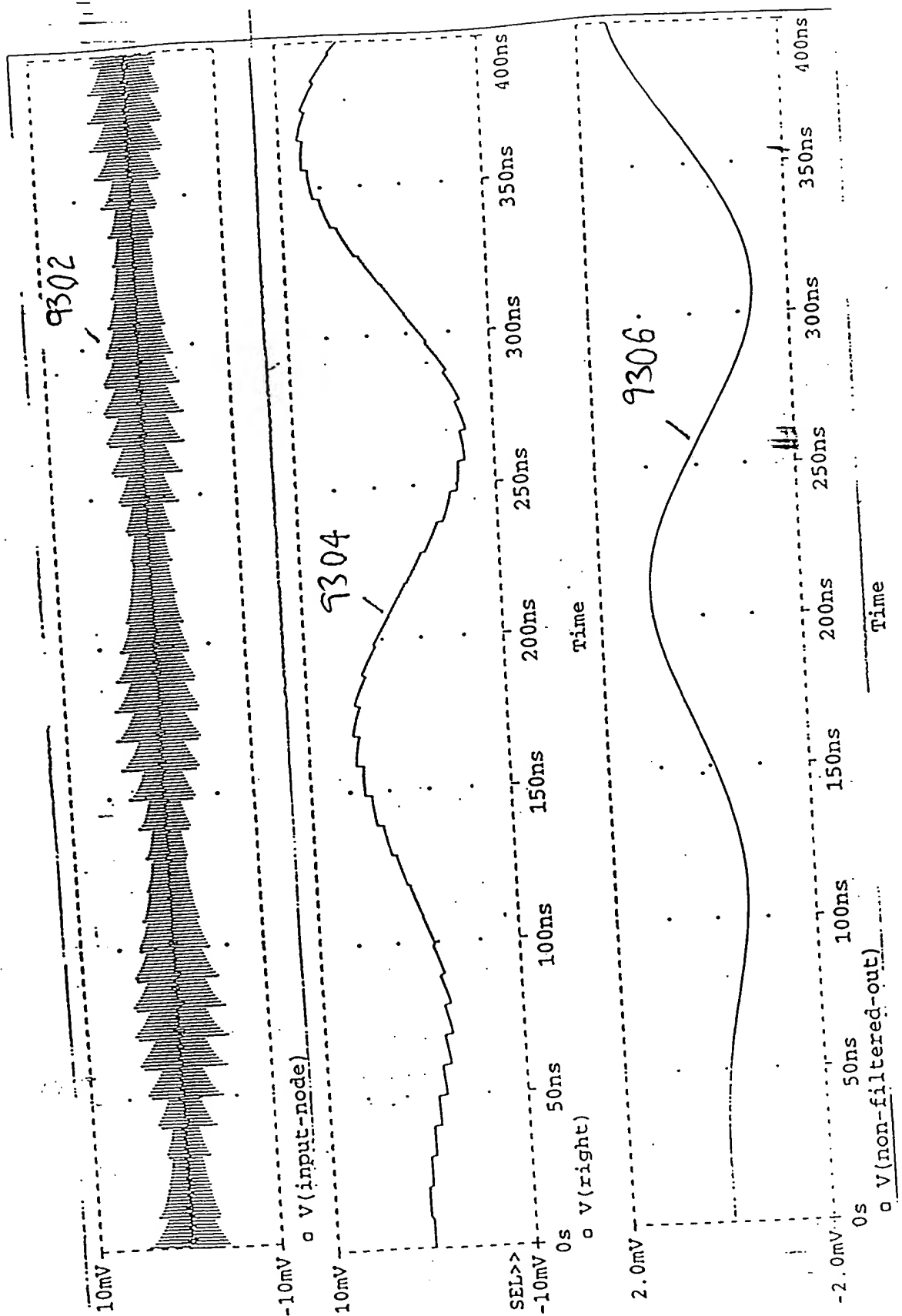
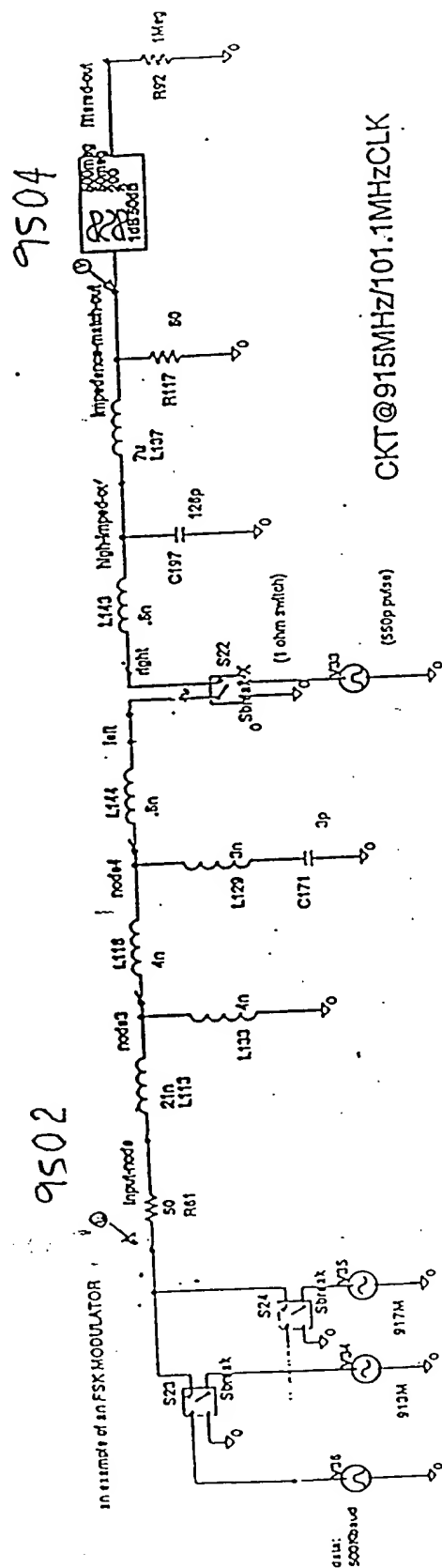


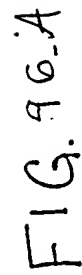
Fig. 94

1. The first part of the document is a list of names and their corresponding addresses. The names are listed in a column on the left, and the addresses are listed in a column on the right. The names are: John Doe, Jane Smith, and Bob Johnson. The addresses are: 123 Main St, 456 Elm St, and 789 Oak St.



CKT@915MHz/101.1MHzCLK

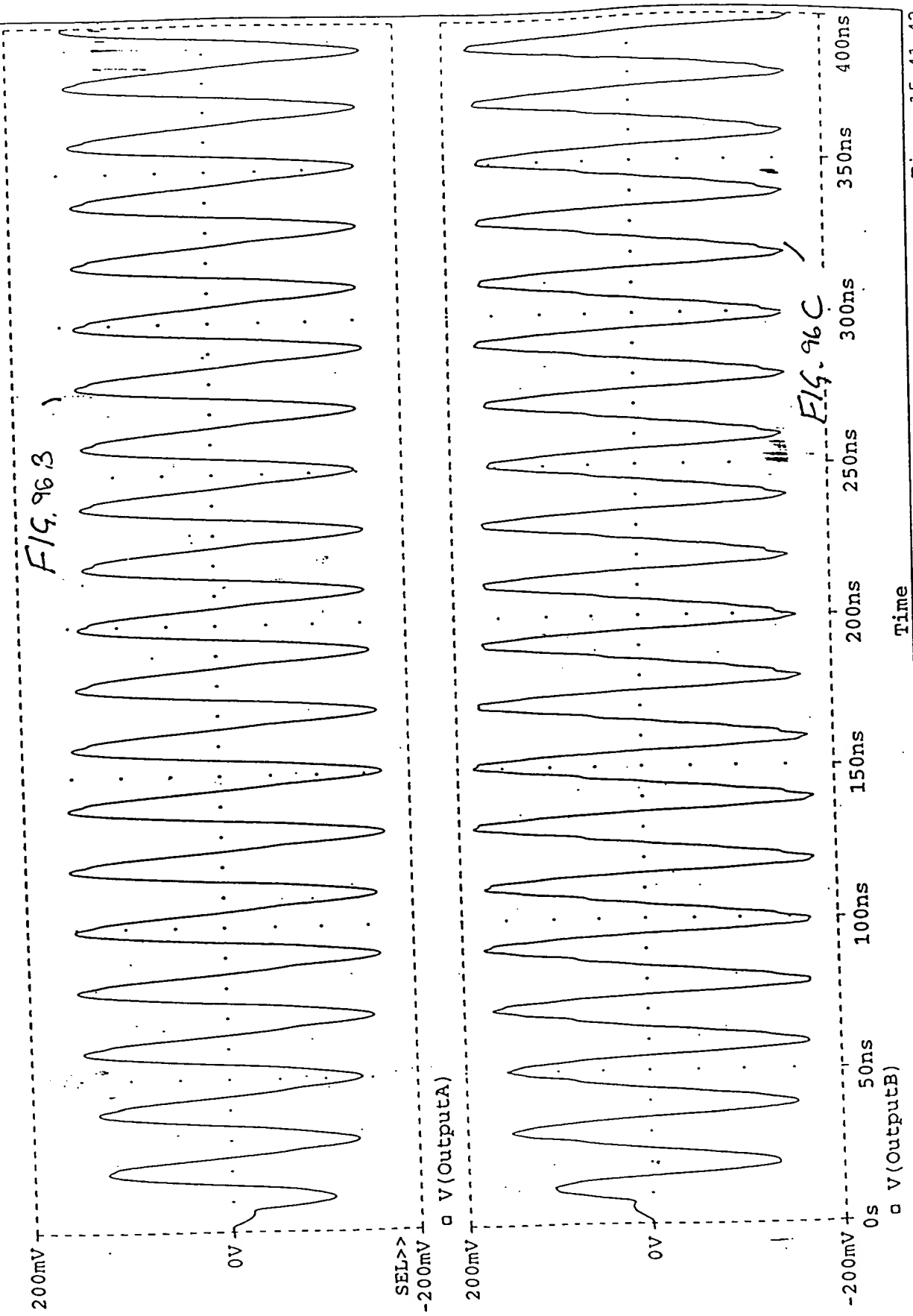
Fig. 15



Date/Time run: 10/14/98 15:37:54

(A) pat1.dat

5



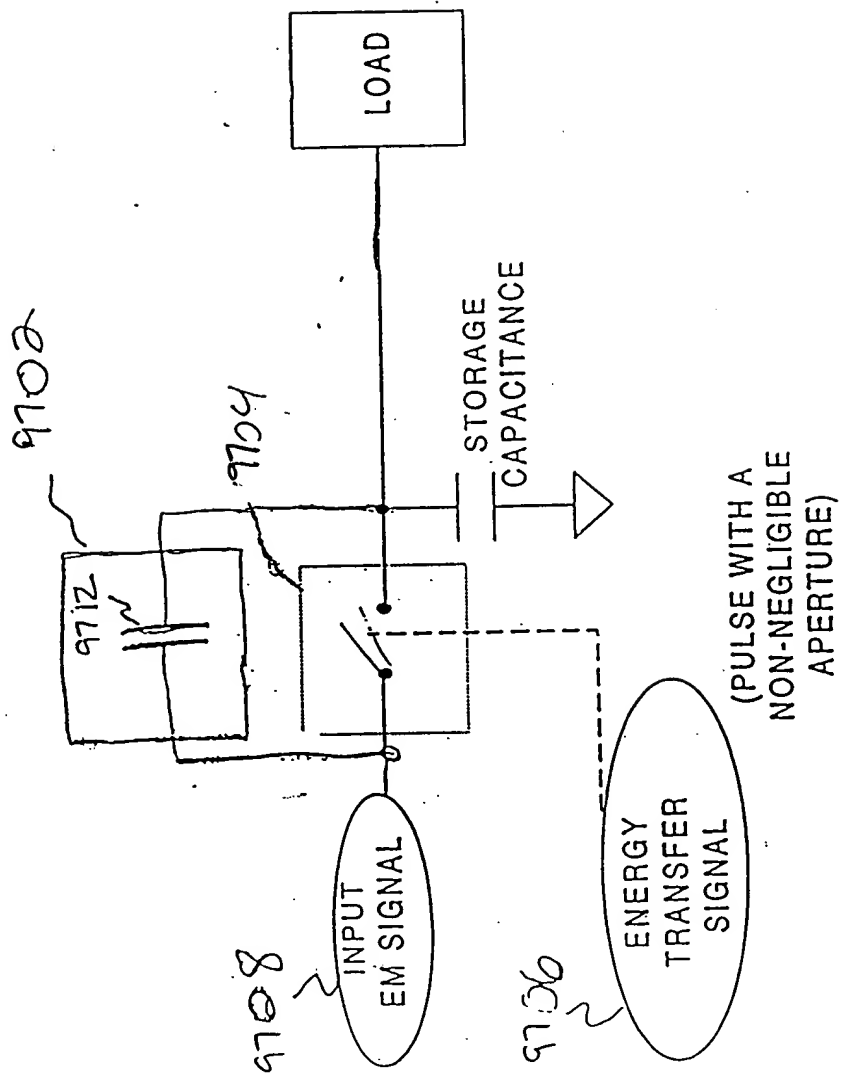


Fig. 97

9002

BYPASS CAPACITOR
(WIDENING THE
APPARENT APERTURE)

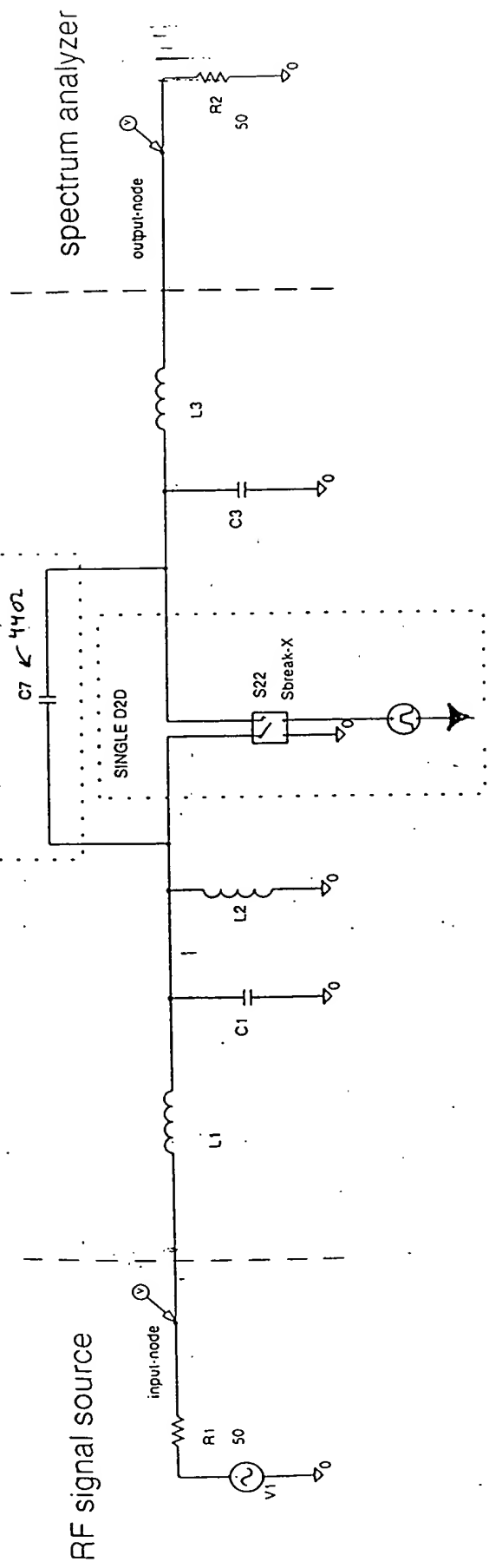


FIG. 98

9912

9910

9910 9911 9912 9913 9914 9915 9916 9917 9918 9919 9920 9921 9922 9923 9924 9925 9926 9927 9928 9929 9930 9931 9932 9933 9934 9935 9936 9937 9938 9939 9940 9941 9942 9943 9944 9945 9946 9947 9948 9949 9950 9951 9952 9953 9954 9955 9956 9957 9958 9959 9960 9961 9962 9963 9964 9965 9966 9967 9968 9969 9970 9971 9972 9973 9974 9975 9976 9977 9978 9979 9980 9981 9982 9983 9984 9985 9986 9987 9988 9989 9990 9991 9992 9993 9994 9995 9996 9997 9998 9999

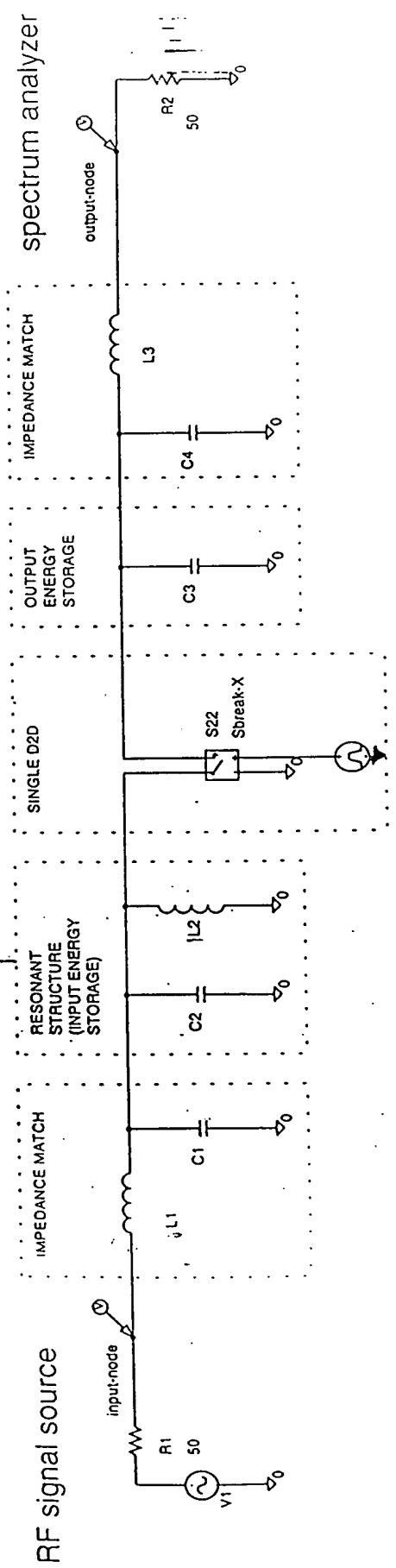


FIG. 99



100BASE-T4 100BASE-T2 100BASE-FX 100BASE-ES 100BASE-SX 100BASE-RX 100BASE-PLX 100BASE-EX 100BASE-EX2 100BASE-EX3 100BASE-EX4 100BASE-EX5 100BASE-EX6 100BASE-EX7 100BASE-EX8 100BASE-EX9 100BASE-EX10 100BASE-EX11 100BASE-EX12 100BASE-EX13 100BASE-EX14 100BASE-EX15 100BASE-EX16 100BASE-EX17 100BASE-EX18 100BASE-EX19 100BASE-EX20 100BASE-EX21 100BASE-EX22 100BASE-EX23 100BASE-EX24 100BASE-EX25 100BASE-EX26 100BASE-EX27 100BASE-EX28 100BASE-EX29 100BASE-EX30 100BASE-EX31 100BASE-EX32 100BASE-EX33 100BASE-EX34 100BASE-EX35 100BASE-EX36 100BASE-EX37 100BASE-EX38 100BASE-EX39 100BASE-EX40 100BASE-EX41 100BASE-EX42 100BASE-EX43 100BASE-EX44 100BASE-EX45 100BASE-EX46 100BASE-EX47 100BASE-EX48 100BASE-EX49 100BASE-EX50 100BASE-EX51 100BASE-EX52 100BASE-EX53 100BASE-EX54 100BASE-EX55 100BASE-EX56 100BASE-EX57 100BASE-EX58 100BASE-EX59 100BASE-EX60 100BASE-EX61 100BASE-EX62 100BASE-EX63 100BASE-EX64 100BASE-EX65 100BASE-EX66 100BASE-EX67 100BASE-EX68 100BASE-EX69 100BASE-EX70 100BASE-EX71 100BASE-EX72 100BASE-EX73 100BASE-EX74 100BASE-EX75 100BASE-EX76 100BASE-EX77 100BASE-EX78 100BASE-EX79 100BASE-EX80 100BASE-EX81 100BASE-EX82 100BASE-EX83 100BASE-EX84 100BASE-EX85 100BASE-EX86 100BASE-EX87 100BASE-EX88 100BASE-EX89 100BASE-EX90 100BASE-EX91 100BASE-EX92 100BASE-EX93 100BASE-EX94 100BASE-EX95 100BASE-EX96 100BASE-EX97 100BASE-EX98 100BASE-EX99 100BASE-EX100

10002 ~

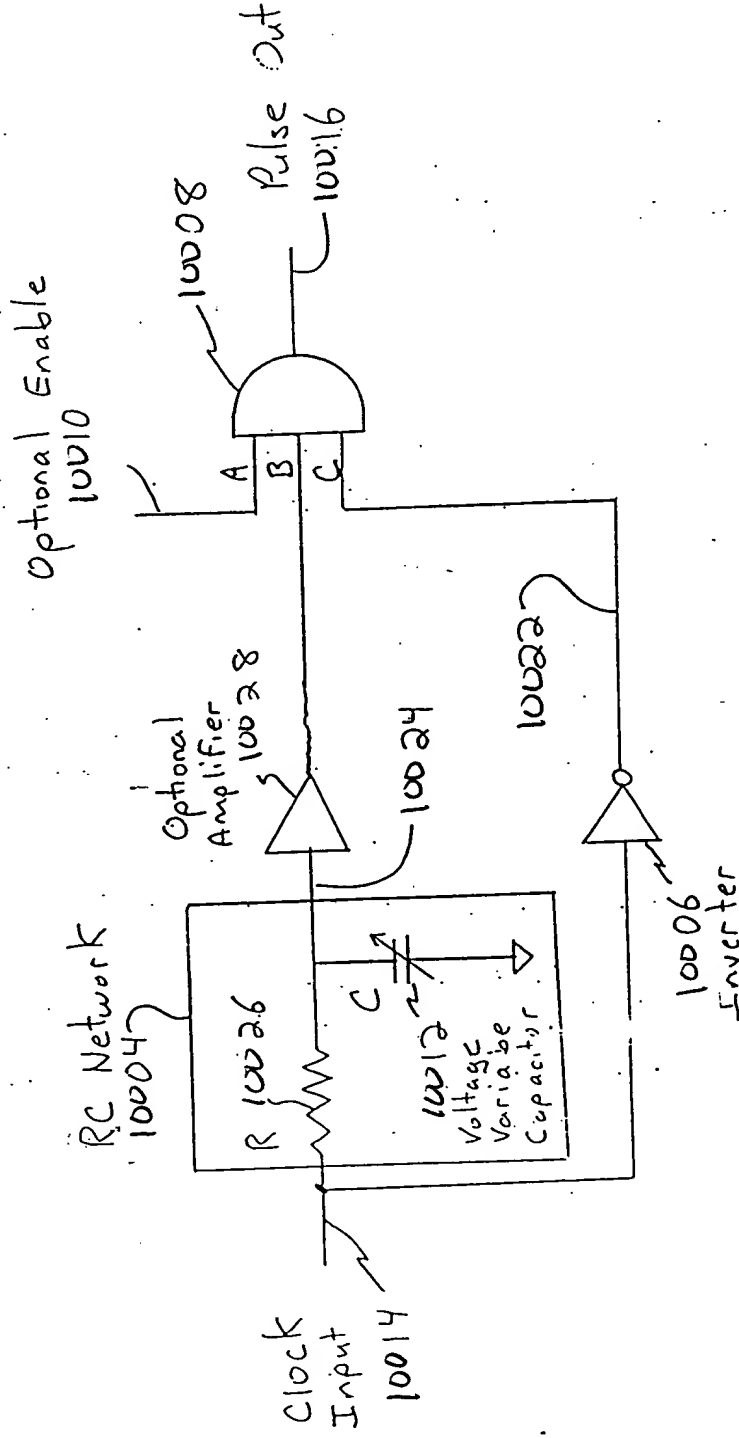
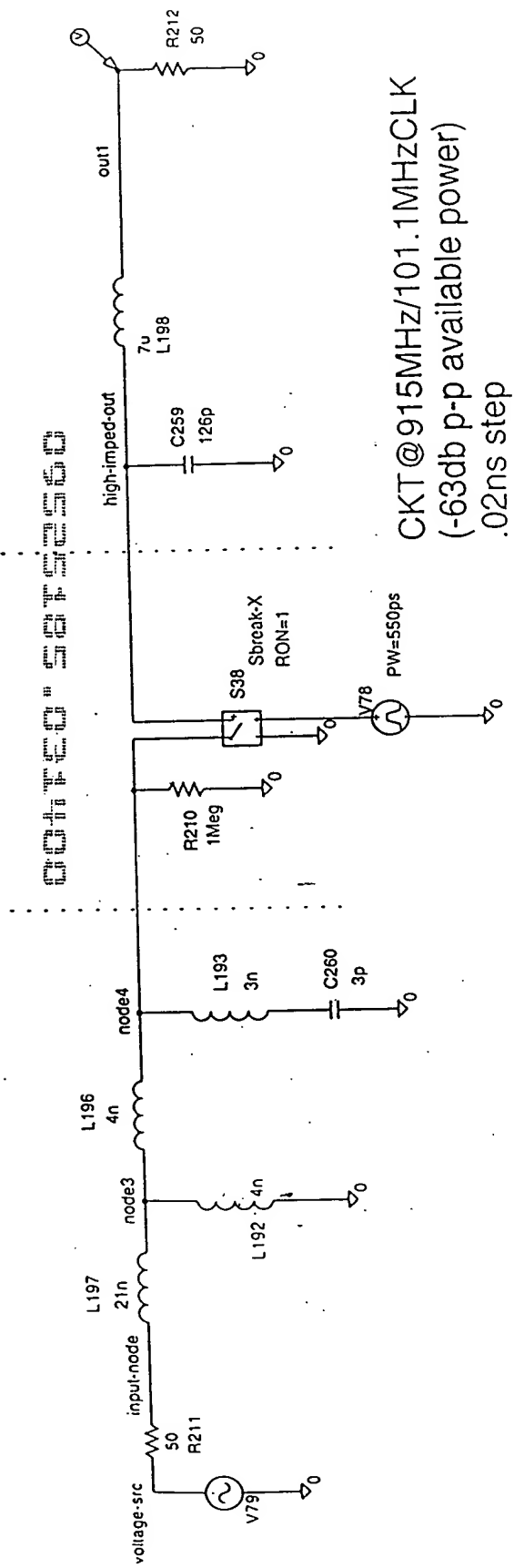


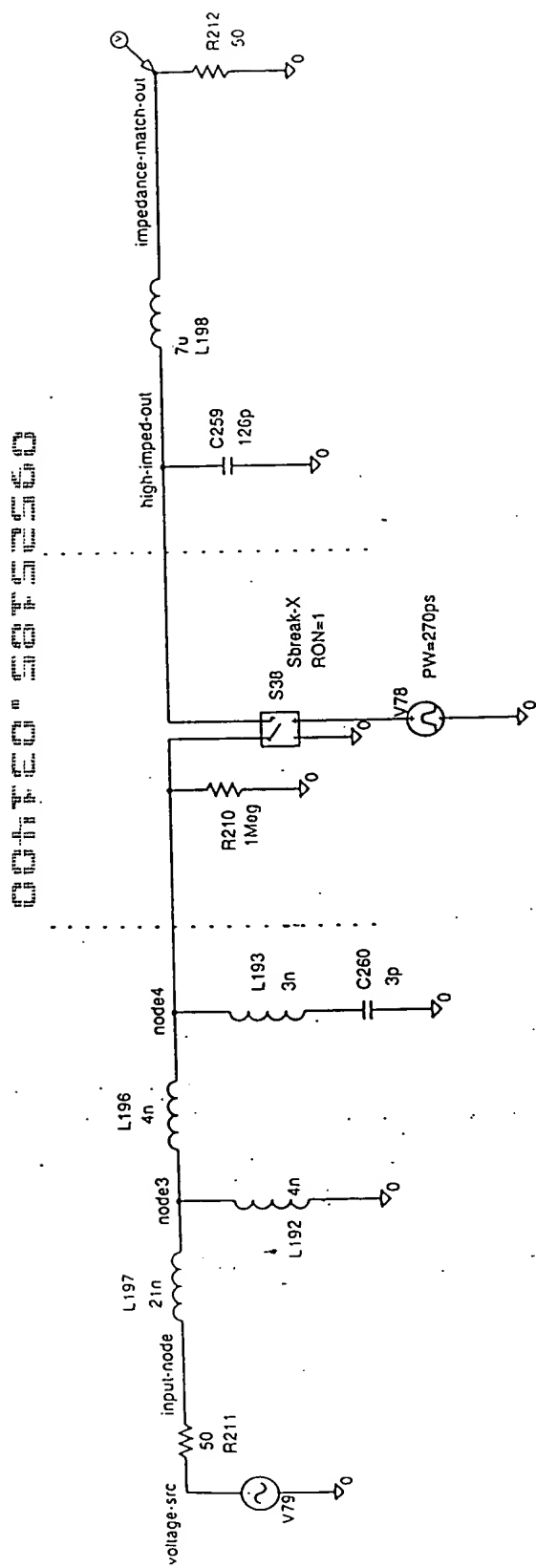
FIG 100A



CKT@915MHz/101.1MHzCLK
 (-63db p-p available power)
 .02ns step

single-series-switch-915M-5M-hieff.sch

FIG. '101

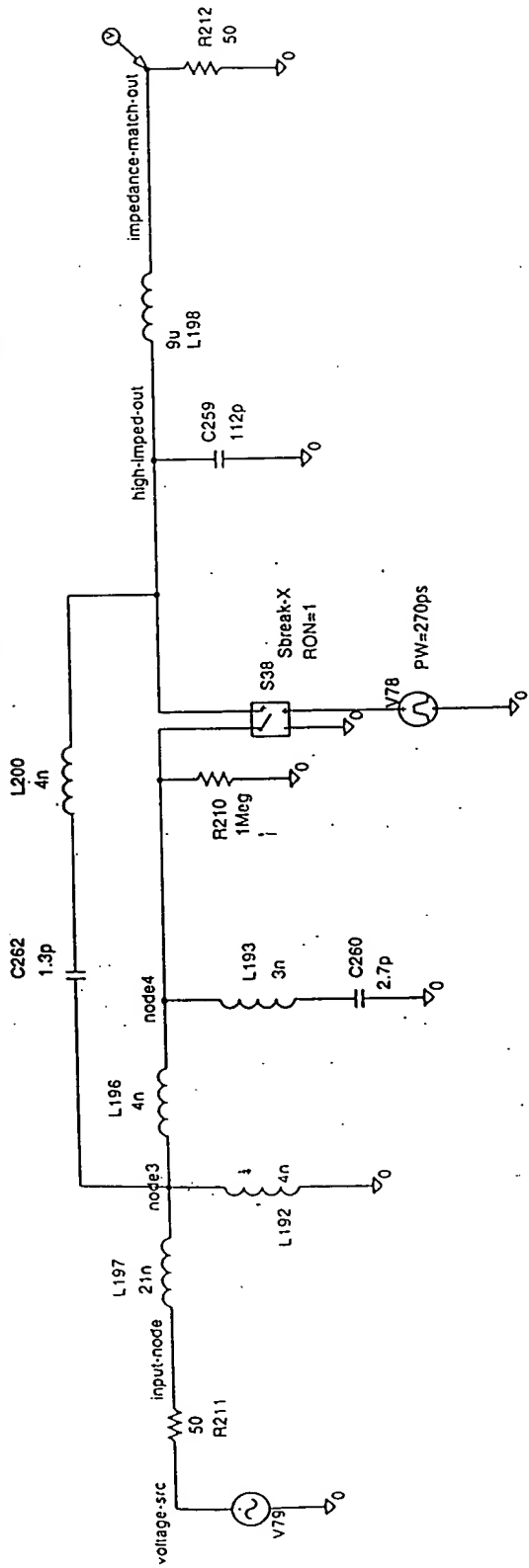


CKT@915MHz/101.1MHzCLK
(-63db p-p available power)
.02ns step

single-series-switch-smaperture915M-5M-hieff.sch

Fig. 162

037160 5073550

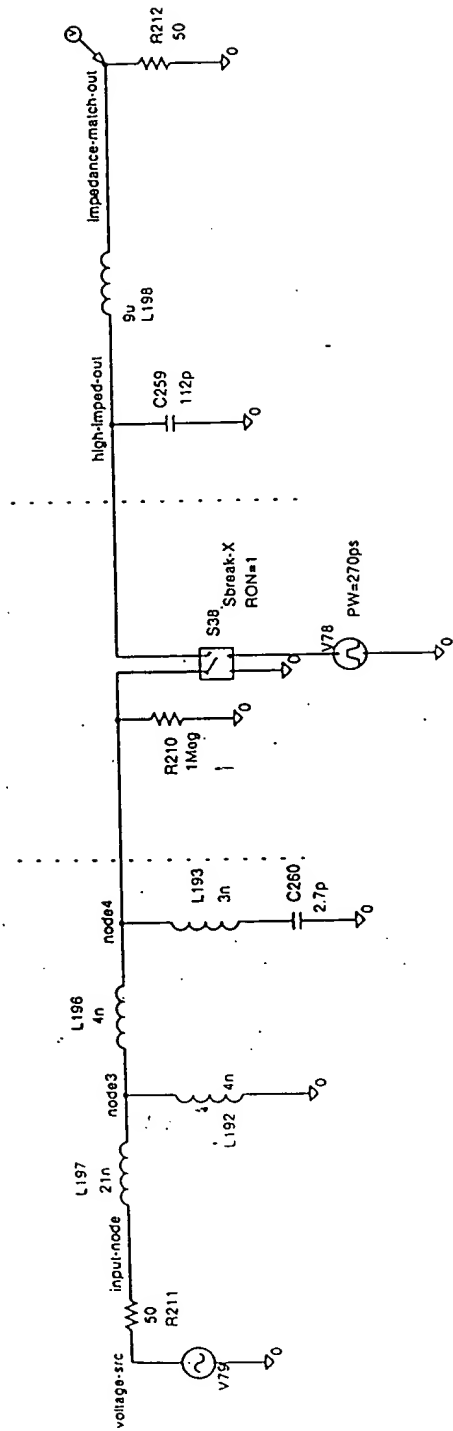


CKT@915MHz/101.1MHzCLK
 (-63db p-p available power)
 .02ns step

single-series-switch-bypass-915M-5M-hieff.sch

FIG. 103

3047E0 33739560



CKT @915MHz/101.1MHzCLK
(-63db p-p available power)
.02ns step

single-series-switch-wobypass-915M-5M-hieff.sch

FIG. 104

(E) single-series-switch-915M-5M-hieff.dat

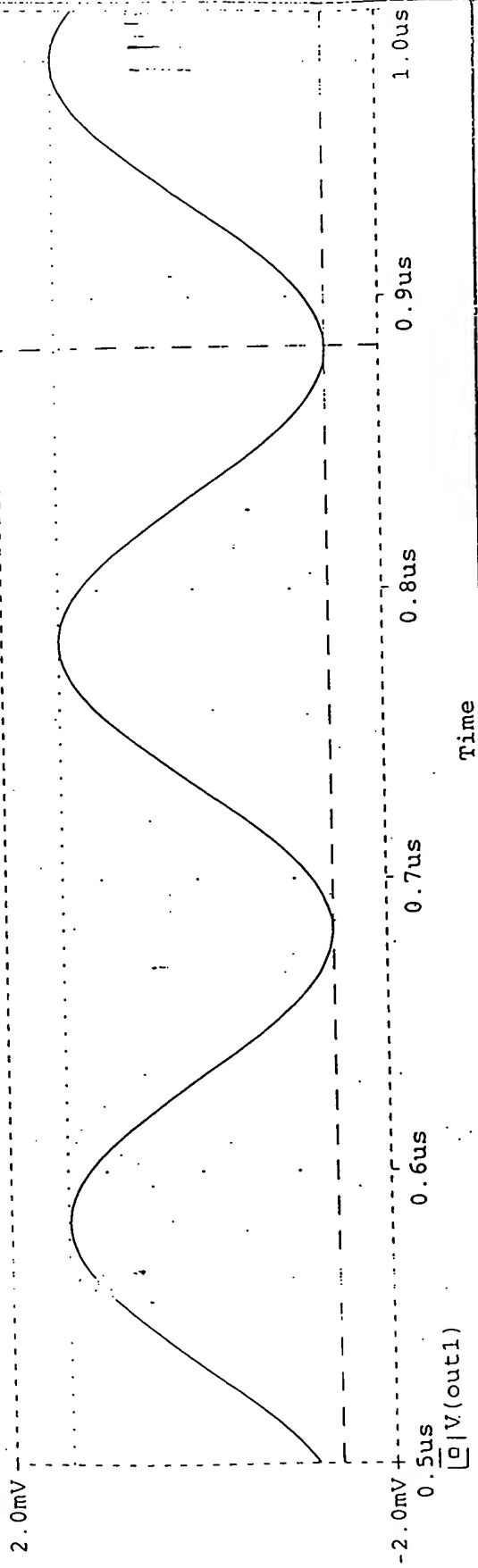


FIG. 105A

(F) single-series-switch-smapture915M-5M-hieff.dat

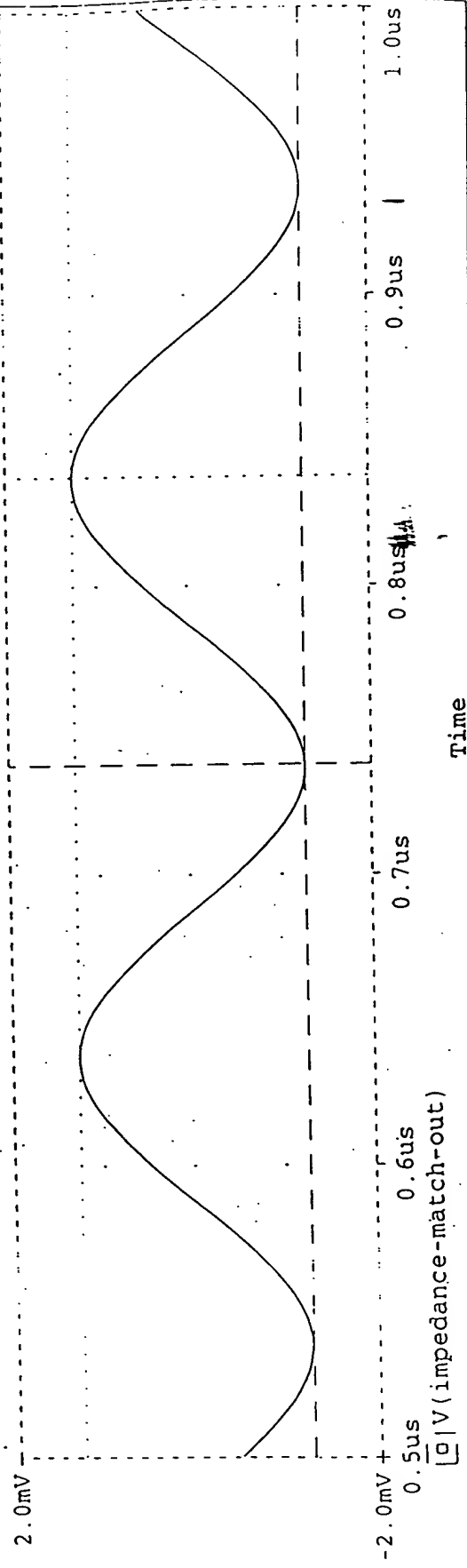


FIG 105B

E1: (981.86n, 1.404m) E2: (883.04n, -1.402m) DIFF(E): (98.82n, 2.806m)
F1: (837.43n, 1.253m) F2: (738.01n, -1.252m) DIFF(F): (99.42n, 2.505m)

(A) single-series-switch-bypass-915M-5M-hieff.dat

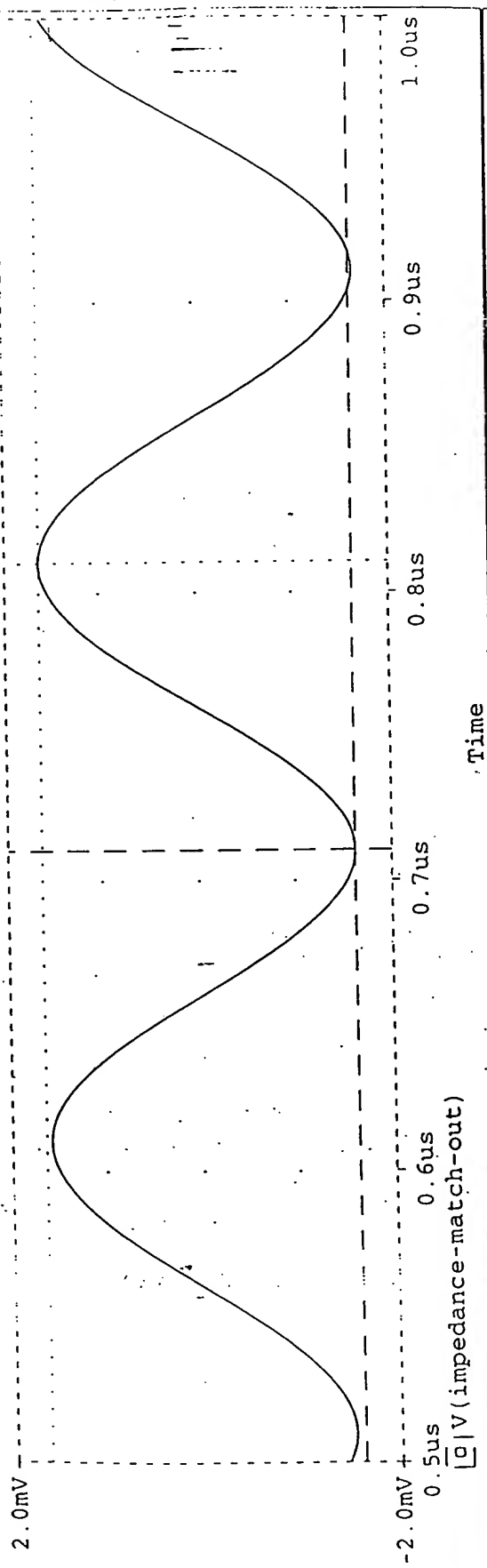


FIG. 106A

(B) single-series-switch-wobypass-915M-5M-hieff.dat

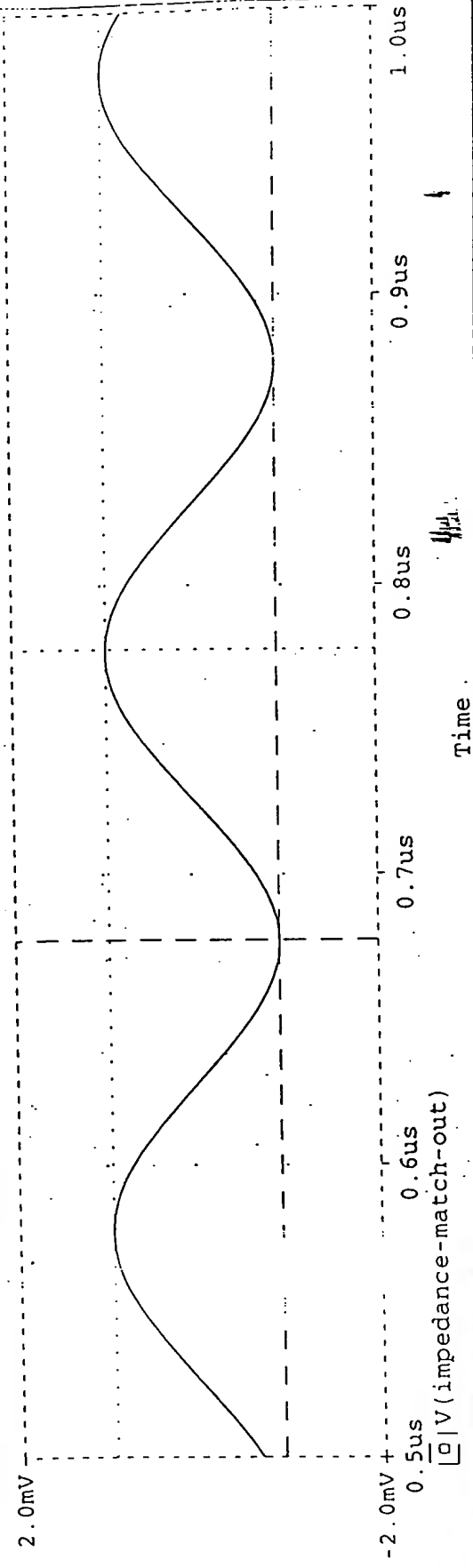
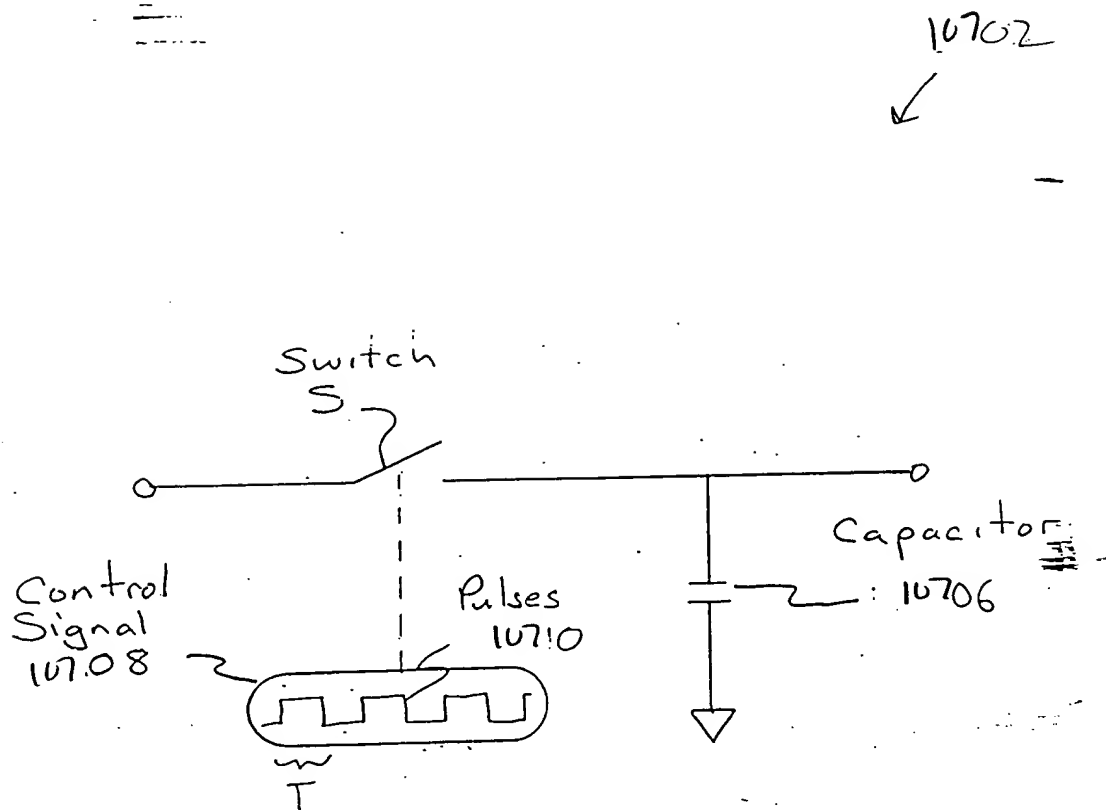


FIG. 106B

A1: (810.53n, 1.642m) A2: (710.52n, -1.621m) DIFF (A): (100.01n, 3.263m)
B1: (777.78n, 942.32u) B2: (677.18n, -942.51u) DIFF (B): (100.60n, 1.885m)



F I G. 107, A

$$q = C \cdot V \quad \text{EQ. 10}$$

$$V = A \cdot \sin(t) \quad \text{EQ. 11}$$

$$q(t) = C \cdot A \cdot \sin(t) \quad \text{EQ. 12}$$

$$\Delta q(t) = C \cdot A \cdot \sin(t) - C \cdot A \cdot \sin(t - T) \quad \text{EQ. 13}$$

$$\Delta q(t) = C \cdot A \cdot (\sin(t) - \sin(t - T)) \quad \text{EQ. 14}$$

$$\sin(\alpha) - \sin(\beta) = 2 \cdot \sin\left(\frac{\alpha - \beta}{2}\right) \cdot \cos\left(\frac{\alpha + \beta}{2}\right) \quad \text{EQ. 15}$$

$$\Delta q(t) = 2 \cdot C \cdot A \cdot \sin\left[\frac{t - (t - T)}{2}\right] \cdot \cos\left[\frac{t + (t - T)}{2}\right] \quad \text{EQ. 16}$$

$$\Delta q(t) = 2 \cdot C \cdot A \cdot \sin\left[\frac{1}{2} \cdot T\right] \cdot \cos\left[t - \frac{1}{2} \cdot T\right] \quad \text{EQ. 17}$$

$$q(t) = \int C \cdot A \cdot (\sin(t) - \sin(t - T)) dt \quad \text{EQ. 18}$$

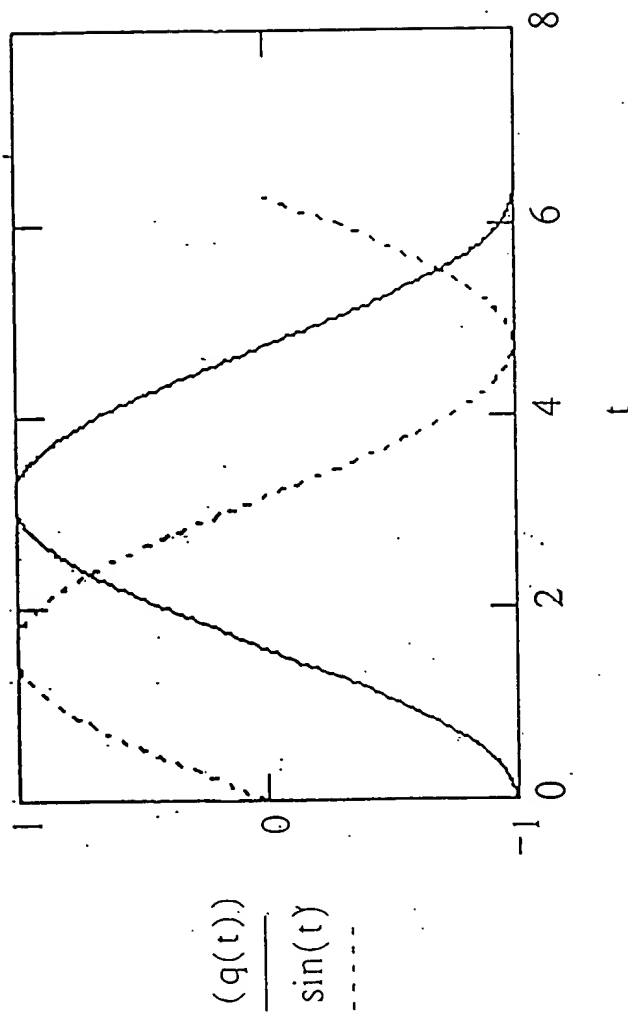
$$q(t) = -\cos(t) \cdot C \cdot A + \cos(t - T) \cdot C \cdot A \quad \text{EQ. 19}$$

$$q(t) = C \cdot A \cdot (\cos(t - T) - \cos(t)) \quad \text{EQ. 20}$$

FIG. 107B

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C=1; A=.5, T=9

FIG. 107.C

For Graph 2: $C=1$, $A=.5$, $T=\pi/10$:

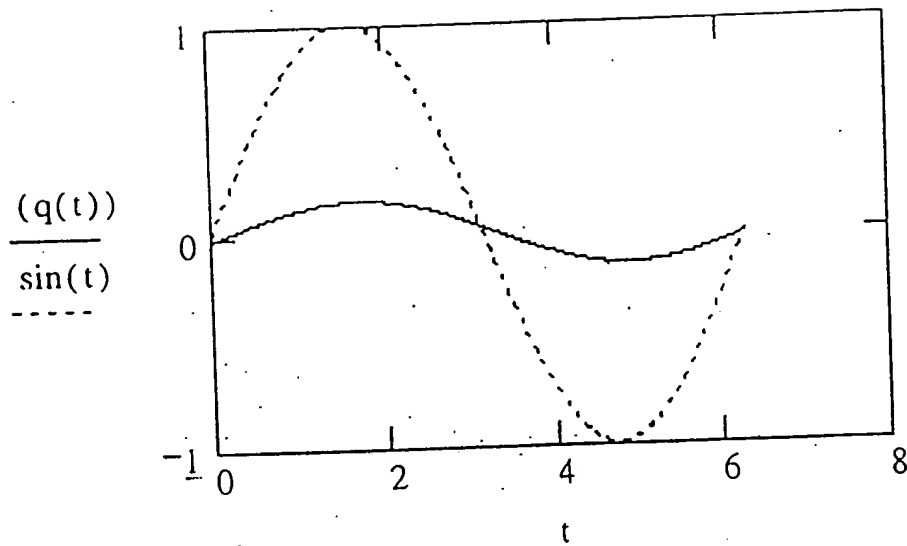


FIG. 10, D

Power - Charge Relationship

$$q = C \cdot V \quad \text{EQ. 21}$$

$$V = \frac{q}{C} \quad \text{EQ. 22}$$

$$V = \frac{J}{C} \quad \text{EQ. 23}$$

$$J = \frac{q^2}{C} \quad \text{EQ. 24}$$

$$P = \frac{J}{S} \quad \text{EQ. 25}$$

$$P = \frac{q^2}{C \cdot S} \quad \text{EQ. 26}$$

FIG. 107, E

Insertion Loss

Insertion loss in dB is expressed by:

$$IL_{dB} = 10 \cdot \log \left[\frac{P_{in}}{P_{out}} \right]$$

or

$$IL_{dB} = 10 \cdot \log \left[\frac{\left[\frac{V_{in}^2}{R_{in}} \right]}{\left[\frac{V_{out}^2}{R_{out}} \right]} \right]$$

FIG. 107, F